INTERNAL REPORTS IN SIMULATION, OPTIMIZATION AND CONTROL

No. SOC-271

FAULT ISOLATION IN LINEAR ANALOG CIRCUITS USING THE L, NORM

J.W. Bandler, R.M. Biernacki, A.E. Salama and J.A. Starzyk

September 1981

FACULTY OF ENGINEERING MCMASTER UNIVERSITY HAMILTON, ONTARIO, CANADA



No. SOC-271

FAULT ISOLATION IN LINEAR ANALOG CIRCUITS USING THE L $_{\rm 1}$ NORM

J.W. Bandler, R.M. Biernacki, A.E. Salama and J.A. Starzyk

September 1981

FAULT ISOLATION IN LINEAR ANALOG CIRCUITS USING THE $\rm L_1$ NORM

J.W. Bandler, Fellow, IEEE, R.M. Biernacki, A.E. Salama, Student Member, IEEE, and J.A. Starzyk

Abstract

This paper deals with fault isolation in linear analog circuits under an insufficient number of independent voltage measurements. The L_1 norm is utilized in isolating the most likely faulty elements. Earlier work is extended by allowing measurements to be taken for more than a single excitation. An iterative procedure is followed in which we utilize linear programming as a powerful tool in solving the problem. Convergence is fast and the results of circuit examples subject to practical tolerances on components are much sharper than in our earlier method.

This work was supported by the Natural Sciences and Engineering Research Council of Canada under Grant A7239.

J.W. Bandler, A.E. Salama and J.A. Starzyk are with the Group on Simulation, Optimization and Control and the Department of Electrical and Computer Engineering, McMaster University, Hamilton, Canada L8S 4L7.

R.M. Biernacki is with, and J.A. Starzyk is on leave from, the Institute of Electronics Fundamentals, Technical University of Warsaw, Warsaw, Poland.

I. INTRODUCTION

With a <u>sufficient</u> number of independent measurements fault isolation is carried out by identifying all network components. There is usually a trade-off between the computational effort required and the number of accessible network nodes. When all the network nodes are accessible the components are identified by solving a system of linear equations [1,2]. If some of the network nodes are inaccessible a system of nonlinear equations [3] is usually solved for identifying the network components. Normally, not all the nodes are accessible and this will make the problem of fault isolation by identifying all network components computationally expensive and time consuming.

For a <u>limited</u> number of independent voltage measurements the identification of all network components is impossible and fault isolation is carried out by identifying the faulty components under the assumption that the changes in all good components are within their tolerances. Two approaches have been followed [4]. The first approach utilizes the measurements in constructing algebraic equations which are invariant on the changes in the faulty elements. The second approach utilizes an estimation criterion in isolating the most likely faulty set [4-6].

In this paper, we examine the use of the least-one-objective function in estimating the faulty elements. Mainly, we extend our previous study to the problem when more than one excitation is performed. This relaxation permits us to use more measurements from the available accessible nodes. An iterative procedure has to be followed, however, to find the faulty elements. In every iteration a linear programming problem is solved for the changes in the network components.

-2-

In what follows we present the formulation of the problem, the description of the iterative procedure and some examples to demonstrate the efficiency of this iterative approximate fault isolation method.

II. APPROXIMATE FAULT ISOLATION

Given a number of independent voltage measurements which is less than the total number of the components of the faulty network, it is required to estimate the most likely faulty elements. If we permit all the network components to change the aforementioned problem will have an infinite number of feasible solutions. Practically, the faulty components are very few in number and the relative change in their values is significantly larger than in the nonfaulty ones.

The change in a network component value from its nominal can be represented by either a current source or a voltage source which is in parallel or in series, with the component, respectively [4]. For a single excitation, the change in the performed voltage measurements from their nominal values due to the change in the values of the network components is given by

$$\Delta \underline{V}^{m} = \underline{H}^{m} \underline{e} , \qquad (1)$$

where $e \stackrel{\Delta}{=} [e_1 \ e_2 \ \dots \ e_n]^T$ is the vector of the assumed sources that represent the changes in the n components from nominal and \underline{H}^m is the hybrid matrix that relates the changes in the measured voltages to the assumed sources \underline{e} . \underline{H}^m is computed directly using the nominal component values.

Every component of e has the form

$$e_{i} = (j\omega)^{\alpha 1} \Delta p_{i} x_{i} = e_{1i} + j e_{2i},$$
 (2)

where $\alpha i = 0$, 1 or -1 depending on the component type, Δp_i is the change in component value and x_i is the actual controlling voltage or current for the component depending on whether e_i is a current or a voltage source, respectively. Normally, x_i is unknown unless a direct measurement of its value has been carried out.

Equation (1) is an underdetermined system of linear equations in the parameters e. We construct the following linear optimization problem which can be easily solved using linear programming to find the most likely faulty components.

$$\begin{array}{ll} & n \\ \text{Minimize } \Sigma & (|e_{1i}| + |e_{2i}|) \\ & e_{1}, e_{2} \\ & i=1 \end{array}$$
(3a)

subject to

$$\Delta \underline{y}^{m} = \underline{H}^{m} (\underline{e}_{1} + j \underline{e}_{2}) , \qquad (3b)$$

where e_1 and e_2 are the real and imaginary part of the vector e_2 , respectively.

The result of the optimization problem will provide us with the vector \underline{e} . Then the network is simulated using the original excitation and the vector \underline{e} to find x_i , i = 1, ..., n. The change in every network component can be easily computed using equation (2). Comparing the change in every component with its allowed tolerance the faulty ones are readily isolated.

The formulation of the optimization problem in (3) does not destroy the linear relation in (1) and it does not create more unknowns since the unknown network response x_i is included in the error parameter (2). But, at the same time, for nonresistive networks it defines two error parameters for every component which cannot be easily correlated. Also the formulation is restricted to a single excitation at any time since any new excitation will add 2n new error parameters.

Practically, we would like to have the system (1) less underdetermined. This is achieved if we increase the number of measurements by exciting the network at more than one external node and/or using excitations at different frequencies. We then consider Δp_i as the error parameter.

In order to preserve the linear system (1), the network responses x_i , i = 1, ..., n, have to be known. Hence, they are assumed and an iterative procedure updates their values and at the same time computes the changes in the component values.

For more than a single excitation, we consider the following optimization problem instead of (3).

$$\begin{array}{c|c} \text{Minimize} & \sum_{i=1}^{n} |\Delta p_i / p_i^0| \\ \Delta p_i = 1 \end{array}$$
(4a)

subject to

$$\begin{bmatrix} \Delta \underline{V}^{m1} \\ \Delta \underline{V}^{m2} \\ \vdots \\ \vdots \\ \vdots \\ \Delta \underline{V}^{mk} \end{bmatrix} = \begin{bmatrix} \underline{H}^{m1} \underline{X}^{m1} \underline{\Omega}^{m1} \\ \underline{H}^{m2} \underline{X}^{m2} \underline{\Omega}^{m2} \\ \vdots \\ \vdots \\ \vdots \\ \underline{H}^{mk} \underline{X}^{mk} \underline{\Omega}^{mk} \end{bmatrix} \Delta \underline{p}, \qquad (4b)$$

where $\Delta p \stackrel{\Delta}{=} [\Delta p_1 \ \Delta p_2 \ \dots \ \Delta p_n]^T$ is the vector of the changes in components from nominal values, and for the *l*th excitation, $\Delta \underline{V}^{ml}$ is the change in the measured voltages from nominal, \underline{H}^{ml} is the hybrid matrix which relates the measured voltages to the assumed sources,

$$X_{n}^{m\ell} \stackrel{\Delta}{=} \operatorname{diag}[x_{1}^{m\ell}, x_{2}^{m\ell}, \dots, x_{n}^{m\ell}], \qquad (5)$$

where x_{i}^{ml} is the ith controlling branch voltage or current depending

whether the assumed source is a current or a voltage source, respectively, and

$$\mathcal{Q}^{m\ell} \stackrel{\Delta}{=} \operatorname{diag}[(j\omega_{m\ell})^{\alpha 1}, (j\omega_{m\ell})^{\alpha 2}, \dots, (j\omega_{m\ell})^{\alpha n}], \qquad (6)$$

defines the frequency dependency of the components. The normalization w.r.t. p_i^0 in (4a) is needed when the nominal values of the components are varied over a wide range.

Optimization problem (4) can be modified by adding the following inequality constraints

$$\Delta p_i > -p_i^x$$
, $i = 1, ..., n$, (7)

where p_i^x is the value of network element after being updated. Initially p_i^x is set to its nominal design value p_i^0 . These constraints are added to prevent any nonphysical solutions.

We utilize the measurements together with an updated model of the network to compute $\underline{H}^{m\ell}$ and $\underline{X}^{m\ell}$, $\ell = 1, ..., k$. The responses $x_i^{m\ell}$, $\ell = 1, ..., k$, i = 1, ..., n are functions of the nodal voltages. We update only the values of the internal node voltages since the external node voltages are known from measurements.

Algorithm

- <u>Step 1</u> Using the nominal component values construct the matrices required by (4).
- <u>Step 2</u> Solve the linear optimization problem (4) and update the network component values.
- <u>Step 3</u> If there is no appreciable change in the component values, stop.
- <u>Step 4</u> Recompute the matrices required by (4) using the updated network. Return to Step 2.

Example 1

Consider the resistive network shown in Fig. 1 with the nominal values of elements $G_i = 1$ and tolerances $\varepsilon_i = \pm 0.05$, i = 1, 2, ..., 20. All outside nodes are assumed to be accessible with node 12 taken as the ground node. Nodes 4, 5, 8 and 9 are assumed internal where no measurements can be performed.

Two faults are assumed in the network in elements G_2 and G_{18} . For Case 1, we applied the fault isolation method with a single excitation at node 1 and simulated the voltages at the nodes 1, 2, 3, 6, 7, 10 and 11. We solved the optimization problem (3) for the error parameters \underline{e} . Then, the changes in the component values were computed. For Case 2, we considered two excitations applied at nodes 3 and 6 sequentially and performed voltage simulations from all available external nodes. We constructed optimization problem (4), and applied 3 iterations of our iterative procedure. See Table I for the results.

Both methods have identified the actual faulty elements, but in Case 2 the estimated changes in the faulty elements approach their true values. Some of the changes in the nonfaulty components have slightly exceeded their allowed tolerances, but these changes are small so we can still consider them nonfaulty.

Example 2

Consider the one stage transistor amplifier in Fig. 2 with its equivalent circuit in Fig. 3. This example was originally considered by Chen and Saeks in [7]. The nominal component values together with the actual values are listed in Table II. Three faults are assumed in the network, namely, C_1 , r_{π} and g_m , with all other elements within their relative tolerances. We first applied optimization problem (3). A single current excitation is applied at node 1 of angular frequency 0.01 rad/sec. Voltage measurements are simulated at the assumed accessible nodes 1, 2, 4, 5 and 6. For optimization problem (4), we excited the network twice at node 1 using two different frequencies, namely, 0.01 rad/sec and 0.0075 rad/sec, and voltage measurements are obtained from the same accessible nodes. The results of both cases are given in Table II. Convergence in Case 2 occurred after 5 iterations. It is clear that the results of Case 2 are much sharper and they identify exactly the faulty elements.

IV. CONCLUSIONS

A method for approximate fault isolation is presented. We utilize the properties of the L_1 norm in isolating the most likely network components which have exhibited large changes in their values. This paper extends our earlier method [4] by allowing voltage measurements to be performed for more than one excitation of the faulty network. This obviously utilizes the available external nodes by obtaining more information about the network under test.

The formulation of the problem necessitates the application of an iterative procedure for its solution. A linear programming problem is solved in every iteration to provide us with the most likely changes in the network components. Linear programming is very efficient and from our experience with the proposed method the iterative procedure converges rapidly.

-8-

By adding more information about the network the problem becomes less underdetermined and the approximate fault isolation method is expected to provide sharper results. This is easily seen from the results of the examples considered in this paper.

Our approximate method can be used by itself for fault isolation, or we could verify the obtained results by constructing algebraic invariant equations corresponding to the detected faulty set as we did in [4].

REFERENCES

- [1] T.N. Trick, W. Mayeda and A.A. Sakla, "Calculation of parameter values from node voltage measurements", <u>IEEE Trans. Circuits and</u> <u>Systems</u>, vol. CAS-26, 1979, pp 466-474.
- [2] R.M. Biernacki and J.W. Bandler, "Postproduction parameter identification of analog circuits", <u>Proc. IEEE Int. Symp. Circuits</u> and Systems (Houston, TX, 1980), pp. 1082-1086.
- [3] N. Navid and A.N. Wilson, Jr., "A theory and an algorithm for analog circuit fault diagnosis", <u>IEEE Trans. Circuits and Systems</u>, vol. CAS-26, 1979, pp. 440-457.
- [4] J.W. Bandler, R.M. Biernacki and A.E. Salama, "A linear programming approach to fault location in analog circuits", <u>Proc.</u> <u>IEEE Int. Symp. Circuits and Systems</u> (Chicago, IL, 1981), pp. 256-261.
- [5] H.M. Merrill, "Failure diagnosis using quadratic programming", IEEE Trans. Reliability, vol. R-22, 1973, pp. 207-213.
- [6] M.N. Ransom and R. Saeks, "Fault isolation with insufficient measurements", <u>IEEE Trans. Circuit theory</u>, vol. CT-20, 1973, pp. 416-417.
- [7] H.S.M. Chen and R. Saeks, "A search algorithm for the solution of the multifrequency fault diagnosis equations", <u>IEEE Trans.</u> <u>Circuits and Systems</u>, vol. CAS-26, 1979, pp. 589-594.

TABLE	Ι
-------	---

RESULTS FOR EXAMPLE 1

			Percentage Deviation		
Element	Nom. Value	Act. Value	Actual	Case 1	Case 2
G ₁	1.0	0.98	-2.0	-4.15	- 2.20
G ₂	1.0	0.50	-50.0	-50.42	-47.86
G ₃	1.0	1.04	4.0	0.0	8.60
G ₄	1.0	0.97	-3.0	0.0	-2.46
G ₅	1.0	0.95	-5.0	0.0	-2.36
GG	1.0	0.99	-1.0	0.0	-0.14
G ₇	1.0	1.02	2.0	0.0	-2.49
G ['] 8	1.0	1.05	5.0	0.0	0.39
G ₉	1.0	1.02	2.0	0.0	0.84
G ₁₀	1.0	0.98	-2.0	0.0	-1.28
G ₁₁	1.0	1.04	4.0	0.0	0.0
G 12	1.0	1.01	1.0	-3.06	1.55
G ₁₃	1.0	0.99	-1.0	0.0	0.0
G 14	1.0	0.98	-2.0	0.0	2.47
G ₁₅	1.0	1.02	2.0	0.0	1.51
G 16	1.0	0.96	-4.0	-7.07	-6.69
G ₁₇	1.0	1.02	2.0	0.0	1.91
G 18	1.0	0.50	-50.0	-40.1	-46.90
G ₁₉	1.0	0.98	-2.0	-9.21	-3.38
G ₂₀	1.0	0.96	-4.0	-6.81	-6.14

	ΤA	BLE	II
--	----	-----	----

RESULTS FOR EXAMPLE 2

	Nom. Value	Act. Value	Percentage Deviation		
Element			Actual	Case 1	Case 2
с ₁	20.0	10.0	-50.0	-48.68	-50.0
R S	75.0	76.92	2.56	0.0	-3.04
rx	10.0	10.2	2.00	0.0	-2.06
r _π	40.0	66.67	66.66	-12.93	53.36
°π	15.0	14.0	-6.66	0.0	6.62
с µ	25.0	24.0	-4.00	-0.32	-4.12
g _m	10.0	5.0	-50.00	0.0*	-46.21
Rc	10.0	9.8	-2.00	-1.36	2.55
c ₂	20.0	19.0	-5.00	-0.65	-5.59
RL	20.0	20.6	3.0	1.43	-3.44
Re	30.0	29.4	-1.96	4.97	1.37
c	10.0	9.5	-5.00	0.0	-5.01
R 1	10.0	10.05	0.5	-1.43	-0.50

* A faulty element has not been detected

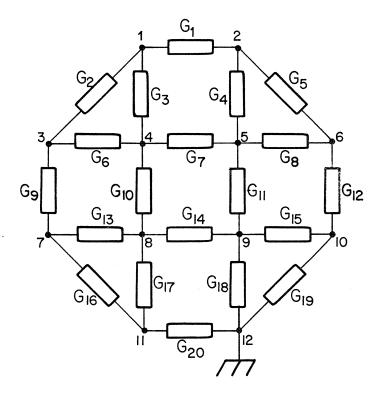


Fig. 1 The resistive network.

ł,

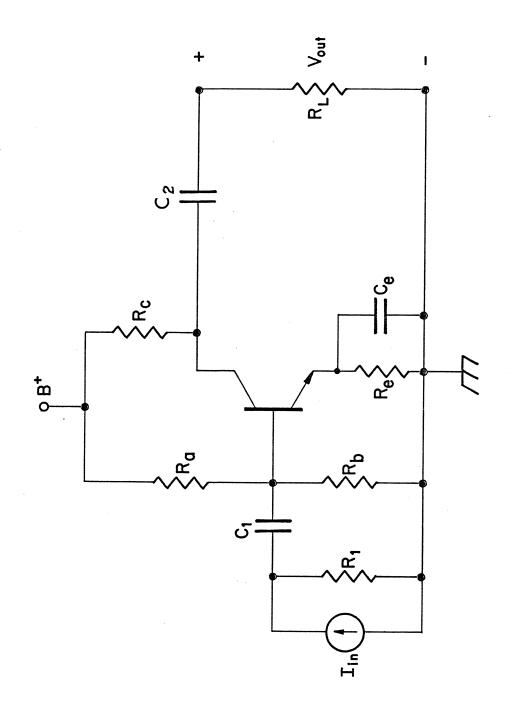
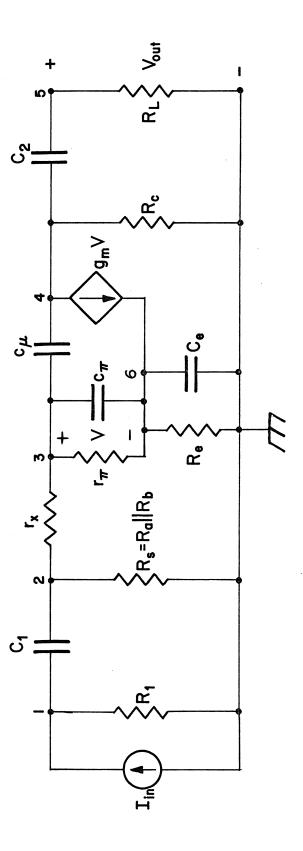
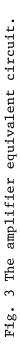


Fig. 2 The transistor amplifier circuit.





SOC-271

FAULT ISOLATION IN LINEAR ANALOG CIRCUITS USING THE L, NORM

J.W. Bandler, R.M. Biernacki, A.E. Salama and J.A. Starzyk

September 1981, No. of Pages: 14

Revised:

Key Words: Fault analysis, analog circuits, linear programming, iterative algorithms

Abstract: This paper deals with fault isolation in linear analog circuits under an insufficient number of independent voltage measurements. The L norm is utilized in isolating the most likely faulty elements. Earlier work is extended by allowing measurements to be taken for more than a single excitation. An iterative procedure is followed in which we utilize linear programming as a powerful tool in solving the problem. Convergence is fast and the results of circuit examples subject to practical tolerances on components are much sharper than in our earlier method.

Description:

Related Work: SOC-233, SOC-235, SOC-236, SOC-244, SOC-251, SOC-259, SOC-263, SOC-266, SOC-267, SOC-268.

Price: \$ 6.00.