

INTERNAL REPORTS IN
SIMULATION, OPTIMIZATION
AND CONTROL

No. SOC-244

SUFFICIENT TEST CONDITIONS FOR PARAMETER IDENTIFICATION
OF ANALOG CIRCUITS BASED ON VOLTAGE MEASUREMENTS

R.M. Biernacki and J. Starzyk

March 1980

FACULTY OF ENGINEERING
McMASTER UNIVERSITY
HAMILTON, ONTARIO, CANADA



SUFFICIENT TEST CONDITIONS FOR PARAMETER IDENTIFICATION
OF ANALOG CIRCUITS BASED ON VOLTAGE MEASUREMENTS

R.M. Biernacki and J. Starzyk

ABSTRACT

In this paper sufficient test conditions for identification of component values of linear analog circuits are investigated. Tests under consideration are assumed to be performed at a single frequency and consist of voltage measurements using different current excitations. Based on the fact that it is sufficient for this identification to perform nodal voltage measurements using all possible independent current excitations a systematic way to eliminate some unnecessary tests is proposed. A simple method for checking whether a reduced number of tests is sufficient for the identification is then formulated.

INTRODUCTION

Recently, there has been considerable interest in fault diagnosis for analog circuits (e.g., [1-9]). One of the possible approaches uses fault simulations and constructing a fault dictionary of mainly catastrophic faults (e.g., [9]). However, looking for so-called "soft faults" authors usually consider the identification of actual component values.

The solvability of the all parameter identification problem was initiated by Berkowitz [1]. His approach was mostly based on current measurements. The use of voltage measurements only was investigated by several other authors [3-5]. They formulated appropriate equations

R.M. Biernacki is with the Group on Simulation, Optimization and Control, Faculty of Engineering, McMaster University, Hamilton, Canada, on leave from the Institute of Electronics Fundamentals, Technical University of Warsaw, Warsaw, Poland. J. Starzyk is with the Institute of Electronics Fundamentals, Technical University of Warsaw, Warsaw, Poland.

This work was supported in part by the Natural Sciences and Engineering Research Council of Canada under Grant A7239.

which can be used to calculate component values based on sufficient number of independent measurements. How to arrange for the least number of independent measurements, however, has not been known so far. This problem is studied in this paper in some detail.

Tests under consideration consist of voltage measurements using different current excitations. The tests are performed at a single frequency, so the most one can expect is to determine the values of passive admittances and control coefficients of controlled sources. Repeating the identification at different frequencies enables one to find the component values provided that there is a unique dependence of them on the frequency response. Thus, in this paper we are interested in determining the unknown nodal admittance matrix \underline{Y}_n of the equation

$$\underline{Y}_n \underline{V}_n = \underline{I}^S, \quad (1)$$

where \underline{V}_n is the vector of nodal voltages measured and \underline{I}^S is the vector of applied nodal current excitations. We assume that the component admittance values can be found from \underline{Y}_n , i.e., that there exists a unique solution to the system of equations

$$\underline{B}_j \underline{y} = \underline{y}_j, \quad j = 1, 2, \dots, n, \quad (2)$$

where $\underline{y} = [y_1 \ y_2 \ \dots \ y_p]^T$ is the vector of component admittances, y_j is the j th column of \underline{Y}_n^T and \underline{B}_j is an appropriate matrix corresponding to the known network topology and consisting of 0, -1 and +1 values. The assumption means, for instance, that there are no parallel elements such as passive admittances or current sources controlled by the same voltage. As is well known, such elements are not solvable, so if they occur we usually treat them as a single component. Finally, note that the existence of the unique solution of (2) depends only upon the network topology.

GENERAL SUFFICIENT CONDITIONS

Consider the i th test as one which consists of nodal voltages \underline{V}_n^i measured when a known current vector \underline{I}^{Si} is applied. Equation (1) holds for every test, i.e., for vectors \underline{V}_n^i and \underline{I}^{Si} , $i = 1, 2, \dots$, each time with the same matrix \underline{Y}_n . Taking n tests we can write a single matrix

equation

$$\underline{Y}_n \underline{V}_t = \underline{I}_t^S, \quad (3)$$

where $\underline{V}_t = [V_t^1 \ V_t^2 \ \dots \ V_t^n]$ is nxn matrix of voltages measured and $\underline{I}_t^S = [I_t^{S1} \ I_t^{S2} \ \dots \ I_t^{Sn}]$ is nxn matrix of consecutive test excitations. From (3) we find the unknown matrix \underline{Y}_n as

$$\underline{Y}_n = \underline{V}_t^{-1} \underline{I}_t^S \quad (4)$$

provided that \underline{V}_t is nonsingular. As a consequence of equations (3) and (4) the following theorem gives general sufficient conditions for the identification.

Theorem 1 If a given linear network can be described by the nodal equation (1) and the test excitations are chosen in such a way that \underline{I}_t^S is a nonsingular matrix then \underline{V}_t is also nonsingular and the solution (4) exists.

Proof of the theorem follows from equation (3) since $n = \text{rank } \underline{I}_t^S \leq \text{rank } \underline{V}_t \leq n$.

Thus, in order to identify all component values we may arrange for n independent current excitations, measure all nodal voltages and then apply equations (4) and (2). This approach, however, can be redundant. In the next section we shall discuss the problem of eliminating some unnecessary tests.

TEST GENERATION

One of possible choices of independent excitations is to apply a unit current consecutively to all n nodes, i.e., we may consider

$$\underline{I}_t^S = \underline{1}. \quad (5)$$

This together with (4) give us the very important relation

$$\underline{V}_t = \underline{Y}_n^{-1} \quad (6)$$

which imposes certain relations between elements of the matrix \underline{V}_t . For instance, if \underline{Y}_n is symmetrical (as for reciprocal networks) then \underline{V}_t is

so. In general, \underline{Y}_n has a particular (usually sparse) form corresponding to the known network topology, hence there are certain constraints to the elements of its inverse. There are three types of these constraints:

$$(1) \quad y_{ij} = 0 \text{ (or } \Delta_{ji} = 0), \quad (7)$$

for nonincident nodes (Δ_{ji} denotes the appropriate minor of \underline{V}_t),

$$(2) \quad y_{ij} = y_{ji} \text{ (or } \Delta_{ji} = \Delta_{ij}), \quad (8)$$

for passive branches,

$$(3) \quad \begin{aligned} y_{ik} + y_{jk} &= y_{ki} + y_{kj} \\ \text{and/or} \quad y_{im} + y_{jm} &= y_{mi} + y_{mj}, \end{aligned} \quad (9)$$

for voltage controlled current sources.

The constraints (7)-(9) are known, so we should take advantage of this in order to eliminate unnecessary measurements. Since the constraints are independent their total number is equal to the number of elements of \underline{V}_t which can uniquely be determined based on remaining elements. However, they cannot be chosen arbitrarily. From the other side what we intend to do in order to perform the least number of tests is to eliminate whole columns of \underline{V}_t . In the following we propose a systematic way which enables us to indicate tests sufficient for component value identification. The method is based on the assumption that all voltages measured as well as all components have nonzero values.

Equation (6) rewritten in a slightly different form

$$\underline{V}_t^T \underline{Y}_n^T = \underline{1} \quad (10)$$

can be considered as n systems of equations

$$\underline{V}_t^T \underline{y}_j = \underline{e}_j, \quad j = 1, 2, \dots, n, \quad (11)$$

where \underline{e}_j denotes the jth column of unit matrix. Note that coefficients of each equation in (11) are taken from only one measurement test and also that unknown elements of \underline{y}_j correspond to the jth nodal cut-set branch admittances.

Let us suppose that, for certain j, \underline{y}_j has a number, say n-k₁, elements of zero value. Since the solution of (11) is unique we do not

need to know the elements of $n-k_1$ columns of $V_{\sim t}^T$ corresponding to zero-value elements of y_j . Therefore, eliminating them we obtain a reduced system (11) having n equations and k_1 unknowns. Then we can also eliminate exactly $n-k_1$ equations (tests). The resulting system has to contain the j th equation for which the right hand side equals 1. Otherwise, if we did not choose the j th equation the resulting system would be homogenous and consequently, since the solution is different from zero, singular.

Now, suppose that we have chosen exactly k_1 equations, including the j th equation. In order to check whether this subsystem is solvable or not we can employ the fact that a subdeterminant of $V_{\sim t}^T$ is different from zero if and only if the complementary subdeterminant (obtained by removing the rows and columns chosen) of the transpose to the inverse of $V_{\sim t}^T$, i.e., of $Y_{\sim n}$, is different from zero. Although the particular value of this determinant depends on particular values of unknown elements of $Y_{\sim n}$ it can be "structurally" (i.e., dependently on zero elements) either of zero or nonzero value. Thus, using this criterion one can indicate the groups of k_1-1 tests which together with the j th test are not sufficient to determine the unknown elements of y_j . Any other group of k_1 tests (including the j th test) is topologically sufficient for this identification. In other words, except very particular values of $Y_{\sim n}$ (e.g., some elements have zero values, although they are assumed to be different from zero) the corresponding submatrix of $V_{\sim t}^T$ is nonsingular.

The simplest situation occurs when y_j has only 2 nonzero elements. Then, assuming that all voltages measured are different from zero (e.g., the network is not weakly connected) any test (equation) can be chosen together with the j th equation.

One of possible choices of k_1 independent equations is that that we consider the tests corresponding to the excitations at all nodes j_1, j_2, \dots, j_{k-1} which are incident with the j th node. This is simply the same result as that of Trick et al. in [4]. However, if we intended to use such tests at every cut-set we would eventually have to consider all nodal excitations as in Theorem 1. In fact, what we actually want to find are some other excitations which can replace all or part of the excitations at nodes j_1, j_2, \dots, j_{k-1} .

In some cases the nonzero elements of y_j can be dependent. For

instance, this is the case when no admittance exists between the j th and the datum nodes. Then we can eliminate one more test out of k_1-1 tests chosen.

Based on one of proper groups of k_1 tests we are able to identify all admittances of the j th cut-set, so although we do not need, for the time being, to indicate which group has been chosen we can consider these elements as known. Then, consider another column \underline{y}_ℓ such that it contains one of the elements of \underline{y}_j . We now know not only all of $n-k_2$ zero elements of \underline{y}_ℓ but also that nonzero admittance $y_{\ell m}$ which is common with \underline{y}_j . As before after eliminating $n-k_2$ columns of \underline{V}_t^T corresponding to zero-elements we can properly select k_2 equations (including the ℓ th equation). Since $y_{\ell m}$ is known and is different from zero it can be shown that after dropping the ℓ th equation we obtain k_2-1 independent equations with k_2-1 remaining unknowns. This is quite an important result: knowing at least one of elements of a cut-set we do not have to use the direct excitation of this cut-set in order to solve for remaining elements. The circuit interpretation of this is the following. The value of $y_{\ell m}$ as well as the voltage across this element are known. Thus, after finding the current we can replace $y_{\ell m}$ by an independent current source and then express it as a combination of nodal current excitations. Doing so with all currents of the j th cut-set we eliminate the j th node. Repeating this for all k independent excitations at the j th node and all incident $k-1$ nodes we now express them as linear combinations of $k-1$ excitations. They are dependent from the remaining network point of view and, in particular, the ℓ th excitation (test) can be determined as a linear combination of the j th excitation (test), which is already chosen, and some other ones.

As before, we do not want, for the time being, to indicate which group of k_2-1 equations has been chosen. However, it is desirable that these k_2-1 equations are included in one of group of k_1 equations considered in the first step. If this is not satisfied we have to augment them.

Using this approach we proceed with the subsequent columns of \underline{Y}_n^T and finally we can decide which equations (tests) may be chosen in order to identify all elements of \underline{Y}_n .

The above discussion is also valid in any case when equation (1) is

based on a system of independent cut-sets instead of nodal cut-sets. In particular, it holds for any fundamental system of cut-sets (generated by a certain tree). Given a tree, \underline{V}_n and \underline{V}_t represent voltages measured across the tree branches and the unit matrix of (5) corresponds to unit excitation at terminals of tree branches, each at a time. The vector \underline{y}_j corresponds to j th cut-set admittances. As a conclusion from our considerations we can formulate the following theorem.

Let us choose a sequence of independent cut-sets $\gamma_1, \gamma_2, \dots, \gamma_n$. Let $\hat{\gamma}_1, \hat{\gamma}_2, \dots, \hat{\gamma}_n$ be reduced cut-sets defined as

$$\hat{\gamma}_1 = \gamma_1 \quad \text{and} \quad \hat{\gamma}_i = \gamma_i - \bigcup_{k=1}^{i-1} \gamma_k.$$

Let β_i denotes the set of indices of cut-sets determined by independent voltages (e.g., tree voltages) corresponding to all elements of $\hat{\gamma}_i$. Finally, consider a set of test indices α such that $\text{card } \alpha \geq \max(\text{card } \beta_i)$.

Theorem 2 Tests α are sufficient for the identification of component admittances if there exist $\alpha_i \subset \alpha$, $\text{card } \alpha_i = \text{card } \beta_i$, such that $\det \underline{Y}_n(\alpha_i | \beta_i) \neq 0$ for $i = 1, 2, \dots, n$, where $\underline{Y}_n(\alpha_i | \beta_i)$ denotes the submatrix of \underline{Y}_n obtained by removing α_i rows and β_i columns.

It is to be noted that the choice of independent cut-sets as well as their sequence is crucial for better selection of tests. Now, we give an example of implementation of Theorem 2.

EXAMPLE

Consider a simple resistive active circuit shown in Fig. 1.

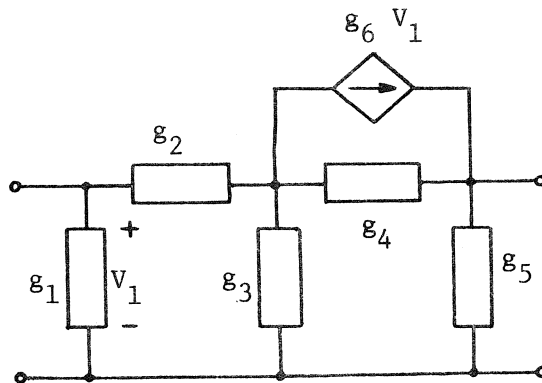


Fig. 1 An active circuit example

Let us choose the sequence of cut-sets $\gamma_1 = \{1,2\}$, $\gamma_2 = \{2,3,5\}$, $\gamma_3 = \{4,5,6\}$ based on the tree $\{1,3,4\}$. The reduced sequence is $\hat{\gamma}_1 = \{1,2\}$, $\hat{\gamma}_2 = \{3,5\}$, $\hat{\gamma}_3 = \{4,6\}$, so we find $\beta_1 = \{1,2\}$, $\beta_2 = \{2,3\}$ and $\beta_3 = \{1,3\}$. Based on the matrix

$$Y_n = \begin{bmatrix} g_1+g_2 & -g_2 & 0 \\ -g_2 & g_2+g_3-g_5 & g_5 \\ g_6 & -g_5 & g_4+g_5 \end{bmatrix}$$

we find that tests $\alpha = \{1,2\}$ are sufficient for the identification provided that $g_5 \neq 0$ and $g_6 \neq 0$. Similarly we can check that tests $\alpha = \{1,3\}$ are sufficient provided that $g_2 \neq 0$ and $g_5 \neq 0$.

CONCLUSIONS

Sufficient test conditions for identification of all component values are investigated in this paper. It is shown that measuring nodal voltages using all possible independent current excitation is sufficient for this identification. Then a systematic way to eliminate some unnecessary tests is proposed. However, the choice of a sequence of cut-sets is crucial for this elimination, i.e., the solution may be or may not be optimal. The results are summarized in Theorem 2 which gives a simple method of checking whether a reduced number of tests is sufficient for the identification. This is illustrated by a simple active circuit example.

ACKNOWLEDGEMENT

The authors would like to express their great appreciation to Dr. J.W. Bandler of McMaster University for his help throughout the course of this work.

REFERENCES

- [1] R.S. Berkowitz, "Conditions for network-element-value solvability", IRE Trans. Circuit Theory, vol. CT-9, 1962, pp. 24-29.
- [2] W. Mayeda and G. Peponides, "Determination of component values in passive networks under limited measurements", Proc. 12th Asilomar Conf. on Circuits, Systems and Computers, Western Periodicals (North Hollywood, Nov. 1978), pp. 761-764.

- [3] T.N. Trick and A.A. Sakla, "A new algorithm for the fault analysis and tuning of analog circuits", Proc. IEEE Int. Symp. Circuits and Systems (New York, NY, 1978), pp. 156-160.
- [4] T.N. Trick, W. Mayeda and A.A. Sakla, "Calculation of parameter values from node voltage measurements", IEEE Trans. Circuits and Systems, vol. CAS-26, 1979, pp.. 466-474.
- [5] R.M. Biernacki and J.W. Bandler, "Postproduction parameter identification of analog circuits", Proc. IEEE Int. Sym. Circuits and Systems (Houston, TX, 1980).
- [6] N. Navid and A.N. Wilson, Jr., "A theory and an algorithm for analog circuit fault diagnosis", IEEE Trans. Circuits and Systems, vol. CAS-26, 1979, pp. 440-457.
- [7] N. Sen and R. Saeks, "Fault diagnosis for linear systems via multifrequency measurements", IEEE Trans. Circuits and Systems, vol. CAS-26, 1979, pp. 457-465.
- [8] T. Ozawa and Y. Kajitani, "Diagnosability of linear active networks", IEEE Trans. Circuits and Systems, vol. CAS-26, 1979, pp. 485-489.
- [9] W. Hochwald and J.D. Bastian, "A DC approach for analog fault dictionary determination", IEEE Trans. Circuits and Systems, vol. CAS-26, 1979, pp. 523-529.

SOC-244

SUFFICIENT TEST CONDITIONS FOR PARAMETER IDENTIFICATION OF ANALOG
CIRCUITS BASED ON VOLTAGE MEASUREMENTS

R.M. Biernacki and J. Starzyk

March 1980, No. of Pages: 9

Revised:

Key Words: Fault analysis, parameter identification, network
 element-value solvability, testing

Abstract: In this paper sufficient test conditions for identification
of component values of linear analog circuits are investigated. Tests
under consideration are assumed to be performed at a single frequency
and consist of voltage measurements using different current excitations.
Based on the fact that it is sufficient for this identification to
perform nodal voltage measurements using all possible independent
current excitations a systematic way to eliminate some unnecessary tests
is proposed. A simple method for checking whether a reduced number of
tests is sufficient for the identification is then formulated.

Description:

Related Work: SOC-233, SOC-235, SOC-236.

Price: \$ 3.00.

