

PL/2100 AND HPCOM

**Dedicated  
to my  
Parents**

PROGRAMMING LANGUAGE 2100

AND

THE COMPILER HPCOM

By

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## Abstract

This report contains a description of the programming language PL/2100, which is a procedure oriented, block structured language with an extensive set of operators, including arithmetic, relational, logical, bit manipulation and shift operators. It is designed for writing PL/2100 programs that conform to the standard of structured programming and for efficiently expressing and implementing algorithms written in it.

This report, also, contains a description of the one-pass working compiler (HPCOM) (for PL/2100) written for the HP/2100A minicomputer.

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CHAPTER I  
INTRODUCTION.

1.1 Comparison of High Level Language and Assembly Language

1.1.1 Assembler Language

An assembler language is a symbolic form of machine language. While machine language is numeric, assembler language allows alphabetic names for operation codes and storage locations.

1.1.1.1 Advantage of Assembler

Assembler language permits the symbolic writing of machine language instructions, thus contributing to the speed and accuracy of the programming and debugging processes.

Through an assembler, users can also access all registers available for programming for the machine.

1.1.1.2 Disadvantage of Assembler

Only one data type viz. WORD of a machine is available.

Operations and access on structured data need programming and required structure is built into the program.

Only two control structures are available for programming i) Unconditional jump instructions (ii) Conditional skip instructions.

### 1.1.1.3. Conclusion

Hence a program in assembler is necessarily built up of very small parts joined by jump instructions.

### 1.1.2 High Level Language

A high level language is one which is independent of the features of a particular machine. Hence it is more easily adaptable by a user who does not know any feature of a machine.

#### 1.1.2.1 Advantages

i) Many data types (e.g. in PASCAL we have an infinite no. of possible data types) are available.

Structure can be built into data where it belongs and need not be built into programs (as in the case of an assembler language).

ii) Reasonable number of control statements could be made available in the language. So a program can be written in a relatively small number of parts (compared to an assembler program of similar size), the flow of control into and from which is easily discernible provided certain rules (as provided in the language) are followed.

#### 1.1.2.2. Disadvantages

The program and data of a high level language are removed from the hardware of a machine. There may be machine instructions which cannot be used (even if they could simplify the processing of a particular problem), as problem-oriented

machine codes cannot be produced by a compiler of a language.

## 1.2 Need for Amalgamation of High Level Language and Assembler Language

The obvious answer to the possibility of getting the benefits from both assembler and high level languages, is to merge the low-level access of registers and instructions of a machine with the availability of numerous data types and control structure of a high level language.

This has been done before viz PL/360<sup>(W1)</sup> and SUE/360<sup>(K1)</sup> but intentional hardware dependency of a language means that the design of such a language must be done right from the beginning for each class of computer.

PL/2100 (Programming Language 2100) has been written for HP/2100A (Hewlett-Packard 2100A) with the intention of incorporating the above-mentioned features in the language.

## 1.3 Minicomputers and Need for High Level Languages for Minicomputers

### 1.3.1 Minicomputers

Although minicomputers have been available for many years, the full range of their applicability to all aspects of computing is, only now beginning to be adequately explored.

The minicomputers are mini in several ways

#### i) mini wordlength

The wordlength of the most common mini machines (e.g. PDP, HP) is 16 bits, although minicomputers are also available with word sizes of 8, 12 and 18 bits.

4

ii) mini memory size

Memory size of minicomputers have been, traditionally, small. They usually have 4K or 8K (1K = 1024 words) of memory, although minicomputers can be expanded into larger memory sizes.

iii) mini cost

May be the most important and attractive feature is the low cost of the minicomputers.

Recent advances in solid state circuit technologies have allowed instruction sets of minicomputers to be sophisticated, keeping the cost within a reasonable range.

Even though internal speed of minicomputers are comparable to those of larger machines, the throughput is smaller than the larger processors because of the short word-lengths.

There are various usages of the minicomputers, e.g. process control, to give greater flexibility to larger machine by providing time sharing or remote job entry, teaching of machine organization because of the simplicity of the hardware etc.

A great enhancement in the use of minicomputers has been because of the possibility of using Disc Operating System for the minicomputers. This, not only gives a simple and flexible operating system from the programmer's point of view, but also gives a huge secondary storage (in disc) complementing the small memory size of the minicomputers.



### 1.3.2 Need for High Level Language for Minicomputers

The recent advances in the hardware design of minicomputers, have not been paralleled by the development of software. System programs for minicomputers have, generally, been written in the assembler language of the host machine.

Sammet<sup>(S1)</sup> has indicated that the advantages of high level language for software implementation:

- i) Easy conversion to another machine.
- ii) Greater ease for a person to pick up somebody else's work.

Except for a few exceptions, high level system languages for minicomputers are not available at present.

The reasons behind the use of low level languages for minicomputer software development were that

- i) The software would have to be written only once and the best possible code sequence should be used.
- ii) Compilers for high level languages could not produce as good a code as a programmer familiar with the idiosyncrasies of the machine.
- iii) A good compiler for an acceptable high level language could not meet the memory size restraints of minicomputers.

The new generation of minicomputer however, provides the capability of using a high level language for software development as the minicomputers have sophisticated instruction sets

and become versatile to handle large programs. This capability is enhanced because of the availability of discs.

1.4 Existing High Level Machine-oriented Language

The first of these two types of languages is PL/360 written for IBM/360 machines (W1).

The other languages have been SUE/360 (K1) written also for IBM/360 machines, the BLISS system implementation language (W2) written for PDP-10 and SUE system language for PDP-11 family machines (K1).

The Burrough family machines are designed with ALGOL in mind. These machines have no symbolic machine language as such! ALGOL is the machine language and system software is implemented in an extended version of ALGOL.

1.5 Features of HP/2100A Machine

The section is devoted to a summary of the different features of HP/2100A, so that relevant instruction sets can be chosen from PL/2100 as well as to get a better understanding of the language PL/2100.

1.5.1 Word-Length

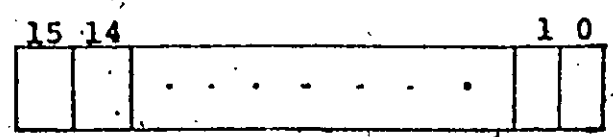


fig. 1.5 HP WORD

HP/2100A has a 16-bit word length and is only word addressable.

The lower order 10 bits (0-9) are used for addresses in simple memory instructions whereas extended memory instructions may have addresses up to 16 bits long.

1.5.2 Registers

The 2100A computer has six 16-bit working registers (including two accumulators A- and B-registers), two one-bit registers (E- and O-registers) and (on the operating panel) one 16-bit display register.

i) A- and B-registers

These are two accumulators which can hold the result of arithmetic operations (independent of each other). These are the absolute locations 000000B and 000001B in the machine memory and hence, can be accessed through the memory reference instructions (both single and extended memory reference instructions).

ii) E-register

This is a one-bit register which can be used with A- and B-registers for many shift and rotate instructions. This is known as the Extend register.

iii) O-register

This one-bit register is known as overflow-register and holds the overflow condition occurring from an arithmetic operation.

iv) M-register

It holds the address of the memory cell currently being read from or written into.

v) T-register

All data transferred into and out-of memory is routed through memory data register.

vi) S-register

It is a 16-bit utility register. In the halt mode of the machine, it can be manually loaded via display register on the panel. In the run mode it can be addressed as an I/O device (select code 01).

vii) Registers available through microprogramming

There are other registers available in HP/2100A, which are available only through microprogramming, not through software programming.

For example:a) Q-, F-registers

These are 16-bit accumulators. Special microprograms must be written in order to access these registers.

b) Scratch pad registers

Like the Q- and F-, the four scratch pad registers are available to software by special microprogramming.

The detailed discussion has been given in the booklet<sup>(H1)</sup> for microprogramming (Hewlett Packard).

### 1.5.3 Memory

The 2100A computer can be equipped with any of six memory configurations from 4K to 32K (1K = 1024 words). The available configurations, which determine the addressing range are: 4K, 8K, 12K, 16K, 24K and 32K.

#### 1.5.3.1 Paging

The computer memory is logically divided into pages of 1024 words each. A page is defined as the largest block of memory which can be directly addressed by the memory address bits (0-9) of a memory reference instruction (single length).

Provision is made to address directly one of the two pages: page zero (base page) and the current page (in which instruction itself is located). A memory reference instruction word includes a bit (bit 10) to specify one or the other of these two pages. To address locations in any other page, indirect addressing is used. Page reference is specified by bit 10 as follows:

Logic 0 = Page Zero (Z)

Logic 1 = Current Page (C)

#### 1.5.3.2 Addressing

All addressing in HP/2100A is done through the memory reference instructions. A HP/2100A memory reference instruction word contains

- (i) for single length instructions [fig. 1.5.3.2.1]
  - a) bit 15 for direct or indirect addressing
  - b) bit 10 for addressing to one of two pages

viz page zero or current page

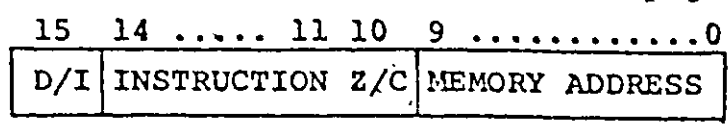


fig. 1.5.3.2.1 Single Length Memory Instruction Word

- c) bits 11-14 for instructions
- d) bits 0-9 for addresses.
- ii) for extended arithmetic instructions [fig. 1.5.3.2.2]
  - a) First word is the instruction itself.
  - b) Bit 15 of the memory address word is for direct or indirect addressing.
  - c) The second word is the address word.

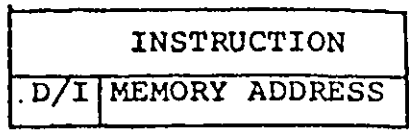


fig. 1.5.3.2.2 Extended Length Memory Instruction Word

1.5.3.3 Indirect Addressing

There is no index register available but a bit is available in memory reference instruction to indicate indirect addressing.

For single length memory reference instructions, bit 15 of the instruction word is used; for extended arithmetic memory reference instructions, bit 15 of the address word is used. Indirect addressing uses the address part of the instruction to access another word in memory, which is taken as a new memory reference for the same instruction. This new address word is a full 16-bits long, 15 bits of address plus another direct or indirect bit. 15-bit length of address

permits access to any location in memory. The first address obtained in indirect phase which does not specify another indirect level becomes the effective address for the instruction.

Direct or Indirect addressing is specified by bit 15 is as follows:

Logic 0 = Direct

Logic 1 = Indirect.

1.5.4. Instruction Format

Instructions for the HP/2100A have four formats. Instructions are classified according to formats.

1.5.4.1 Memory Reference

Single length memory reference instructions: Format is given in fig. 1.5.4.1.

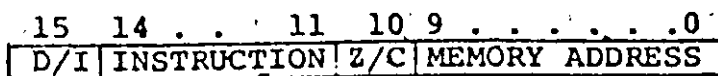


fig. 1.5.4.1 Single Length Memory Instruction

Instruction is 4-bit long and is placed in bits 14-11 (inclusive).

1.5.4.2 Extended Arithmetic Memory Reference Instructions

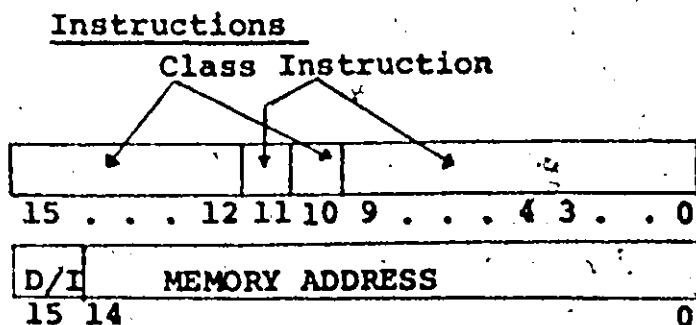


fig. 1.5.4.2 Extended Arithmetic Memory Instruction

Instruction code is given in the first word and the address is taken from the next word.

### 1.5.4.3 Register Reference

The 39 register reference instructions execute various functions on data contained in the A-, B-, E-registers.

The instructions are divided into two groups, the shift-rotate group and alter skip group. In each group, several instructions may be packed into one word (termed "micro-instructions" in the HP literature). Since two groups are separate and distinct the packed instructions from two groups cannot be mixed.

#### 1.5.4.3.1 Shift-rotate Group

There are 20 instructions in the shift-rotate group. The bit 10 is zero for skip-rotate group.

#### 1.5.4.3.2 Alter-skip Group

There are 19 instructions in the alter-skip group. This group is specified by "1" in bit 10.

A detailed discussion of these instructions and the rules of packing microinstruction have been given in Appendix B.

The figure 1.5.4.3.1 shows register reference instruction format.

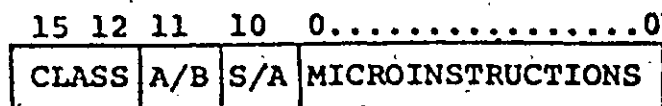


fig. 1.5.4.3.1 Register Reference Instruction Format



A/B → denotes A- or B-register

Logic 0 → A

Logic 1 → B

S/A → denotes Skip-rotate or Alter-skip groups

Logic 0 → S

Logic 1 → A . . .

### 1.5.5 Data Format

The basic data format for the 2100A computer is a 16 bit word. Bit positions are numbered from 0 through 15, in order of increasing significance. Data are stored in two's complement. Bit 15 of the data format is used for the sign bit, a "0" in this position indicates a positive number and a "1" indicates a negative number. The data is assumed to be a whole number, thus binary point is assumed to be the right of the number.

The basic word can be divided into two 8-bit bytes. The byte format is used for character-oriented I/φ devices. Packing of two bytes into one word is accomplished by the software drivers. In I/φ operations the higher order byte (Byte 1 viz 8-15) is the first to be transferred.

### 1.5.6 Conclusion

The section 1.5 has given briefly some of the salient features of the HP/2100A computer. The details of the other features may be obtained from the Hewlett-Packard reference manuals (H2, H3).

## 1.6 Programming Language 2100 (PL/2100)

### 1.6.1 Choice of High Level Language

The high level language chosen for PL/2100 is PASCAL<sup>(W2)</sup>.

The reasons for the choice of this most recently developed language are:

- i) Beautiful and powerful data structure.
- ii) Ease of extension for implementation by bootstrapping.

### 1.6.2 Choice of Features of HP/2100A Assembler

The choice of HP/2100A instructions which will appear as operators in PL/2100A is difficult. For simplicity, at present, only few shift instructions have been chosen as shift operators.

These instructions are ALS, ARS, RAL, RAR, and ALF and corresponding instructions for the B-register.

Other instructions may be included easily and the compiler can be modified, accordingly, without much difficulty. It should not take more than a month for a person who is familiar with the compiler.

The reason for the choice of these instructions as operators, is that they allow shifting to be done on the contents of a particular register.

The memory reference instructions chosen are only IOR and XOR as most of the other memory reference instructions could very well be substituted by different statements of the language PASCAL.

### 1.6.3 Form of PL/2100

The detailed description of the language in BNF is given in the Appendix A.

Several of the most salient design features are:

- a) The main statement constructions are the assignment, while, if-then-else, case, go to, repeat and call statements.
- b) Every program consists of a sequence of procedures which can access a set of global variables, parameters or local variables.
- c) These are compound statement constructions as well as block constructions.
- d) Procedure may be recursive if they are so declared.
- e) An extensive set of operators are permitted in an expression. These are arithmetic, logical, relational and shift operators.
- f) A wide variety of data types is allowed. Scalar, subrange, Array and Record types. Various other data types can be formed with these basic data types.
- g) Another feature is the introduction of "Synonymy" between different simple variables ie. a number of simple variables can be declared to be "synonymous" (a term borrowed from PL/360<sup>(W1)</sup>). In other words, two or more identifiers may refer to one storage location. This is close to "equivalence" statement used in FORTRAN.

## 1.7 Purpose of This Project

This project is, mainly, concerned with the design of a high level language (PL/2100) for the HP/2100A computer and produces a working compiler for it.

The language PL/2100 has been, carefully, designed with the hope of:

- i) Easy extension.
- ii) Varieties of data structures.

The ease of extension and the various data structure make it attractive to be used for the minicomputer HP/2100A which, in the present installation, does not support a compiler for this type of high level language.

The compiler has been written in PASCAL (which is available on CDC-6400) and is one pass. It runs on the CDC-6400 and produces relocatable binary which can, easily, be interfaced with the Disc Operating System (DOSM) of the HP/2100A computer.

The compiler, at the present stage, is far from being an ideal one. It does all the basic things necessary to handle expressions and various statements, but does not produce codes for procedures.

HPCOM (the compiler for PL/2100) as a one-pass system will not fit into HP/2100A machine [It takes about 55K at the present stage]. The DOSM system of HP/2100A allows segments of a program residing on the disc, to be brought onto the memory. Attempts have been made to segment the HPCOM logically, so that

different segments could be brought in physically from the disc to memory. Logical segmentation was possible and it would have taken 9 or 10 passes to compile a program in PL/2100. This is time consuming and too complicated. It was thought to be wise to discard this approach. (A detailed discussion of segmentation will be given in Chapter IV). Hence there remain two possibilities viz either to take a more limited subset of PL/2100 or use the CDC-6400 computer for developing the software for HP/2100A. The latter is considered to be better and more feasible. HPCOM runs on CDC-6400.

Though more work has to be done to make the compiler better, the present work is a first important step towards a more complete system.

We will describe the method of compiling and different aspects of HPCOM in Chapter II. Chapter III deals with the code generation part of the compiler HPCOM. In Chapter IV, we will describe I/φ routines, the interface with the operating system and the segmentations of HPCOM. In Chapter V, we have given the concluding remarks along with the limitations and the possible modifications of HPCOM.

## CHAPTER II

### COMPILING AND THE HPCOM COMPILER

#### 2.1 One-pass Compiler

♦ The key to an efficient compiler for a fast computer with a relatively large main store is the one-pass scheme. It minimizes the number of references to secondary store which involve the operating system and exceed all other processes by orders of magnitude of time consumption. The restrictions imposed on the language due to the choice of a one-pass scheme, are minimal (viz the objects have usually to be declared textually prior to being referenced. Note that programming language 2100 has been designed keeping this in mind) and the complications due to unavoidable forward references are small<sup>(W3)</sup>.

Though the HP/2100A does not have large core, a one-pass scheme was chosen. In the beginning it was supposed to be bootstrapped onto HP/2100A but later the idea was discarded as was indicated in Chapter I. The HPCOM (compiler) runs on the CDC-6400.

The HPCOM compiler generates relocatable binary code for HP/2100A. The gain in compilation time (on the CDC-6400) compared to the time in many-pass compiler is, somewhat, reduced

by the use of standard relocatable loader (involving the operating system of HP/2100A). The advantage is the ability to merge "binary" programs after compilation and thus one can make full use of the library routines.

In a one-pass compiler, the preparation and the code generation parts are fused with semantic routines of the semantic analyser. A typical one pass scheme is given in fig. 2.1.

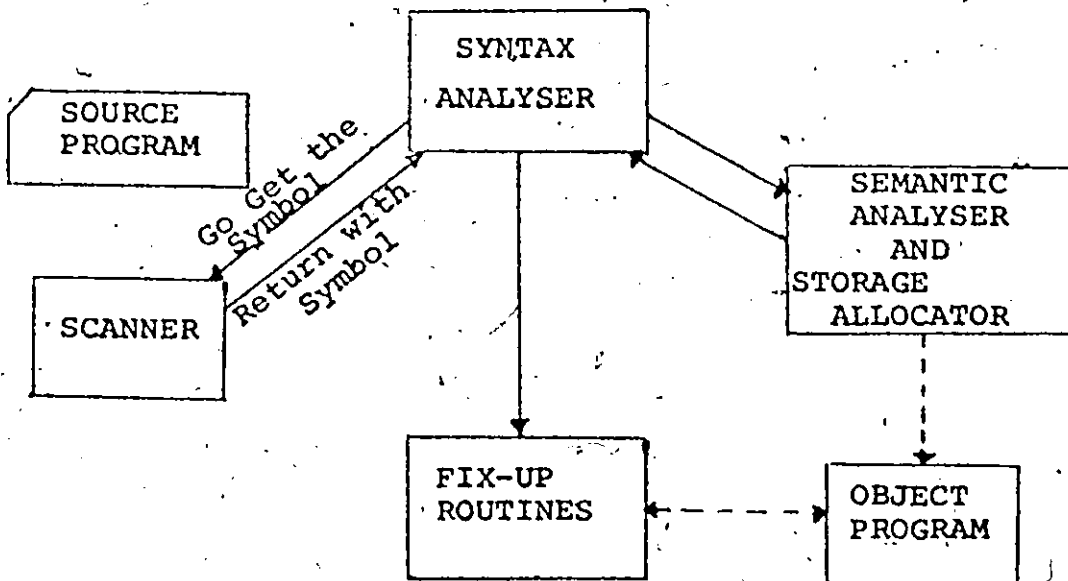


fig. 2.1 A Typical One-Pass Compiler

## 2.2 Tables of Reserved Words and Symbols of PL/2100

The programming language 2100 has several key words and symbols which are to be known at the compile time. These symbols and words are stored in different tables. The reserved words and symbols are associated with integer tokens which are

stored in tables as well. The reserved words of different lengths are stored in different tables. This has been done to reduce search time. In the actual world of programming, the tables are represented by arrays of integer and characters. For details, see Appendix C.

## 2.3 Context Table

### 2.3.1 The Description of Objects During Compilation

All identifiers occurring in a program are stored in a table along with a description of the object they name. Since every object is characterized by various attributes with different ranges of values, the record is the appropriate data structure. Since various objects are described by different sets of attributes, the record has a variant part. The table itself is an array of such records. The context table contains description of all named objects. The definition of the table itself is given below; it uses the following data types:

#### TYPE

AR = ARRAY[1..10] of CHAR;

SHRINT = -1777B .. + 1777B;

BITRANGE = 0..16;

ADDRESS = 0..1777B;

RG3 = 0..3;

IDKCLASS = (TYPES, KONST, PROC, VARS, FIELD,  
TAGFIELD, DUMMYCLASS);



TYPFORM = (NUMERIC, SYMBOLIC, ARRAYS, RECORDS,  
FILES, REGISTERS);

IDKINDS = (ACTUAL, FORMAL);

OPTPWR = (NOOPT, PUREP, POSP, NEGP);

VAR

CONTEXTTABLE: ARRAY[0..250] OF  
PACKED

RECORD

NAME: AR; NXTEL: SHRINT; SYNCCELL: BOOLEAN;

CASE KCLASS: IDKCLASS OF

TYPES : (SIZE: ADDRESS;  
CASE FORM: TYPFORM OF

NUMERIC: (BITS: BITRANGE; MIN, MAX: INTEGER);

SYMBOLIC: (FCONST: INTEGER; BITSIZE: BITRANGE);

ARRAYS: (AELTYPE, INXTYPE: SHRINT;  
LO, HI: SHRINT; SZE: BOOLEAN;  
OPTTYP: OPTPWR;  
EXP1, EXP2: BITRANGE);

RECORDS: (FSTFLD, RECVAR: INTEGER);

KONST: (CONTYPE: INTEGER;  
CASE CONKIND: IDKINDS OF

ACTUAL: (SUCC: INTEGER; VALUES: INTEGER);  
FORMAL: (CADDR: ADDRESS; CLEVEL: RG3);

PROC: (PROCTYPE, FORMALS: INTEGER;  
PROCKIND: IDKINDS;  
PROCADDR: ADDRESS; PROCLEVEL: RG3;  
SEGSIZE: INTEGER);

VARS: (VTYPE: INTEGER; VKIND: IDKINDS;  
SYNPTR: SHRINT;  
VADDR: ADDRESS; VLEVEL: RG3);

FIELD: (FLDTYPE: INTEGER; FLDADDR: ADDRESS;  
BITDISPL, BITWIDTH: BITRANGE);

```
TAGFIELD : (CASESIZE: INTEGER; VARIANTS: INTEGER;
           CASE TAGVAL: BOOLEAN OF
```

```
           FALSE: (CASETYPE: INTEGER);
           TRUE:  (CASEVAL: INTEGER));
```

```
END;      ↪ END OF THE DEFINITION OF THE CONTEXT
           TABLE ↓
```

Anonymous objects which are generated during compilation and correspond to component variable denotations, primaries, expressions, etc. are described by variable local to the various processing procedures. These are specified as follows:

TYPE

```
ATTRKIND = (VARBL, SVAL, LVAL, LCOND);
```

```
ATTR = RECORD
```

```
    TYPTR: INTEGER;
    CASE KIND: ATTRKIND OF
```

```
    VARBL: (ACCESS: (DRCT, INDRCT, INXD);
```

```
           BREG: RG3; DPLMT: INTEGER;
```

```
           CASE PCKD: BOOLEAN OF
```

```
           FALSE: ;
           TRUE:  (BITADR, BITSZ: BITRANGE));
```

```
    SVAL: (VAL: INTEGER);
```

```
    LVAL: (CTERM: INTEGER);
```

```
    LCOND: (JMP: 0..3; ARITH: BOOLEAN);
```

```
    END;
```

The complete datatype definitions used by the compiler to describe objects are included, here, not only to convey an insight into the compiler organization, but also to

demonstrate the power of PL/2100 data definition facilities (similar to PASCAL). They allow for a transparent and fully symbolic, machine independent form of data specifications, but at the same time make an economic usage of storage possible (the packed record has been used for that purpose).

### 2.3.2 Search Method

A linear search method has been used to search through the tables of reserve-words and the symbol table. The reason this method has been used, is because of its simplicity. In order to reduce the time for the search, the reserved words of different lengths are stored in different tables along with a table of pointers pointing to the first elements of the tables of reserved words. Thus search need not be made through all the tables at the same time. The identifiers are put into the objectable as they are encountered. A pointer is used, in the description of the identifier, to point to the previously encountered identifier. Identifiers stored are, obviously, all different.

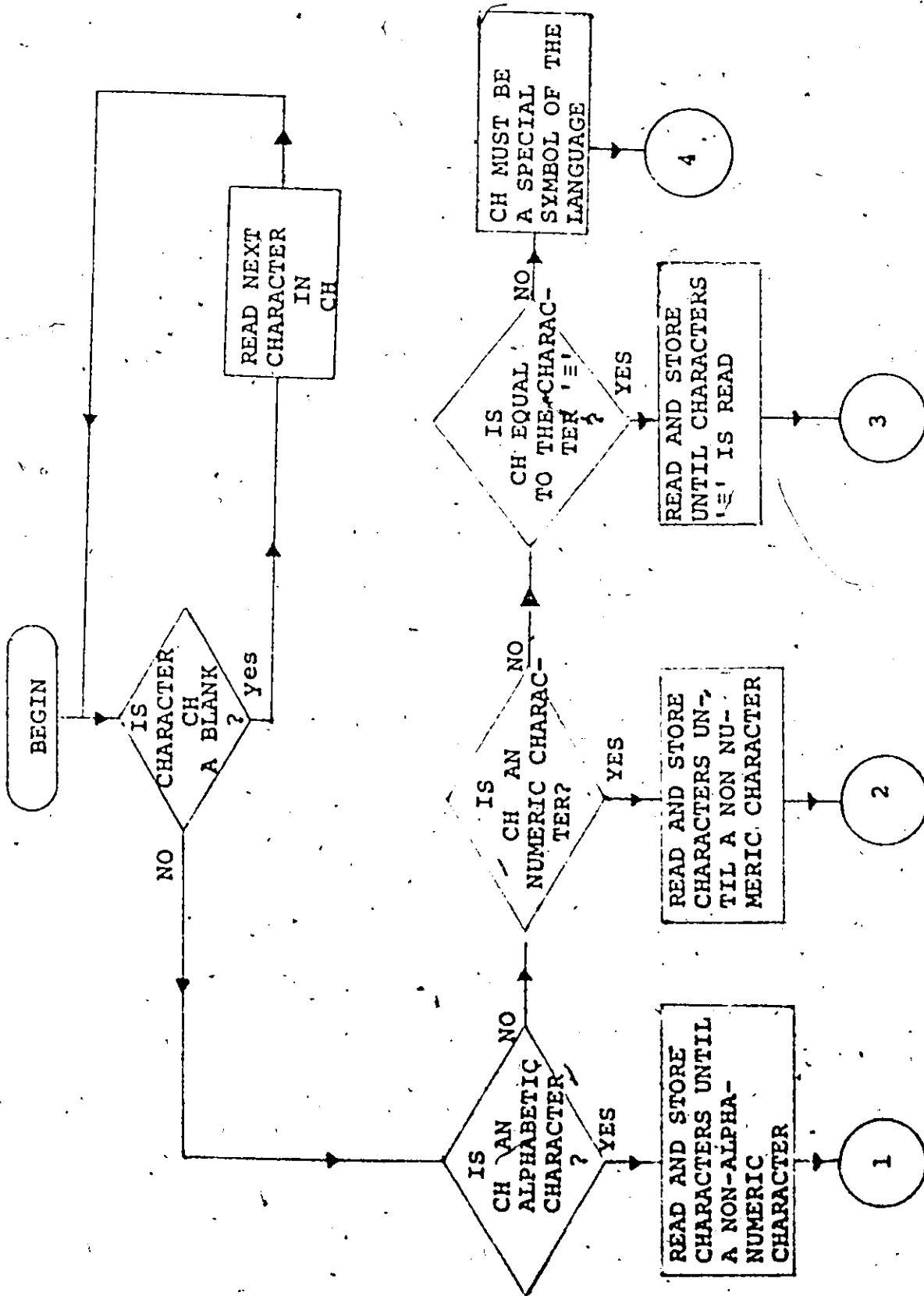
An indication has been given in this paragraph to show why binary search and hash-coded search methods have not been used. The binary search method has the disadvantage that identifier and the reserved words are to be put into alphabetic order. The variant parts of the record describing the objects in the object are of different lengths (in no. of bits) and packing would be lost as the identifiers are moved from one

place to another as is necessary in binary search method. This will, naturally, cause a lot of troubles. The hash-coded search seems to be better but is more complicated and as such could be time-consuming.

The linear search method, compared to binary search and hash-coded search methods, seems to be a bit slow but much simpler. In fact, a proper study should be done with the tables of reserved words and the context table used in HPCOM compiler, to see which of these search methods is better and more efficient.

#### 2.4 Lexical Scanner

The simplest part of a compiler is the lexical scanner used to scan the text (or the source program). In PL/2100, the word-delimiters (or reserved words e.g. begin) are represented like identifiers (without escape characters) and must be interpreted by the scanner. Identifier (and also numbers) are therefore, considered as basic symbols. It is a source-oriented scanner and has made full use of recursive definitions of procedures possible in PASCAL (the language in which the compiler has been written). The main controls of the scanner is given in fig. 2.4 and a listing of the program in Appendix D.



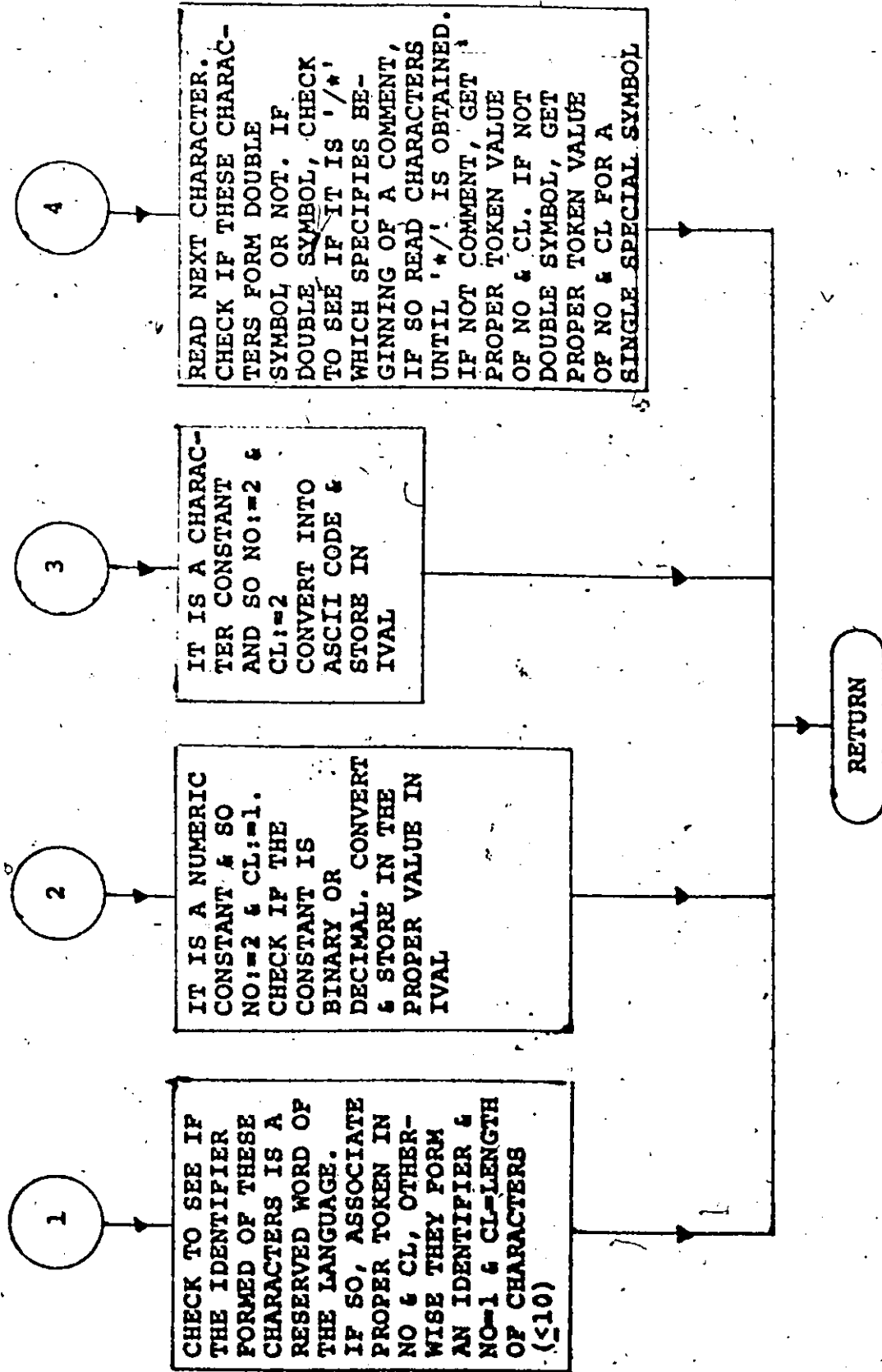


fig. 2.4 Main control section of lexical analyser

## 2.5 Syntactic Analysis of the Source Text

The design of the programming language 2100 is based on syntax allowing the application of a reasonably simple and perspicuous analysis technique. The method chosen was first used by Conway<sup>(C1)</sup> who called it the "SEPARABLE TRANSITION DIAGRAM" technique. This method has also been used by Wirth<sup>(W3)</sup> in the design of the PASCAL compiler. This method has been chosen for the design of the compiler (HPCOM) for PL/2100.

The syntax of the language is presented as a finite set of pseudo-finite state recognizers. The attribute "pseudo" is due to the fact that some of the basic symbols to be recognized are replaced by sentences recognizable by one of the members of this set. The recognizers may thus activate each other, possibly causing recursion. This top-down parsing technique has the following advantages:

- i) Every single recognizer can be presented by a lucid finite graph directly representing the recognizer's program.
- ii) If a programming system is available offering recursive procedures, no explicit stack mechanism need be programmed. (It is important to note that, as PASCAL has capability of recursive procedures, this has been done in HPCOM).
- iii) Program paths introduced to handle syntactic errors can be represented in the syntax graph.

It is important to note that syntax analyser is top-down.

The syntax analyser has recursive procedures for nonterminal symbols of the language and these procedures parse phrases for the nonterminals. The procedures are told where in the program to begin looking for a phrase for all the nonterminals; hence syntax analyser is goal-oriented or predictive. This method is known as recursive descent method. The syntax analyser uses a bottom-up parsing principle to obtain input (viz integer tokens) via the source oriented lexical scanner (which associates the tokens with the different symbols and identifiers of the language).

The dependence relationships between the various main procedures of the syntax analyser are given in fig. 2.5.

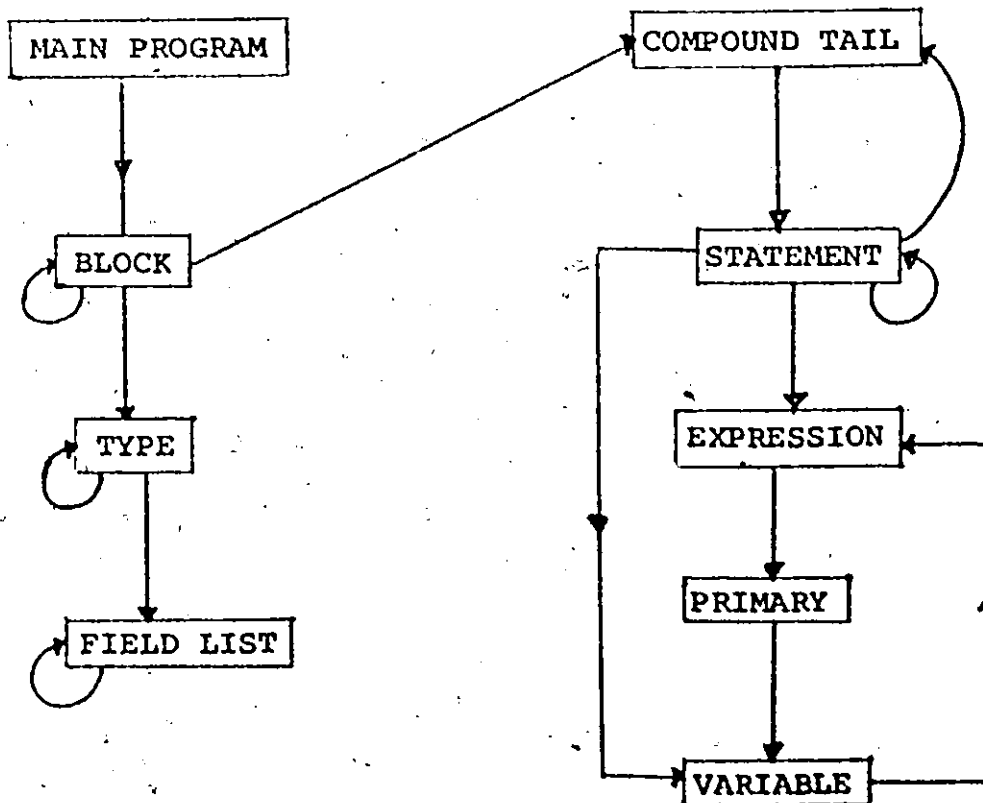


fig. 2.5 presents the block diagram of the syntax analyser



The detailed syntax of PL/2100 in terms of 'transition diagrams' is given in Appendix E.

## 2.6 Semantic Routines and Code Generators

The advantage of using recursive descent method becomes evident in semantic routines as one can insert code for a phrase anywhere within a procedure, not just at the end of it, when a phrase has been detected. No explicit stack mechanism is necessary to store the parsed phrase. In HPCOM semantic routines and code generators are fused with the syntax analyser which calls them whenever necessary. If coroutines are used, semantic routines and code generators need not be fused with the syntax analyser. The code generators are, of course, dependent on the features of the target machine. Since code generators are very important part of the compiler, they are discussed in detail in the next Chapter III.

## 2.7 Initialization Routine and Fix-up Routines

The initialization routine is the first routine to be called in the compiler and it initializes different variables and the symbol table.

The fix-up routines are called at the end of the code generations and are used to fix the codes up namely, placing the proper branch addresses, allocating the constants at the end of the data stack and assigning the temporaries used.

## 2.8 Routines Used to Produce Relocatable Binary in Proper Format

These routines are used to put the code in a form acceptable to the relocatable loader of the HP/2100A machine.

## 2.9 Overall View of the Different Parts of HPCOM

In this section, the following block diagram has displayed the various parts of the compiler as being called from the main procedure.

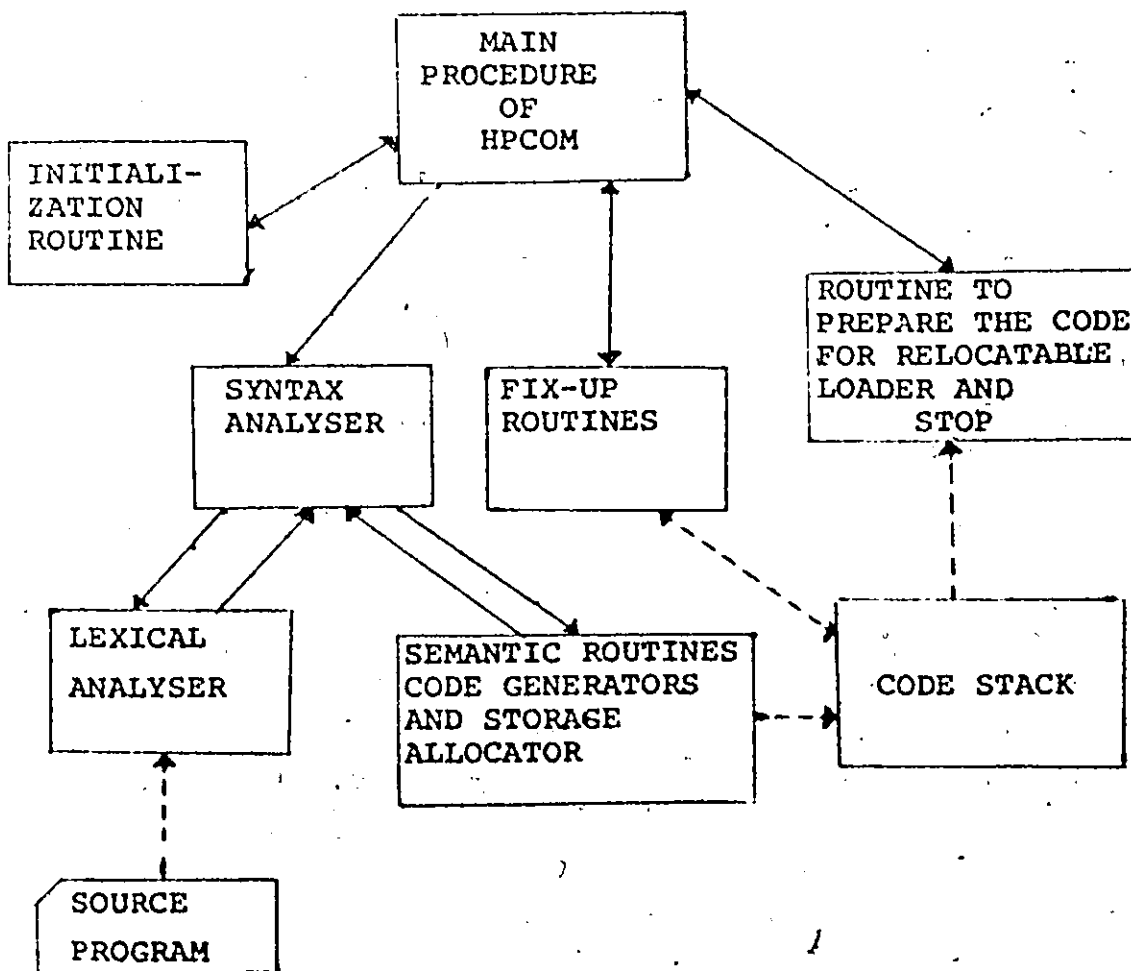


fig. 2.9 Displays Different Parts of HPCOM Compiler

## 2.10 Conclusion.

The language PASCAL has been used to write the compiler HPCOM. The reasons are that the PASCAL compiler is fast and that the language PL/2100 closely resembles PASCAL in data structure. Not much effort need be spent to write the compiler in PL/2100, because the features available in both PASCAL and PL/2100 have been used (except the powerset or (set of) type which is not available in PL/2100). This would be advantageous to a person who wants to bootstrap the HPCOM compiler to HP/2100A machine.

The design of the compiler is governed by the fact that the compiler should produce efficient code. Efficiency, in the case of HPCOM, refers primarily to space (required to store the object code in the target machine), not time. This is because the HP/2100A has a small memory and as such a program in PL/2100 should not occupy too much space in the core of the target machine (HP/2100A).

## CHAPTER III

### CODE GENERATION

#### 3.1 Code Generators

This part of the compiler is machine dependent i.e. their structure and the algorithms used depend on the target machine. The following diagram (fig. 3.1) gives a view in the organization of the code emitter. Needless to say that these routines check syntax and get the semantics as well. (This is apparent from the discussion in Chapter I).

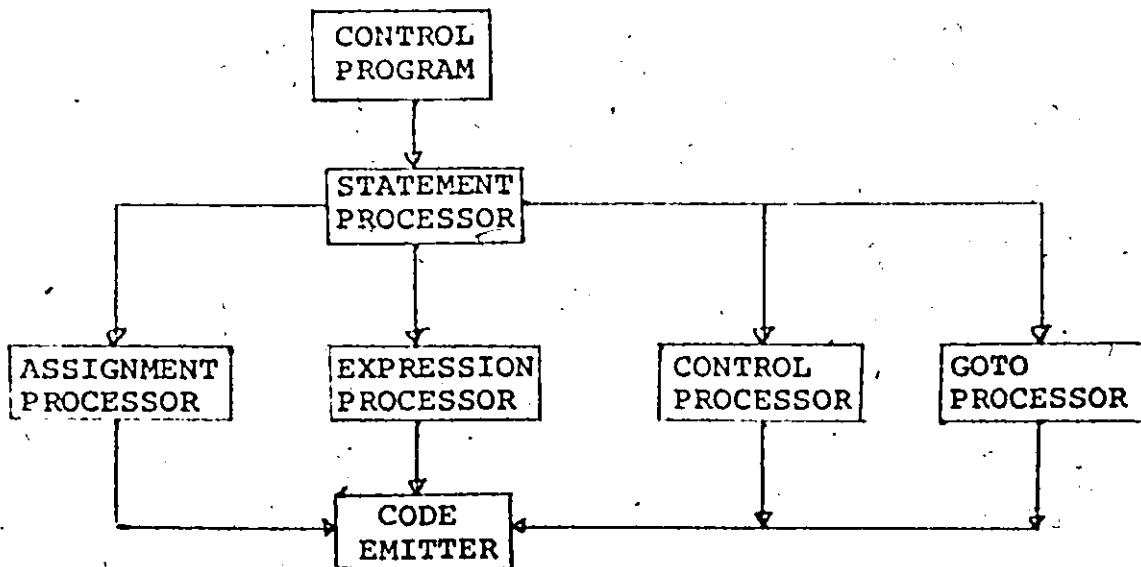


Fig. 3.1 Code Emitter Organization

#### 3.1.1 Control Program

The control program is driven by syntactic productions recognized by the parser. Whenever a syntactic entity is recog-

nized, the control program calls the statement processor which, in turn, calls the module responsible for the code emission of the class of productions to which the recognized production belongs. The compiler has extensive type checking capabilities, and it is the responsibility of the different processors to initiate type checking where necessary.

### 3.1.2 Expression Processor

Whenever an arithmetic expression is recognized, the statement processor activates the expression processor. The function of the arithmetic expression processor is to prepare all the operands of the expression for processing by generating proper codes for their values (at run time) as required. Thus the expression processor calls other modules (not shown in fig. 3.1) to get the address of the operands. Once the operator is recognized it finds out if more operands are necessary for the operator. The processor parses from left to right with equal precedence for all the operators. Once a meaningful sentence is recognized, it generates codes which yield the value of the expression.

There are only two hardware accumulators namely, A- and B-registers for the HP/2100A machine. Thus it might be necessary to store the partial value of the expression (during runtime) and hence temporary memory locations are necessary to store these values. At compile time, the arithmetic processor generates proper codes and assigns the locations

necessary for this purpose. An attempt has been made to optimize the number of temporaries. The maximum number of temporaries necessary for an expression are also the maximum number of temporaries for the whole program, as these temporary locations are made free after an expression is executed (during runtime). The arithmetic processor calls two routines viz. LDTMP (load from the temporary) and STTMP (store into temporary) for this purpose. It also keeps a table of pointers so that fix-up routines can, properly, assign the locations.

### 3.1.3 Control Section

The control section processor handles the code generation for all the control structures of the programming language 2100. Both selections and exits from a loop are managed by the compile time tables. These tables are updated by the control section processor, and whenever all pertinent information is available, these tables are used to emit fix-ups and branch tables. Both the management and the use of the tables, are machine independent.

### 3.1.4 Assignment Processor and GOTO Processor

Assignment processor is called whenever a value of an expression is to be assigned to a variable.

Whenever an unconditional branching is recognized in a program, the GOTO processor is activated. It updates and keeps track of the branching table (at compile time) so that proper codes are generated. Both forward and backward jumps are allowed.

### 3.1.5 Object Code Emitter

The object code emitter is responsible for the production of HP/2100A object code. The produced code is placed into a code stack containing all the codes generated for a program. The code stack provides the compiler the capability of peephole optimization but no attempt has been made to optimize the code produced.

### 3.2 Code Emitted for Different Statements and Types of Operands

In this section the code emitted for expressions, different statements and operands of different types will be discussed.

#### 3.2.1 Expression Evaluation

The format of arithmetic operations on HP/2100A insists that one of the operands of the expression be located in one of the two accumulators viz A- and B-registers. The evaluation of expressions often results in values which must be stored temporarily. These values are stored in temporary memory locations, not in A- and B-registers as these registers could be used in evaluating the expressions. A great deal of effort has been spent on HPCOM to make it allocate temporary locations efficiently. These are not allocated dynamically at run time.

To evaluate an arithmetic expression, the first operand is loaded in the A-register (by convention, the first operand is always loaded in the A-register and also the result of the expression is always in the A-register) and the HP arithmetic

operations (viz. MPY, ADA, etc.) are used to evaluate the expression. The code generated for different arithmetic operations, is given in terms of HP assembly language instructions (whose uses and meanings are described in Appendix B).

### 3.2.1.1 Codes Generated for Arithmetic Operators

#### 3.2.1.1.1 Addition

e.g. 1<sup>st</sup> operand + 2<sup>nd</sup> operand

LDA 1<sup>st</sup> operand /\* Load first operand in the A-register \*/

ADA 2<sup>nd</sup> operand /\* Add 2<sup>nd</sup> operand to the contents of A-register \*/

#### 3.2.1.1.2 Subtraction

e.g. 1<sup>st</sup> operand - 2<sup>nd</sup> operand

LDA 1<sup>st</sup> operand /\* Load first operand in A-register \*/

LDB 2<sup>nd</sup> operand /\* Load second operand in B-register \*/

CMB, INB /\* Take 2<sup>S</sup> complement of the 2<sup>nd</sup> operand \*/

ADA 1 /\* Add contents of B-register to the contents of A-register, the result of subtraction is in A-register \*/

#### 3.2.1.1.3 Multiplication

e.g. 1<sup>st</sup> operand \* 2<sup>nd</sup> operand

LDA 1<sup>st</sup> operand /\* load 1<sup>st</sup> operand in A-register \*/

MPY 2<sup>nd</sup> operand /\* Multiply contents of A register with the 2<sup>nd</sup> operand \*/

TOR 1 /\* Inclusive or contents of B-register with the contents of A-register \*/



It is important to note that the last code viz IOR 1 is important, as the sign bit of the result of multiplication is in B-register. It is implicitly assumed that the result of multiplication is less than  $(2^{15}-1)$  i.e. the 15<sup>th</sup> bit of A-register is always zero, and also that bits 0 to 14 of B-register are zero.

#### 3.2.1.1.4 Division

e.g. 1<sup>st</sup> operand div 2<sup>nd</sup> operand

LDA 1<sup>st</sup> operand /\* load 1<sup>st</sup> operand in the A-register \*/  
 CLB  
 SSA /\* skip next instruction if the sign bit of A-register is zero i.e. the 1<sup>st</sup> operand is positive \*/

CMB, INB /\* 1<sup>st</sup> operand is negative, take 2<sup>s</sup> complement of it and store in B- and A-registers combined \*/

DIV 2<sup>nd</sup> operand /\* Divide the contents of B- and A-register by the 2<sup>nd</sup> operand. The result is in A-register \*/

#### 3.2.1.2 Codes for Relational And Logical Operators

##### 3.2.1.2.1 OR (V)

e.g. 1<sup>st</sup> operand {<sup>V</sup><sub>OR</sub>} 2<sup>nd</sup> operand

LDA 1<sup>st</sup> operand /\* Load 1<sup>st</sup> operand in the A-register \*/  
 IOR 2<sup>nd</sup> operand /\* Inclusive or 2<sup>nd</sup> operand to the contents of A-register. The result is in A-register \*/

##### 3.2.1.2.2 AND (A)

e.g. 1<sup>st</sup> operand {<sup>A</sup><sub>AND</sub>} 2<sup>nd</sup> operand

LDA 1<sup>st</sup> operand /\* Load 1<sup>st</sup> operand in the A-register \*/  
 AND 2<sup>nd</sup> operand /\* And 2<sup>nd</sup> operand to the contents of the A-register. The result is in A-register \*/

### 3.2.1.2.3 Relational operators

e.g. 1<sup>st</sup> operand  $\left\{ \begin{array}{l} \text{LT} \\ \text{LE} \\ \text{GT} \\ \text{GE} \\ \text{NE} \\ \text{EQ} \end{array} \right\}$  2<sup>nd</sup> operand

In cases of the relational operators, first operand is subtracted from the 2nd operand (for convenience) and the result (in the A-register) of the subtraction is checked (a check on the sign bit of the A-register and/or the contents of the A-register is necessary) to see if the relation is true or false, and the result is set to true (1 in the A-register) or false (0 in the A-register).

The following three words of instruction are common to all relational operations:

LDA 1<sup>st</sup> operand /\* Load the 1<sup>st</sup> operand in the A-register \*/  
 CMA, INA /\* Take 2<sup>s</sup> complement of the 1<sup>st</sup> operand \*/  
 ADA 2<sup>nd</sup> operand /\* Add 2<sup>nd</sup> operand to the contents of the A-register. The result of the subtraction is in the A-register \*/

#### 3.2.1.2.3.1 LT(<)

e.g. 1<sup>st</sup> operand {<LT} 2<sup>nd</sup> operand

SSA, RSS /\* skip the next instruction, if the result of the subtraction is not positive \*/

SZA, RSS /\* skip the next instruction, if the result of the subtraction is not zero \*/

3.2.1.2.3.2 LE(<)

e.g. 1<sup>st</sup> operand { $\overset{<}{LE}$ } 2<sup>nd</sup> operand

SSA, SZA /\* skip the next instruction, if the result of the subtraction is not positive \*/

3.2.1.2.3.3 GE(>=)

e.g. 1<sup>st</sup> operand { $\overset{\geq}{GE}$ } 2<sup>nd</sup> operand.

SSA, SZA /\* skip the next instruction, if the result of the subtraction is positive or zero \*/

3.2.1.2.3.4 GT(>)

e.g. 1<sup>st</sup> operand { $\overset{>}{GT}$ } 2<sup>nd</sup> operand

SSA, RSS /\* skip the next instruction, if the result of the subtraction is not positive \*/

3.2.1.2.3.5 NE(≠)

e.g. 1<sup>st</sup> operand { $\overset{\neq}{NE}$ } 2<sup>nd</sup> operand

SZA, RSS /\* skip the next instruction if the result of the subtraction is not zero \*/

3.2.1.2.3.6 EQ(=)

e.g. 1<sup>st</sup> operand { $\overset{=}{EQ}$ } 2<sup>nd</sup> operand

SZA /\* skip the next instruction, if the result of the subtraction is zero \*/

Next two words of code are common to all relational

operations:

CLA, RSS /\* set A register to zero and skip the next instruction. That is to say that the result of relational operation is false \*/

CLA, INA /\* set A register to 1, The result of relational operation is true \*/

3.2.1.3 Bit Operators3.2.1.3.1 IOR

e.g. 1<sup>st</sup> operand ior 2<sup>nd</sup> operand

LDA 1<sup>st</sup> operand /\* load 1<sup>st</sup> operand in the A-register \*/

IOR. 2<sup>nd</sup> operand /\* inclusive OR 2<sup>nd</sup> operand to the contents of the A-register. The result is in the A-register \*/

### 3.2.1.3.2 XOR

e.g. 1<sup>st</sup> operand xor 2<sup>nd</sup> operand

LDA 1<sup>st</sup> operand /\* Load 1<sup>st</sup> operand in the A-register \*/

XOR 2<sup>nd</sup> operand /\* Exclusive OR 2<sup>nd</sup> operand to the contents of the A-register. The result is in the B-register \*/

### 3.2.1.4 Shift Operators

e.g.

operand  $\left\{ \begin{array}{c} \underline{\text{als}} \\ \underline{\text{ars}} \\ \underline{\text{ral}} \\ \underline{\text{rar}} \\ \underline{\text{alf}} \end{array} \right\} \langle \text{factor} \rangle$

for the A-register, where  $\langle \text{factor} \rangle$ , an integer, species the number of shifts to be made.

Similarly for the B-register, we have

operand  $\left\{ \begin{array}{c} \underline{\text{bls}} \\ \underline{\text{brs}} \\ \underline{\text{rbl}} \\ \underline{\text{rbr}} \\ \underline{\text{blf}} \end{array} \right\} \langle \text{factor} \rangle$

For the operator alf (or blf), the factor is determined as a modulo 4 (because 4 alf or blf shifts are the same as no shift).

For other operators, the factor is determined as modulo 16. Following examples will make it clear.

#### 3.2.1.4.1

(a) operand alf 6

At compile time, factor is determined as a modulus of 4 and the result is 2. The code generated is.

LDA operand /\* load operand in the A-register \*/

ALF,ALF /\* make alf shift twice \*/

(b) operand alf 7

Codes generated are

LDA operand

ALF,ALF

ALF

#### 3.2.1.4.2

(a) operand rar 7

Codes generated are

LDA operand

RAR,RAR

RAR,RAR /\* code has been optimized  
in these cases \*/

RAR,RAR

RAR

(b) operand rar 19

The resultant factor is 3 and codes generated are

LDA operand

RAR,RAR /\* 3 RAR shift necessary \*/

RAR

#### 3.2.1.4.3

(a) operand als 18

Codes generated are

LDA operand

ALS,ALS /\* factor = 18 mod 16 = 2 \*/

(b) operand als 5

Codes generated are

LDA operand

ALS,ALS

/\* two ALS shift allowed per

ALS,ALS

instruction word \*/

ALS

#### 3.2.1.4.4

(a) operand ars 17

Codes generated are

LDA operand

ARS

/\* one ARS shift necessary \*/

(b) operand ars 6

Codes generated are

LDA operand

ARS,ARS

/\* operand is shifted

ARS,ARS

by six places \*/

ARS,ARS

#### 3.2.1.4.5

(a) operand ral 36

Codes generated are

LDA operand

ALF

/\* resultant factor is 4 and 4 RAL shifts are equal to one ALF shift \*/

(b) operand ral 3

Codes generated are

LDA operand

RAL,RAL /\* 3 shifts necessary \*/

RAL

#### 3.2.1.4.6

Code generated for the shift operators for the B-register is similar except the the result is in the B-register.

for example

operand <shift operator> factor

Since the value of an operand is always in the A-register, we generate codes as follows,

LDA operand /\* load operand in the A-register \*/

STA 1 /\* store the value of the operand from the  
A-register in the absolute location 1 which  
is the B-register \*/

This is followed by similar codes for shift operators for the B-register. The result is in the B-register.

#### 3.2.2 Assignment statement

e.g. <variable>: =<expression>

The result of expression is always in A register.

Hence code generated is,

STA variable ad /\* store the contents of A-register  
in location for the variable \*/

3.2.3 IF statement

IF { Boolean expression } THEN {statement} ELSE {statement}

The result of the necessarily boolean expression is either false (value 0 in A) or true (value 1 in A). Hence codes generated are:

```

    for IF {expression} THEN {statement};
    SZA,RSS /* skip next instruction,if the result of boolean
              expression is true (value 1) */
    JMP LAB1 /* jump to location LAB1 . if the result
              of boolean expression is false (value 0) */.
    .
    .
    .
    .
    .
    .
    LAB1 NOP /* A no operation instruction */

```

Codes generated for

```

    IF {expression} THEN {statement} ELSE {statement}
    SZA,RSS /* if result of expression is true, we go to
              THEN part */
    JMP,LAB1 /* jump to else part of if statement */
    .
    .
    .
    .
    .
    .
    JMP,LAB2 /* jump around else part of the statement */
    LAB1 NOP /* else part starts here */
    .
    .
    .
    .
    LAB2 NOP /* A dummy no operation instruction used to know
              during compile time, where to jump around
              else part */

```



### 3.2.4 REPEAT statement

e.g. REPEAT

```

Statement{s}
UNTIL {expression is true}
LAB1 NOP      /* GENERATES DUMMY NO OP INSTRUCTION */
:            /* code generated for statement{s} */
:            /* code generated for boolean expression
:            Result 'true' (value 1) or 'false'
:            (value 0) is in A */
SZARSS       /* skip next instruction, if result of
              boolean expression is true */
JMP LAB1     /* result of expression is false, repeat the
              REPEAT loop */
NOP          /* Another dummy no operation instruction */

```

### 3.2.5 WHILE statement

e.g. WHILE <expression> DO <statement>

```

LAB1 NOP      /* start of WHILE loop */
:            /* code for expression, result true or false
:            is in A-register */
SZARSS       /* skip next instruction if the result of
              expression is true */
JMP LAB2     /* jump out of the while loop, the result
              of expression being false */
:            /* code for statement */
:
JMP LAB1     /* GO REPEAT WHILE LOOP */
LAB2 NOP     /* END OF WHILE LOOP */

```

### 3.2.6 GOTO statement:

e.g. GOTO <integer>

```
JMP LABEL /* JMP TO THE LABELLED LOCATION */
```

### 3.2.7 FOR statement

```

          e1                      e2
          +                      +
FOR <identifier> := <expression> { TO } <expression>
                        { DOWNTO }

```

```
code for 1st expression DO statement
```

```
STA <identifier> /* STORE the value of the first
                  expression in the location of
                  the identifier */
```

```
/* code for 2nd expression */
```

```
STA <TEMP> /* store the result of 2nd expression in
            a known location */
```

```

LAB1 { LDA <identifier> /* load value of identifier in A */
      CMA, INA /* take 2S complement */
      ADA <TEMP> /* calculate e2-e1 */
      SSA, SZA /* if e2-e1 is -ve, for loop is
                finished
      JMP LAB2 /* jump out of for loop */
      . . . /* statement codes */
      LDA <identifier> /* load value of identifier in
                        the A-register
      INA /* INCREMENT IT */
      STA <identifier> /* store the new value of e1 in
                        identifier */
      JMP LAB1 /* GO BACK TO CHECK FOR-LOOP */
LAB2 NOP /* dummy no operation to go out
          of for loop */

```

For 'DOWNT0' the relation  $e1 \geq e2$  must hold for the loop to continue. Hence the difference in coding would be

```

LAB1 LDA <TEMP> /* load e2 */
      CMA,INA    /* complement e2 */
      ADA <identifier> /* calculate e1-e2 */

```

### 3.3 Data representation

We now discuss the internal representation of the PL/2100 data types, and the way in which operations on them are implemented. One of the deficiencies of the HP/2100A instruction set is the lack of facility whereby structured data types may be easily manipulated. These deficiencies are the following.

- 1) The HP/2100A instruction set allows the manipulation of only 8 bit bytes (in case of character) and/or maximum 16 bit word with one instruction. The PL/2100 language however allows the manipulation of structured types (arrays, records) which in the majority of cases are greater than 16 bits in length. No problem arises as long as those subfields of the structured types are being handled that fit into a word or less. Restricting the data structures to those whose lengths are at 16 bits would, however, destroy one of the most elegant features of the PL/2100.
- 2) The HP/2100A does not have convenient instructions for address calculation. An address may be calculated through one of the accumulators at a time. When dealing with data types which involve either implicitly or explicitly a variable offset,

from the base address of a variable of the data type (arrays), the use of an index register would be most useful. HP/2100A machine has no hardware index register as such (but indirect addressing is possible through one bit in the instruction word).

### 3.3.1 Numeric types

Numeric types in HP/2100 implementation are those whose internal representation is a sixteen bit word.

All numeric operations allowed by the PL/2100 system language, except modulo, are implemented. Arithmetic operations are evaluated in A- and B-register and result is always in A-register (unless stored).

### 3.3.2 Scalar types

The values of the programmer defined scalar types are ordered by the position of the identifier names in the defining list. Internal values are assigned in order starting with zero, and the base 2 logarithms of the largest value determines the number of bits required. The symbolic constants  $C_0, C_1, \dots, C_n$  of a scalar type, are represented internally as  $0, 1, 2, \dots, n$ .

### 3.3.3 Array types

An array is a structure consisting of a fixed number of components all of the same type. The dimensionability of an array is specified in the array declaration.

In PL/2100, the bounds of an array must be constant, and known at compile time. As a result, we can allocate (at

compile time) to a variable of the array type the amount of storage required to hold all the elements of the array. The address of an array element consists of the base address of the array offset by the index of the element into the array.

The effective relative address (relative to the top of the data stack) of an array is calculated as follows:

Let us assume that the array is one dimensional, and the lower and upper bounds of the array are denoted by LO and HI respectively. The size of each element of the array is denoted by SIZE (the size is the number of HP words required to hold an array element of a particular type, e.g. if an array element is integer, SIZE = 1). The base address of the array variable is given by, say, DPLMT. The effective address of an array element is calculated according to the following formula,

$$\text{effective address} = \text{DPLMT} + (\text{value of index} - \text{LO}) * \text{SIZE}.$$

For a multidimensional array, the effective address can be calculated similarly. The HPCOM compiler is capable of handling multidimensional arrays.

We have given an example of the code required to access an array element. The declaration for an array variable is given below:

```
VAR
```

```
A: ARRAY [1..100] OF INTEGER;
```

The above declaration reserves storage for 100 members (16-bit integer) array named A and declares the variable index to take a value from 1 to 100.

To access an array element, we emit the following sequence of code:

```

CLB          /* Clear the B-register */
STB <temp>   /* STORE the contents of B-register in a fixed
              location <temp> known at compile time. This
              location will contain the address of the
              array element */
LDA <index>  /* Load the value of the index in the A-register;
              if index is a constant, it can be calculated
              at compile time */
MPY <size>   /* <size> is 1, in the particular example given
              above, and is known at compile time */
ADA <disp>   /* <disp> is the displacement from the base
              address and is = base address - LO * <size>.
              This is calculated at compile time */
ADA <temp>   /* contents of location <temp> is added, this
              allows multidimensional arrays to be compiled
              as well */
STA <temp>   /* store the effective address of the array
              element in the known location <temp> */

```

#### 3.3.4 Record types

A record type is a structure consisting of a number of components, possibly of different types. The record definition specifies for each component of the record, called a field, the type of the field, and an identifier which denotes it.

A record type may have a variable format where one of the fields of the record, called the tagfield, indicate the chosen format of record at any time. For example,

##### TYPE

```

person = RECORD name, firstname: ARRAY [1..10] OF char;
          age: integer;
          married: boolean;

```

```

CASE s: sex OF
    male: (enlisted, bold:boolean);
    female: (pregnant:boolean;
            size: ARRAY [1..3] OF integer);

```

END

Since PL/2100 does not allow the definition of data types whose length may change at run time, we know at compile time the displacement of all subfields from the start of the record. As a result, no runtime index calculation from the start address is necessary. The address of the field of the record is the base address of the record variable, plus the displacement of the field from the start of the record. The maximum amount of storage allocated to a variable is the sum of the fixed part of the record and the largest variant.

#### 3.4 Synonym Declaration

Through the synonym declaration, more than one simple variable (i.e. not of structured type) can occupy the same location at the run time.

In this section we present how these simple 'synonymous' variables are allocated to the same location. A pointer is used to link the synonymous variables circularly.

For example, if the synonym declaration is, namely,

SYN

A = B,C,D;

the identifiers A,B,C,D are connected as given in fig. 3.4.1.

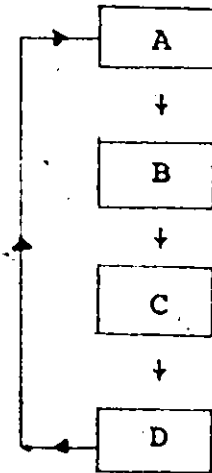


fig. 3.4.1 circular linkage of synonymous variables

If the above declaration is followed by,

B = E,F,G;

the linkage is changed as given in fig. 3.4.2

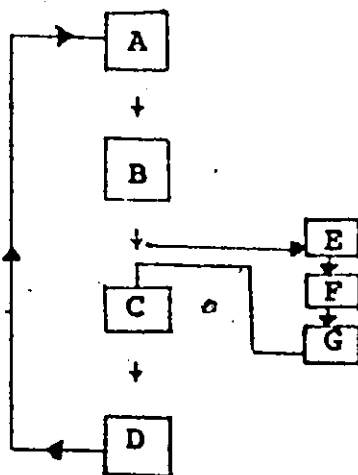


fig. 3.4.2 The changed circular linkage of synonymous variables

A boolean pointer is associated with each of these synonymous identifiers, and is false and remains false until the identifiers are allocated to a location.

All the synonymous identifiers are allocated to an address (relative to the top of the data stack) of the synonymous



variable encountered first in the variable declaration part.

For example, if in the variable declaration part, we have

```
C : integer;
```

and its relative address is 3, then all the synonymous variables declared above will be given the address 3 and the boolean pointers will be set to true. These synonymous variables will be of type integer.

An attempt to allocate space to any of these synonymous variables (which have been allocated to a location already) will cause a compile time error.

### 3.5 Procedure

Though HPCOM compiler at the present stage is not capable of compiling procedures, an indication of how it could be done is given in this section.

In the case of the PASCAL compiler, every procedure has associated with it a data segment consisting of a header and the local data space of the procedure. The data segments are linked by two chains namely, static and dynamic links, respectively <sup>(W3)</sup>.

The PASCAL implementation report <sup>(W3)</sup> suggests that the base addresses of all active data segments (those that may be accessed from the presently executing procedure) be stored in a display. The display would then be contained in hardware register for quick access. But HP/2100A machine has only two hardware registers viz A- and B-registers and this

method is not suitable. Another method used in the implementation of ALGOL for the Burroughs 5500 was considered. This method has also been used in the implementation of the SUE compiler<sup>(K1)</sup>. In this method two pointers are stored in two registers; these two pointers point to data segments of global and the most recently activated procedures. The static link is stored in another register. Again this method would be unacceptable as the HP/2100A machine has two registers (viz A- and B-registers) which are used, mostly, for arithmetic calculations.

The suggestion put forward here, is to use complete display method as has been implemented in PASCAL compiler (for simplicity and convenience) and to store the information at the top locations of the data stack of the main procedures. These locations are known at compile time and can be set aside for this purpose only. These locations will act as a set of working registers. The disadvantage of the method is that it requires memory access quite often whereas the advantage is that a compiler-writer can have as many nesting of procedures as he likes, by allocating sufficient number of working registers.

### 3.6 Arrangement of the Code Stack for the Main Procedure

In this section, the following block diagram presents the arrangement of the codes as generated (along with the locations kept aside for variables, constants used in the program and temporaries used for expressions).

TWO WORDS USED: ONE FOR THE LOAD POINT AND ANOTHER FOR SKIPPING THE DATA STACK, viz Load point → NOP JMP LI
TWO WORDS FOR STORING BUFFER ADDRESS AND BUFFER LENGTH FOR I/O ROUTINES
ONE WORD FOR STORING THE UPPER BOUND OF <u>FOR</u> LOOP
TWO WORDS FOR STORING THE ADDRESS OF AN ARRAY ELEMENT
AS MANY WORDS AS NECESSARY MAY BE RESERVED FOR STORING ADDRESS OF ALL ACTIVE DATA SEGMENTS OF PROCEDURES
LOCATIONS RESERVED FOR VARIABLES DECLARED IN THE MAIN PROCEDURE
OBJECT CODE OF THE MAIN PROGRAM WILL RESIDE HERE
ONE WORD FOR EACH CONSTANT USED IN THE MAIN PROGRAM
MAXIMUM NUMBER OF TEMPORARIES USED IN THE MAIN PROGRAM

} NOT  
YET  
IMPLEMENTED

fig. 3.6 displays code stack of a PL/2100  
main procedure

## CHAPTER IV

I/O IN HPCOM, INTERFACE WITH OPERATING SYSTEM  
AND SEGMENTATION4.1 Input/Output (I/O)

In order for a programmer to communicate with the computer, the computer is, normally, provided with external INPUT/OUTPUT devices. The HP/2100A machine is provided with several of these devices. These are as follows:

<u>device</u>	<u>function</u>	<u>logical unit numbers</u>
Teleprinter	Input/output	1
Teletype	Input/output	7
Line Printer	Output	8
Card Reader	Input	5

The logical unit number is associated with each device and this number distinguishes one device from the other, so that the machine knows which device it should read from or write onto.

The HP/2100A machine, at the present installation, is provided with the Disc Operating system (DOSM) which can perform input and output operations using EXEC calls.

4.1.1 Read/Write Calling Sequence in HP Assembler Language

A typical calling sequence to transfer information to or from an external I/O device is given below in HP/2100A assembler language.

```

      :
      :
      : EXT EXEC
      :
      : JSB EXEC (Transfer control to DOS-M)
DEF RCODE (REQUEST code) } DEF **+5 (Point of return from DOSM)
DEF CONWD (Control      }
  Information) } DEF BUFFER (Buffer Location)
      :
      : DEF BUFL (Buffer Length)
      :
      :
RCODE      DEC      1 (or 2)      (1 = READ, 2 = WRITE)
CONWD      OCT      conwd          (described later)
BUFFER     BSS      n              (Buffer of n words)
BUFL       DEC      n (or -2n)     (same n; words (+) or character (-))

```

#### 4.1.1.1 CONWD

The conwd, required in the calling sequence, contains the following fields:

$\emptyset$	$\emptyset$	W		K	V	M	LOCAL UNIT #
15	14	13	12 11 10 9	8	7	6	5 4 3 2 1 0

#### FIELD

#### FUNCTION

W

If 1, tells DOS-M to return to the calling program after starting the I/ $\emptyset$  transfer. If W =  $\emptyset$ , DOS-M waits until the transfer is complete before returning.

K

Used with keyboard input, specifies printing the input as received if K = 1. If K =  $\emptyset$ , "no printing" is specified.

V

Used when reading variable length records from punched tape devices in binary format (M=1, below). If V =  $\emptyset$  the record length is determined by the word count in the first non-zero character which is read in.

M

Determines the mode of data transfer.  
If  $M = \emptyset$  transfer is in ASCII  
character format, and if  $M = 1$ ,  
binary format.

#### 4.1.1.2 'Conwd' used in the HPCOM compiler

For all the I/Ø devices,

$W = \emptyset$ , i.e. DOS-M waits until the transfer is complete. The reason is that the execution of the latter (often a read statement, for example) part of the program may depend on the data received from the input.

$K = \emptyset$  or 1 i.e.  $K$  could be specified 1 if printing the input is required (in keyboard mode only).

$V = \emptyset$  i.e. record length is determined by buffer length. In the HPCOM, a variable buffer length has been used as one does not need to print the whole buffer every time. At compile time the maximum buffer length is fixed to 72 HP words.

$M = \emptyset$  i.e. All transfers are made in ASCII character mode.

The following table displays the conwd used for different I/Ø devices.

Table 4.1.1.2 displays 'conwd' for different I/Ø devices

Device	conwd INPUT		OUTPUT
	printing of the input	no printing of the input	
Teleprinter (oscilloscope)	401 (octal)	1	1
Teletype	407 (octal)	7	7
Card Reader	-	5	-
Line Printer	-	-	8

#### 4.1.2 I/O Routines

Two I/O routines (one is GET which gets input from a particular device and the other is PUT which puts output on to a particular device) are written in the HP assembly language. These routines read input into the buffer or write the buffer on to a output device

Since the programs obtained from the compiler (HPCOM) would be working under the Disc Operating Systems (DOSM) of the HP/2100A, the routines GET and PUT are written keeping this in mind. The GET and PUT routines use the EXEC calling sequence for input and output.

Different information necessary for input and output are described below.

##### 4.1.2.1 Buffer

The buffer for input/output in a program in PL/2100 is allocated by the compiler HPCOM.

##### 4.1.2.1.1 Buffer Address

The buffer address is known at the compile time and is stored at the top of the data stack. This address is passed through the B-register into the GET and PUT routines and is immediately stored on entering the GET or PUT routine.

##### 4.1.2.1.2 Buffer Length

The maximum buffer length (as specified by the compiler) is 72 HP words (i.e. 144 ASCII characters because two ASCII characters may be stored in a single HP word).

For input the buffer length is fixed and is 72. The DOSM returns from the input mode when line feed for a particular device is read.

For output the buffer length may be variable (but  $\leq 72$ ) and the DOSM returns from the output mode after the buffer of a particular length has been written onto a particular device specified in the program.

#### 4.1.2.2 Logical Unit Number

The logical unit number of a particular device can be provided in a PL/2100 program. This has been described in the section 4.1.1.2.

#### 4.1.3 Routines to Convert ASCII Characters to Integer and Vice Versa

Since input/output from or onto a particular device is in ASCII character mode two routines ALLOC and ALLOK have been used to convert ASCII character to integer and vice versa.

##### 4.1.3.1 ALLOC

This routine is associated with the output routine PUT. This routine gets the value of a variable through the A-register. A pointer to the most recent vacant place in the buffer (initially the pointer points to the top of the buffer) is passed through the B-register. The routine ALLOC converts the integer into proper ASCII character code and allocates it to the buffer. A pointer pointing to the vacant place in the buffer is passed back to the main routine through the



B-register and is stored at a reserved place in the data stack and can be used to allocate the value of the next variable (in the same WRITE statement) in the buffer. Also is passed the length of the buffer for a particular WRITE statement. This information can be used by PUT routine.

After an output operation, the buffer is made free so that it can be used for further output or input.

The figure below gives an indication of the use of buffer pointer. For example, let us assume that we want to output two integer variables A and B.

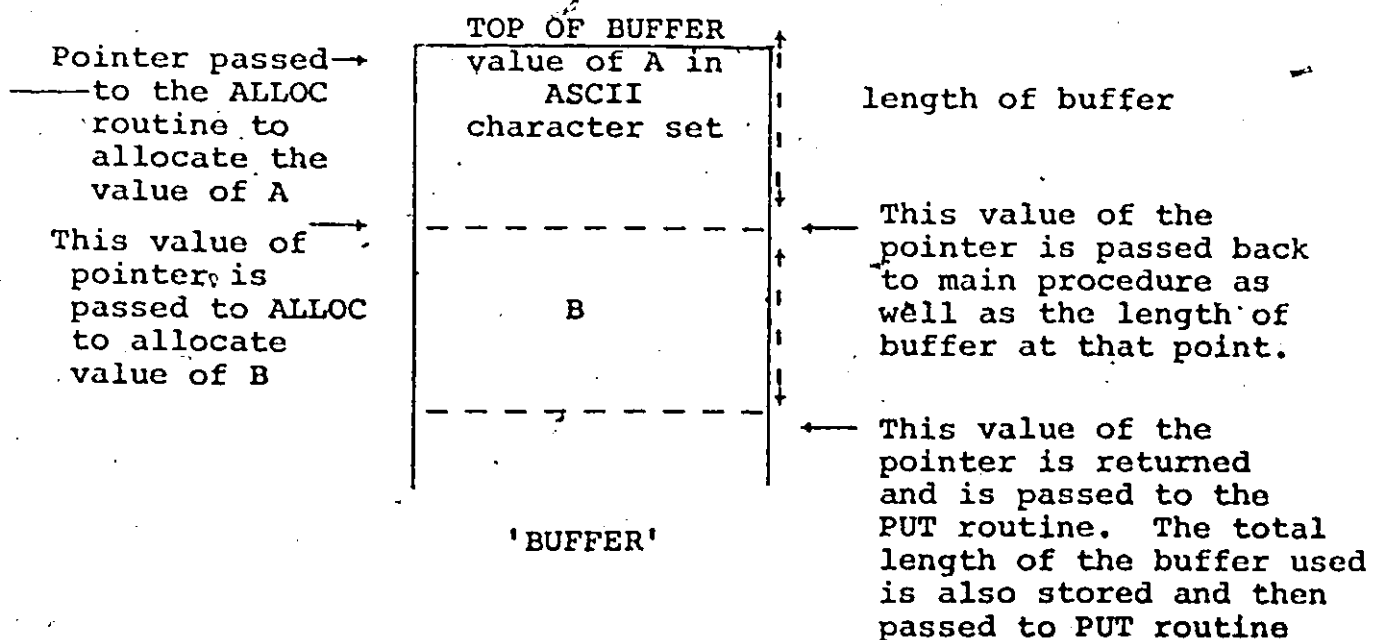


fig. 4.1.3.1 displays the use of buffer pointer.

#### 4.1.3.2 ALLOC

The routine ALLOC works along with the input routine GET. It converts the ASCII character input to integer value and returns the value in the A-register to be stored in a variable

location. A pointer to the buffer is passed to the routine through the B-register and is used to get different integers.

#### 4.1.4 I/O FORMAT

Only free format is used in both input and output. The free format is indicated by an asterisk (\*) in the READ or WRITE statement.

The input data are all separated by commas (,) and the comma is used to differentiate between one data to another. Any number of blanks could be used between the data.

The output data are all written out followed by one or two blanks. Only free format is used.

The following examples will make these clear:

##### i) Input

If we want to read two numbers 4 and 45, the input should be

bb...b 4 bbb..b,bbb...b 45 b...b,b...b line feed

The blanks (b) are all optional

##### ii) Output

If we want to write two numbers 4 and 45, the output would look like

bb4b45bb

These blanks (b) would be provided by the routine ALLOC.

#### 4.1.5 Code Generated for Read and Write Statement

In this section we describe the code generated by the compiler for READ and WRITE statements

##### 4.1.5.1 Read statement

e.g. READ (401B,\*,A,B);

where

- i) 401B specifies 'conwd' i.e. it means that the input is printed out as received from the device with logical unit number 1.
- ii) \* specifies free format
- iii) A,B are two integer variables.

Codes generated are:

LDA buffer address	/* Load into A the buffer address stored in a fixed location known at the compile time */	
LDB conwd	/* Load into B the value of 'conwd' */	
JSB GET	/* JUMP to the routine GET, to get the input */	
LDB buffer pointer	/* initially it points to the top of the buffer */	
JSB ALLOK	/* GO and GET the integer A from ALLOK, integer is in the A-register; the new buffer pointer in the B register */	
STA A	/* store the integer in the variable location A */	
STB buffer pointer	/* store the new buffer pointer */	} actually not necessary
LDB buffer pointer	/* load the new buffer pointer */	
JSB ALLOK	/* Go and get the integer B in the register A, the buffer pointer is in the B-register */	
STA B	/* store the integer in the variable location B */	

#### 4.1.5.2 WRITE statement

e.g. WRITE (7,\*,A,B);

where

- i) 7 is the 'conwd' i.e. write the values of the variables A and B on the device with the logical unit number 7
- ii) \* specifies free format
- iii) A and B are two integers to be written out

Codes generated are:

```

:
:
CLA          /* The content of the A-register is
              zero */

```

```

STA buffer length /* Initial buffer length is zero */

```

```

LDB buffer address /* Load into B the buffer address;
                   initially it is the top of the
                   buffer */

```

The value of an expression may be calculated here; leaving the result in the A-register

```

LDA A      /* Load into the A-register, the
           value of the variable A */

```

```

JSB ALLOC  /* Returns with the pointer to the
           buffer vacant place in the B-
           register and the new buffer length
           in the B-register */

```

```

ADA buffer length /* get new buffer length */

```

```

STA buffer length /* store the new buffer length */

```

```

LDA B      /* load the variable B into the
           A-register */

```

```

JSB ALLOC  /* put B in the buffer */

```

```

ADA buffer length

```

```

STA buffer length

```

```

LDB buffer address /* load buffer address in the
                    B-register, the A-register
                    contains the buffer length */

JSB  BT           /* write the buffer;

RSS              skip next instruction word

DEC 7            on to the device with logical
:               unit no. 7 */
:
:

```

It is important to note that the value of the variable is in A-register, the reason is that by convention the value of an expression is in the A-register and so the value of the expression can be passed to the ALLOC routine without the use of any other instruction. As both A and B are used to pass buffer address and buffer length, the logical unit number (or better conwd) has been passed to the routine PUT as shown above. Any arithmetic expression and/or constant (both integer and character constants) can be written out.

There is no check on the logical unit number and so the programmer would have to know which logical number to use.

## 4.2 Living with the Operating System

The requirements of a PL/2100 program and HPCOM in particular for interactions with their environment (the operating system) take several forms.

### 4.2.1 Interaction of HPCOM with its environment

The compiler HPCOM runs on the CDC-6400 machine under the SCOPE operating system. Since HPCOM is written in PASCAL, the PASCAL compiler residing on a file is called to compile and

load the program HPCOM. Only thing the operating system SCOPE has to do is to attach the file containing the PASCAL compiler and then the PASCAL compiler compiles HPCOM and loads the compiled HPCOM onto the memory. Then CDC-6400 machine executes the HPCOM program (compiler). The input to the HPCOM compiler is a program in PL/2100 and the output, the relocatable binary format (suitable for the standard relocatable loader of the HP/2100A), is written on to a PASCAL file and then punched on to cards. These cards can then be read and loaded onto the HP/2100A memory for execution. The SCOPE operating system is, thus, called on to punch the output from the HPCOM compiler on to the cards.

#### 4.2.2 Interaction of PL/2100 program with its environment

A PL/2100 program runs on the HP/2100A machine which works under the Disc Operating System (DOSM).

Since the PL/2100 program is already in the relocatable binary format and is on the cards, it may either be loaded on the disc by the relocatable loader or be stored on the user file on the disc and be loaded later on. This could be done either through a Prog loadr call or through a STORE directive by the DOSM.

The standard relocatable loader called by the DOSM will relocate the program and writes the core image (the absolute binary program) of the program on to the disc. This may be stored on the user file by the STORE directive.

Then in order to execute the program the RUN directive is used by the DOSM and the program is loaded onto the memory and executed.

The DOSM system handles the EXEC calls (which are the line of communication between an executing program and DOS-M) <sup>(H4)</sup>. One of the operations of EXEC calls is to perform input and output from the external devices. Thus a program with the help of its environment can communicate with the outside world in a given format specified in the program.

#### 4.2.3 Operations done by the DOSM for a PL/2100 program

In this subsection we present what the Disc Operating System (DOSM) does for a PL/2100 program, namely,

- i) Loading already compiled PL/2100 program into the memory of the computer (HP/2100A) and then placing it in execution.
- ii) String input (ASCII character input).
- iii) String output (ASCII character output).

#### 4.3 Segmentations of the Compiler HPCOM

One of early ideas in the implementation of the compiler was to run it on the HP/2100A machine. The idea was rejected (as discussed in section 1 in Chapter I) because of the inconvenience of the implementation. In this section, we look into the idea of what was thought should be done or could be done.

#### 4.3.1 Necessity for segmentation

The HP/2100A machine, at the present installation (at the Department of Applied Mathematics, McMaster University), is provided with 12K memory (1K = 1024 machine words). Out of this core of memory, DOSM (the operating system) takes about 4K for its own routines, so a programmer is virtually left with 8K of memory. On the other hand, the HPCOM compiler for the PL/2100A language takes at the present time, about 55K. (A small subset of PL/2100 might be used to get a smaller compiler but most of the beautiful features of the language PL/2100 would be lost!). Hence to implement this compiler on the HP/2100A machine, it is necessary to segment the compiler.

#### 4.3.2 Segmentation

Once we know why we need to segment the HPCOM compiler, we look into the matter of how segmentation could be done.

##### 4.3.2.1 Physical Segmentation

Under the DISC operating system, the different parts of the same program may be brought into the memory. This is how it is done.

User programs may be structured into a main program and several segments, as shown in figure 4.3.2.1. The main program starts at the beginning of the user program area.



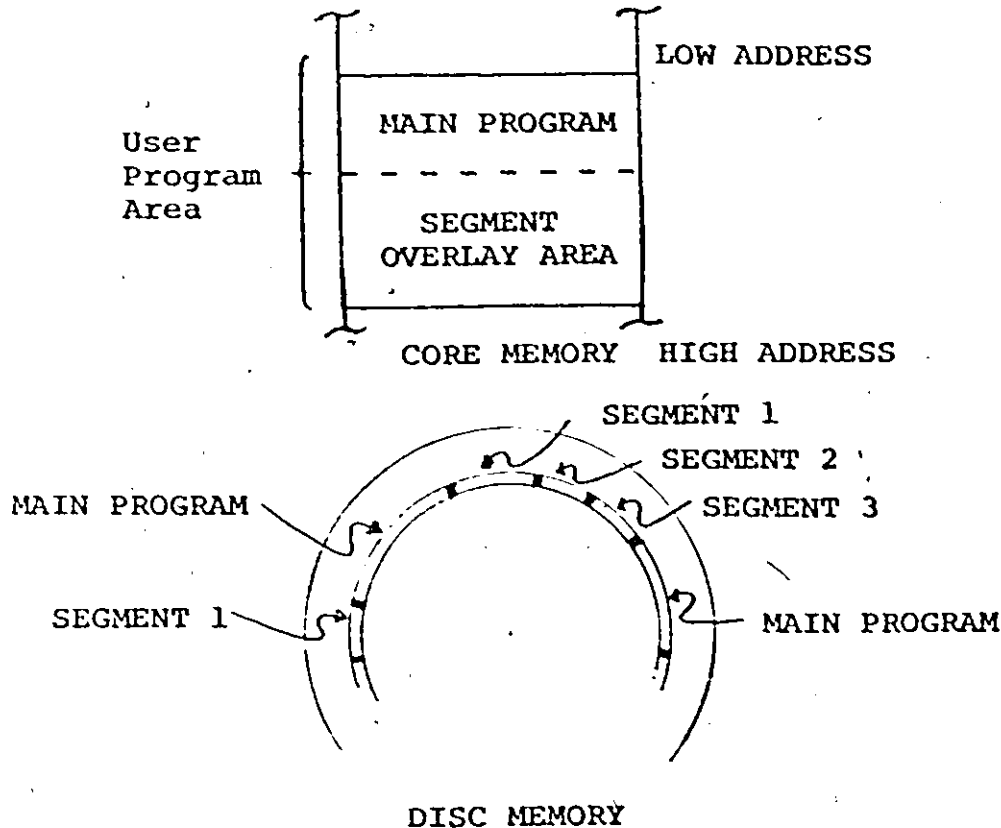


figure 4.3.2.1.a Segmented Programs

The area for the segments starts immediately following the last location of the main program. The segments reside on the disc, and are read into the core by an EXEC call, when needed. Only one segment may be in core at a time. When a segment is read into core, it overlays the segment previously in core. These EXEC calls may be generated by the compiler. This may be done by using a keyword SEG followed by an integer (to specify different segments) and whenever this keyword is encountered, the compiler would generate the proper EXEC calls. Each segment and the main program are distinct through their name (NAM name [,type]) associated with it. The main program must be type 3 and the segments must be type 5.

Each segmented program should use unique external reference symbols, otherwise the loader may link segments and main program incorrectly. The EXEC calling sequence (for loading main program and the segmented program) are given below in the HP assembler language:

```

EXT EXEC
  :
  :
JSB EXEC (transfer control to DOS-M)
DEF * +3 (to 8) (governed by the number of
                parameters)
DEF RCODE (request code)
DEF SNAME (segment name)
DEF PRAM 1 (first optional parameter)
  :
  :
DEF PRAM 5 (fifth optional parameter)
  :
  :
RCODE DEC 8 or 10 (8 = segmented programs,
                  10 = main program)
SNAME ASC 3,xxxxx (xxxxxx is the segment name)
PRAM1 --- (up to 5 words of parameter
PRAM2 --- information are passed to
  : the segmented or the main
  : program)
PRAM5 ---

```

When a main program and a segment are currently residing in core, they operate as a single program. Jumps from a segment to a main program (or vice versa) can be programmed by declaring an external symbol and referencing it via a JMP or JSB instruction (fig. 4.3.2.1.b). A matching entry symbol must be defined as the destination in the other

program. It is the programmer's responsibility to make sure that the correct program is in core before any JMP instructions are executed.

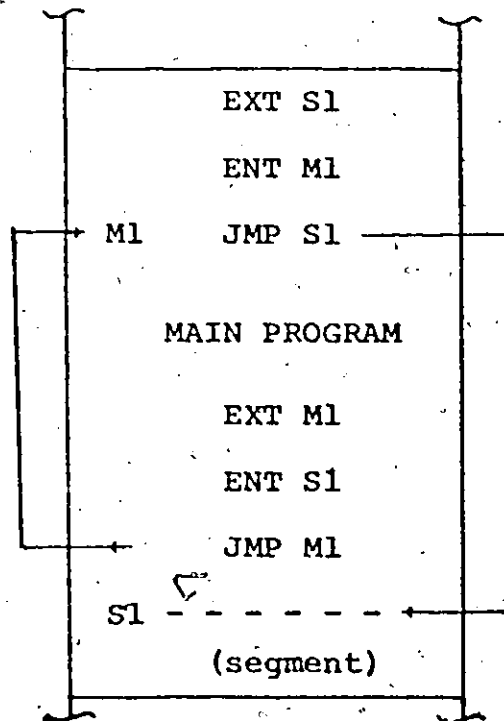


fig. 4.3.2.1.b main-to-segment jump

#### 4.3.2.2. The logical segmentation of the compiler

Since the main program and only one segment could be in the core of memory at a particular time, it is necessary that the information that might be needed from one part of the compiler to another must be kept in the main program which is always present in the core. These informations include the symbol table, tables of reserved words in PL/2100 and all other global variables used in the compiler.

The other segments have been found conveniently from the syntax diagram of the PL/2100. The following block diagram shows the different segments that could be called by the main program and by each other.

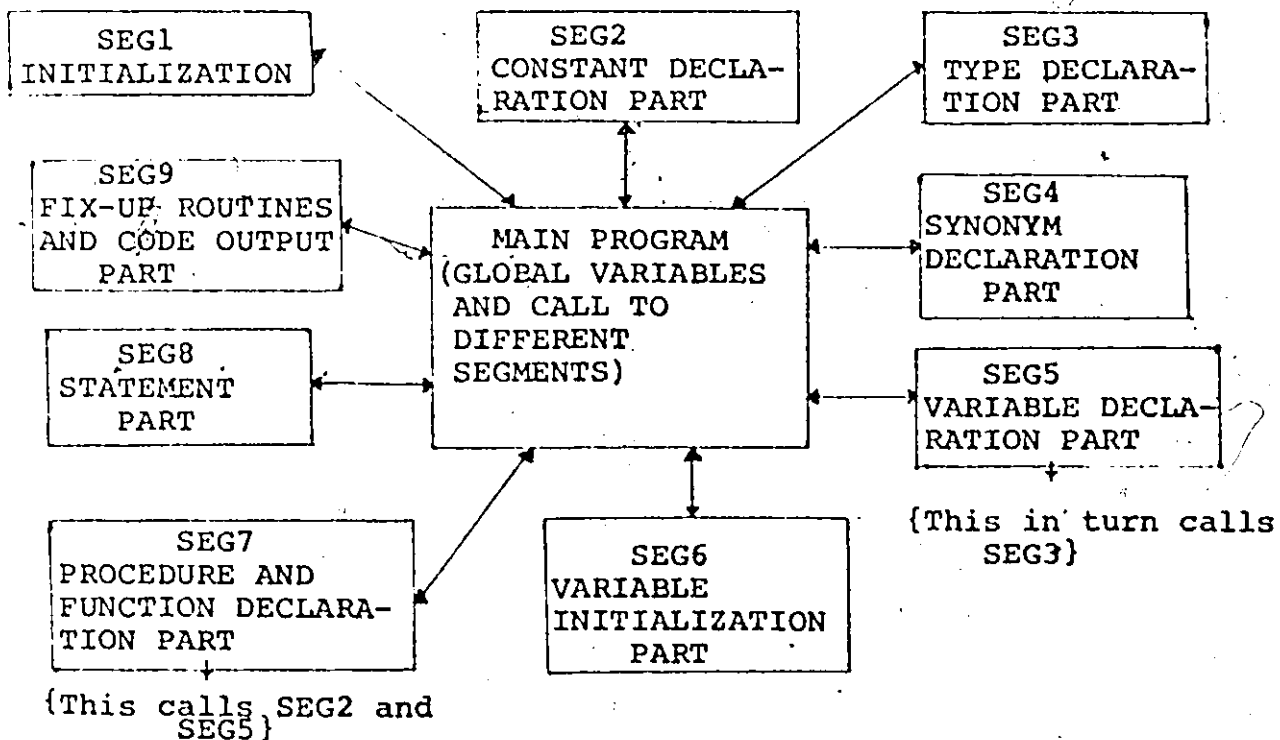


fig. 4.3.2.2 Block diagram showing the segmented part of the compiler

The diagram above gives an indication of how HPCOM might be segmented into different parts. As one segment is overlaid by the other segment, it is necessary to have one segment calling another through the main program. For example, when seg5 calls seg3 it is necessary to jump back to the main program which in turns calls seg3. On returning

from seg3, main procedure calls the seg5 and enters at the point in seg5 where it left before calling seg3. Another possibility is to segment the seg5 further into two parts viz. one before the call of seg3 and another after the call of seg3 and these two segments (viz seg51 and seg52) are called from the main program in the order namely,

```

      :
      Call seg51
      Call seg3
      Call seg52
      :

```

All the necessary information is kept in the main program.

The main program, as one can see, is becoming larger in size and the other parts of the compiler need to be segmented further into smaller sizes. The method seems to be very complicated and cumbersome.

At the present stage as the HPCOM compiler does not produce codes for the procedure, it is not possible to check how big each segment would be.

An estimate of the amount of core required by the symbol table and other tables shows that the main program needs to be about 4.5K long (the size of symbol table is about 2.5K). Thus one is left with 3K for each segment. The compiler needs to be segmented further. For example, the statement part might be divided further into segments containing different statements (like if-then-else, repeat, goto etc.).

Because of the complexity of the segmentation, the method was discarded.

This section on segmentation at least tends to show the difficulty one might find in implementing a large compiler in a machine of small core. The swapping of one segment from another could be time-consuming and the segmentation becomes very complex.

## CHAPTER V

### CONCLUSION AND PROPOSALS FOR FURTHER WORK

#### 5.1 Conclusion

The design of the language PL/2100 allows user-defined data types (which could be infinite in number). It, also, allows the use of some HP/2100A Assembler Instructions as operators; this allows the programmer to do bit-manipulations on the contents of a particular accumulator (namely, A- or B-register). The language allows "synonym declaration" (à la PL/360) on simple variables, only, so that more than one variable may use the same location in the core. The language, also, allows the variable initialization through the VALUE declaration.

The compiler HPCOM, written for PL/2100, is designed to be one-pass and runs on the CDC-6400 computer. The compiler HPCOM is written in PASCAL which is available on the CDC-6400 machine.

The compiler HPCOM, produces code for the various statements and expressions of the language PL/2100.

The input/output routines allow the transfer of information (to and from the external devices) in the ASCII character mode. Further modifications need to be done to have binary input/output; this is important if PL/2100 is to

be used for writing the operating system of the HP/2100A computer.

The HPCOM compiler does not produce code for the procedure, so the compiler needs to have some routines to produce code for procedures (if procedures need be used in the language PL/2100).

The HPCOM compiler produces codes for the target machine HP/2100A in relocatable binary form, so that the standard relocatable loader could be used to link the object programs and load the object programs for execution.

The design of the language PL/2100 and its compiler HPCOM facilitates the extension and modifications to be done easily. We suggest a further modification in the language PL/2100 in the use of EXTERNAL symbols. This could be added in the declaration part of the language. The symbols could be followed by the key word EXTERNAL and parameters might be allowed, too. For example,

EXTERNAL

IDIM(I,J) ; ↗ calculate  $K=I-\text{MIN}(I,J)$  ↘

↗ K in A-register ↘

FLOAT(I) ; ↗ CONVERT I to real X ↘

↗ result X in A- & B- register ↘

The addition of externals would facilitate the use of mathematical routines available in the HP/2100A library

The present work is mostly concerned with the design of the language PL/2100 and in producing a working compiler



HPCOM for it. Its merits rest on being the first very important step in building up a more complete system.

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APPENDIX A

## SYNTAX AND SEMANTICS OF PL/2100

A.1 Informal Definition of PL/2100

This section presents an informal introduction to the language. It is meant to give the reader an overview of the basic components of the language. A full syntactic and semantic definition of the language is contained in the next section.

A.1.1 The core of PL/2100

It is the core of the language which is recommended as an educational tool for teaching programming and as a tool for writing an operating system for the HP/2100A.

A.1.2 Basic Data

The basic data type of the language is scalar types. Their definition indicates an ordered set of values i.e. introduce an identifier as a constant standing for each value in the set. Apart from definable scalar types, there exist in PL/2100 two standard scalar types, whose values are not denoted by identifiers but instead by numbers and quotations respectively, which are syntactically distinct from identifiers. These types are: integer and char.

An integer may be written as a constant or it may be represented by a variable identifier.

A constant may be represented in integer, octal or character string form.

The set of values of type char is the character set available on a particular installation (which is ASCII character set for HP/2100A at the present installation). A character constant could only be two characters long for HP/2100A. A character is any element of the ASCII character set.

A scalar type may also be defined as a subrange of another scalar type by indicating the smallest and the largest value of the subrange.

A variable identifier is defined as any sequence of letters or digits beginning with a letter. Every integer and character variable may be initialized to a constant value at compile time.

### A.1.3 Basic Operators

All the basic operators act only on integer values. They are divided into five classes:

- a) the arithmetic operators of addition (+), subtraction (-), multiplication (\*), division (div), and unary minus (-).

The arithmetic operators return the integer value which results from the operation.

- b) the logical operators and (and), or (or) and not (not).

The logical operator returns a 1 if the result of the operation is true and a zero if the result of the operation is false. An individual operand is considered true if it is one and false if it is zero.

the relational operators greater than ( $>$ ), greater than or equal to ( $\geq$ ), equal to ( $=$ ), less than or equal to ( $\leq$ ), less than ( $<$ ), and not equal to ( $\neq$ ). The relational operator returns a 1 if the relation is true and a 0 if the relation is false.

#### A.1.4 Data Structures

The data structures, available in PL/2100 are arrays and records. Array and record must be declared.

The bounds for an array are scalar type or subrange of type integer. Arrays may be initialized to any constant value. For referencing an array variable, the subscript or index may be any legal expression. The time needed for a selection of an array component does not depend on the value of the selector (index). The array structure is therefore called a random-access structure.

In a record structure, the components (called fields) are not necessarily of the same type. In order that the type of a selected component be evident from the program text (without executing the program), a record selector does not contain a computable value, but instead consists of an identifier uniquely denoting the component to be selected. These component identifiers are defined in the record type definition. Again, the time needed to access a selected component does not depend on the selector, and the record structure is therefore also a random-access structure.

A record type may be specified as consisting of several variants. This implies that different variables, although said to be of the same type, may assume structures which differ in a certain manner. The difference may consist of a different number and different types of components. The variant which is assumed by the current value of a record variable is indicated by a component field which is common to all variants and is called the tag field. Usually the part common to all variants will consist of several components, including the tag field.

#### A.1.5 Statement Structures

The choice of statement structures was motivated by the desire to promote structured programming. The basic statement structures of the language are

- a) the assignment statement

<variable> ::= <expression>

- b) the if-then-else statement

if <expression>

then <statement><sub>1</sub> |

if <expression> then <statement><sub>1</sub>

else <statement><sub>2</sub>

which executes <statement><sub>1</sub> if <expression> is true, or <statement><sub>2</sub> if <expression> is false. The else part of the statement is optional.

c) the while statement

WHILE <expression> do <statement> which executes <statement> followed by the while statement, if necessarily Boolean <expression> is true. If <expression> is false, control passes to the statement following the while statement.

d) the repeat statement

repeat <statement> until <expression>

The expression controlling repetition must be of type Boolean. The sequence of statements between the symbols repeat and until is repeatedly (and at least once) executed until the expression becomes true.

e) the for statement

for <control variable> = <initial value> { down to  
to  
<final value> do <statement>

The for statement indicates that a statement is to be repeatedly executed while a progression of values is assigned to a variable which is called the control variable of the for statement.

The control variable, the initial value and the final value must be of the same scalar type (or subrange thereof). The repeated statement must alter neither the value of the control variable nor the final value.



f) the CASE statement

The case statement consists of an expression (the selector) and a list of statements, each being labelled by a constant of the type of the selector. It specifies that one statement to be executed whose label is equal to the current value of the selector.

```
case <expression> of <case list element> end
<case list element> ::= {<case label>:}<statement>
                        |{<case label>:}
```

A.1.6 Program Structure

The choice of program structure was motivated by the desire to promote modular design of programs. A complete PL/2100 program consists of a program header card, followed by a nonempty sequence of segment definitions, followed by the main segment (or procedure) which is followed by the symbol progend. The program begins execution at the start of the main procedure.

A.1.7 Segment Definitions

A segment is either a procedure or a function. A procedure definition takes the form:

```
procedure <procname> {(<parameter list>)}
      {<local declaration list>} <statement part>
```

where <procname> is the name of a procedure (an identifier), <parameter list> is an optional parameter list which consists of typed formal parameters, and <local declaration list>

is a possibly empty list of local variables that may not be initialized. A procedure could be recursive. Global variables may appear anywhere in the statement sequence.

A function definition takes the form

```
function <funcname> {(<parameter list>)}: <result type>
    {<local declaration list>} <statement part>
```

where <funcname> is the name of the function (an identifier), and <parameter list> and <local declaration list> are as defined above. The identifier representing the function name, returns the value of the function. <type> represents the type of the function (represented by the identifier).

#### A.1.8 Comments

A comment is any string of characters (except the symbol \*/) between /\* and \*/ and may appear in the program wherever a blank may occur.

### A.2 Syntax and Semantics of P1/2100

This section contains the syntactic and semantic definition of the language.

#### A.2.1 Notation and Terminology

According to traditional BNF (Backus-Normal Form OR Backus Naur Form) notation, syntactic constructs are denoted by English words enclosed between angular brackets < and >.



The program starts with a symbol program followed by the identifier which is the name of the program, and followed by a list of options (if any).

In the list of options, R specifies the printing of the table containing object codes, B specifies the printing of the object code in relocatable binary format and P specifies the relocatable binary code to be punched on the card.

### A.2.2.3 Example

```

PROGRAM FACT, B, P, R
  /* FINDS THE FACTORIAL OF INTEGERS UP TO 4 */
  CONST MAX = 4;
  VAR K, N, FACT: INTEGER;
/* this is the
beginning of a
<compound tail>*1 BEGIN
      1:      K: = 1; FACT: = 1
            IF (K EQ MAX) THEN GO TO 2;
            K: = K+1;
            FACT: = FACT*K;
            GO TO 1;
      2:      N: = FACT;
            END;
/* this is
the end of
a compound
tail */
PROGEND
/* there is
no procedure
or function present */

```

## A.2.3 Declarations

### A.2.3.1 Syntax

<declaration list> ::=

<label declaration part>

| <constant definition part>

```

|<type definition part>
|<variable declaration part>
  {<variable initialization part>}
|<synonym definition part>
<label declaration part> ::= <empty>|
    label <label>{, <label>}
<constant definition part> ::= <empty>|
    CONST <constant definition>{,<constant definition>};
<type definition part> ::= <empty>|
    type <type definition>{;<type definition>};
<synonym definition part> ::= <empty>|
    syn <synonym definition>{;<synonym definition>};
<variable declaration part> ::= <empty>|
    var <variable declaration>{;<variable declaration>};
<variable initialization part> ::= <empty>|
    value <variable initialization>{;<variable initialization>};

```

#### A.2.3.1.1

```

<constant definition> ::= <identifier> = <constant>
<constant> ::= <unsigned constant> | <sign><number>
               | <identifier>
<unsigned constant> : = <number> | ≡ <character>⊙ ≡

```

#### A.2.3.1.2

```

<type definition> ::= <identifier> = <type>
<type> ::= <scalar type> | <subrange type> | <array type> |
           <record type> | <type identifier>
<type identifier> : = <identifier>

```

A.2.3.1.2.1

<scalar type> ::= ( <identifier> {, <identifier>} )

A.2.3.1.2.2

<subrange type> ::= <constant> .. <constant>

A.2.3.1.2.3

<array type> ::= array [ <index type> {, <index type>} ]  
   of <component type>

<index type> ::= <scalar type> | <subrange type>

<component type> ::= <type>

A.2.3.1.2.4

<record type> ::= record <field list> end

<field list> ::= <fixed part> | <fixed part>; <variant part>  
   | <variant part>

<fixed part> ::= <record section> {; <record section>}  
   : <type>

<variant part> ::= case <tag field> : <type identifier>  
   of <variant> {; <variant>}

<variant> ::= { <case label> : }<sup>Ⓢ</sup> ( <field list> ) | { <case label> : }<sup>Ⓢ</sup>

<case label> ::= <unsigned constant>

<tag field> ::= <identifier>

A.2.3.1.3

<variable declaration> ::= <identifier> {, <identifier>}  
   : <type>

A.2.3.1.4

<variable initialization>

::= <identifier> = <constant> | { <constant>, { <constant> }<sup>Ⓢ</sup> }

A.2.3.1.5

<synonym definition>

:: = <identifier> = <identifier>{,<identifier>;}

A.2.3.2 Semantics

The declaration list consists of all the constant and integer, character, array and record variables that are global to the program.

Integer variables and arrays, and character variables and arrays may be initialized with their values assigned at compile time. In order to facilitate the initializing several elements of an array with the same value, any initialization value may be followed by an asterisk (\*) followed by a constant, implying that initial value should be assigned to the next consecutive sequence of elements whose length is defined by the constant in parentheses.

A.2.3.3 Examples

A.2.3.3.1 const A = 1, B = 2, C = 3;

A.2.3.3.2 type

color = (red, orange, yellow, green, blue);

cards = (club, diamond, heart, spade);

index = -10 .. 10;

days = Monday .. Friday;

vector = array [1 .. 10] of integer;

booltab = array [1..10] of boolean;

```

calendar = record day : 1..31;
           month : 1..12;
           year : 0..2000;
           end

```

#### A.2.3.3.3 syn

D = A,B,C;

B = J,I;

#### A.2.3.3.4 var

A: vector;

B : char;

C : boolean ;

D : array [1..10] of integer;

### A.2.4 Program Segments

#### A.2.4.1 Syntax

<procedure and function declaration part> ::= =  
 {<procedure or function declaration>;}

<procedure or function declaration> ::= =  
 <procedure declaration> | <function declaration>

<function declaration> ::= =

<function heading> <label declaration part>

<constant definition part> <type definition part>

<variable declaration part>.

<procedure and function declaration part> <statement part>



```

<function heading> ::= =
  function <identifier> (<formal parameter section>
    {;<formal parameter section>}) : <result type>;
  <result type> ::= = <type identifier>

<procedure declaration> ::= = <procedure heading><label
  declaration part>
  <constant definition part><type definition part>
  <variable declaration part>
  <procedure and function declaration part><statement part>

<procedure heading> ::= = procedure <identifier> ; |
  procedure <identifier>(<formal parameter section>
    {;<formal parameter section>});

<formal parameter section> ::= =
  <parameter group> |
  const <parameter group>{;<parameter group>}|
  var <parameter group>{;<parameter group>}|
  function <parameter group>|
  procedure <identifier>{,<identifier>}

<parameter group> : = <identifier>{,<identifier>}:
  <type identifier>

```

#### A.2.4.2 Semantics

A PL/2100 program consists of a sequence of procedures and functions called program segments.

#### A.2.4.3 Example

A.2.4.3.1 procedure add (var x : integer; var y : integer);

begin

z: = x+y+1; /\* z is a global variable \*/

end;

A.2.4.3.2 function sum (var x: integer; var y: integer):

integer;

begin

sum: = x+y

end;

## A.2.5 Statements

### A.2.5.1 Syntax

<statement part> ::= <compound statement>

<compound statement> ::= begin <component statement>

{;<component statement>} end

<component statement> ::= <statement> |

<label definition> <statement>

<label> ::= <integer>

#### A.2.5.1.1

<statement> ::= <simple statement> |

<structured statement>

<simple statement> ::= <assignment statement> |

<procedure statement> | <go to statement>

<assignment statement> ::= <variable> := <expression> |

<function identifier> := <expression>

<procedure statement> ::= <procedure identifier> |

<procedure identifier> (<actual parameter>

{, <actual parameter>})

<procedure identifier> ::= = <identifier>  
 <actual parameter> ::= <expression> | <variable>  
                   | <parameter identifier> | <function identifier>  
 <goto statement> ::= goto <label>  
                   <label> ::= = <integer>

#### A.2.5.1.2

<structured statement>  
 ::= = <compound statement> | <conditional>  
                   statement> | <repetitive statement>  
                   <conditional statement> ::= = <if statement> |  
   <case statement>  
                   <repetitive statement> ::= =  
                                   <while statement> | <repeat statement>  
                                   | <for statement>

#### A.2.5.1.2.1

<if statement> ::= = if <expression> then <statement>  
                   | if <expression> then <statement> else  
                   <statement>  
 <case statement> ::= = case <expression> of  
                   <case list element> { ; <case list element> } end  
 <case list element> ::= = { <case label> : } <statement> |  
                   { <case label> : }

#### A.2.5.1.2.2

<repeat statement> ::= = repeat <statement>  
                   { ; <statement> } until <expression>  
 <while statement> ::= = while <expression> do <statement>

```

<for statement> ::= = for <control variable>: =
                    <for list> do <statement>

<for list> ::= = <initial value> to <final value> |
                    <initial value> downto <final value>

<control variable> ::= = <identifier>

<initial value> ::= = <expression>

<final value> ::= = <expression>

```

### A.2.5.2 Semantics

A statement part is a sequence of statements.

### A.2.5.3 Examples

A.2.5.3.1      y := x

A.2.5.3.2      if x then  
                   y := x  
                   else  
                   y := z

A.2.5.3.3      case operator of  
                   plus : x := x+y ;  
                   times : x := x\*y ;  
                   absval : if x < 0 then x := x  
                   end

A.2.5.3.4      while i > 0 do  
                   begin  
                   z := z\*x ;  
                   i := i div 2  
                   end

A.2.5.3.5 repeat

```

    k := i mod j;
    i = j;
    j := k
until j = 0;

```

A.2.5.3.6 for i = 2 to 100 do if a[i] > max then

```

    max := a[i]

```

A.2.6 ExpressionA.2.6.1 Syntax

```

<expression> ::= <primary> | <operator><expression>
                | <shift><operator><expression>

```

```

<primary> ::= <constant> | <variable> |
              (<expression>) | <unary op><primary>

```

```

<variable> ::= <identifier> | <identifier> * <identifier>
              | <identifier> [<expression>]

```

```

<shift> := <shift operator><factor>

```

```

<factor> ::= <integer>

```

```

<operator> ::= <arithmetic operator> | <logical operator>
              | <relational operator> | <bit operator>

```

```

<arithmetic operator> ::= + | - | * | div

```

```

<logical operator> ::= and | or

```

```

<relational operator> ::= LT | < | LE | ≤ | GE | ≥ | EQ | = |
                        NE | ≠ | GT | >

```

```

<shift operator> ::= ALS | BLS | ARS | BRS | ALF | BLF |
                  RAL | RBL | RAR | RBR

```

<bit operator> ::= = IOR|XOR

<unary op> ::= = NOT|-

<constant> ::= = <signed constant>|<unsigned constant>

<signed constant> := <sign><integer>

<sign> := +|-

<unsigned constant> ::= = <integer>|<octal>|<character constant>

<integer> ::= = <digit><sup>Ⓢ</sup>

<digit> ::= = 0|1|2|3|4|5|6|7|8|9

<octal> ::= = <octal digit><sup>Ⓢ</sup>B

<octal digit> := 0|1|2|3|4|5|6|7

<character constant> ::= = ≡ <character string> ≡

<character string> ::= = <letter or digit><sup>Ⓢ</sup>

<letter or digit> ::= = <letter>|<digit>

<identifier> ::= = <letter or digit><sup>Ⓢ</sup>

<letter> ::= = A|B|C|D|E|F|G|H|I|J|K|L|M|N|O|P|  
Q|R|S|T|U|V|W|X|Y|Z

### A.2.6.2 Semantics

An expression is a rule for computing a numerical value.

A primary is either a constant, a variable or an expression enclosed in parentheses. The operators have the following meaning associated with them. Note that several of the operations are machine dependent.

#### A.2.6.2.1 Unary minus (-)

Returns the negative of the primary, it precedes.

A.2.6.2.2 Logical not (not)

Returns a 1 if the value of the primary it precedes is zero and returns a zero if the value of the primary it precedes is one.

A.2.6.2.3 Left Arithmetic Shift (als)

The A-register is shifted left by the number of bits specified by the <factor>. Sign bit is not affected. Bit shifted out of bit 14 is lost. A "0" replaces vacated bits on the right.

A.2.6.2.4 Right Arithmetic Shift (ars)

The A-register is shifted right by the number of bits specified by the <factor>. Sign bit (bit 15) is not affected; copy of sign bit is shifted into the bits adjacently right to it. Bit shifted out of bit 0 is lost.

A.2.6.2.5 Rotate A Left (ral)

Rotate A-register left by number of places specified by the <factor>. Bits 15, 14 etc. are rotated respectively, into bit 1, 0 etc.

A.2.6.2.6 Rotate A Right (rar)

Rotate A-register right by number of places specified by the <factor>. Bits 0, 1 etc. are rotated, respectively, into bits 14, 15 etc.

A.2.6.2.7 Rotate A Left Four (alf)

Rotate A register left four places and <factor> denotes how many of this type of rotation is to be done. For each alf rotation bits 15, 14, 13, 12 are rotated around to bits 3, 2, 1, 0 respectively.

#### A.2.6.2.8 Corresponding Shifts on the B-register

The operators brs, rbl, rbr, blr, bls, blf may be used to shift bits on the B-register and these correspond exactly to those operators described for the A-register.

#### A.2.6.2.9 Bit inclusive or (ior)

The contents of the A-register are combined with the contents of the memory location as an "inclusive or" logic operation.

#### A.2.6.2.10 Bit exclusive or (xor)

The contents of the memory location are combined with the contents of the A-register by an "exclusive or" operation.

#### A.2.6.2.11 Addition (+)

Returns the sum of the expression and the primary.

#### A.2.6.2.12 Subtraction (-)

Returns the result of subtracting the primary from the expression.

#### A.2.6.2.13 Multiplication (\*)

Returns the result of the multiplication of the primary and the expression.

#### A.2.6.2.14 Division (div)

Returns the quotient of the primary and the expression.

#### A.2.6.2.15 Equal (=)

Returns a 1 if the relation and the expression are equal, and 0 otherwise.



#### A.2.6.2.16 Other Relations

Not equal ( $\neq$ ), greater than ( $>$ ), less than ( $<$ ), greater than or equal ( $\geq$ ) and less than or equal ( $\leq$ ) are similar to equal ( $=$ ).

#### A.2.6.2.17 Logical And (and)

Returns a 1 if the both of the boolean expression and the primary are nonzero and a zero otherwise.

#### A.2.6.2.18 Logical or (or)

Returns a 1 if either or both of the expressions and the primary are non-zero and zero otherwise.

#### A.2.6.2.19 Precedence

The operators are of the same precedence. Expression is evaluated from left to right. Only an expression between two parentheses have higher precedence than the one not inside a pair of parentheses.

#### A.2.6.2.20 Character String

A character string constant occupies one machine word. The constant is padded on the right with blanks or truncated on the right as needed to fit into one word. Note this is machine dependent to the extent of both word size and internal character representation.

In HP/2100A, the word size is 16 bits and can have only two characters which are, internally, represented as ASCII character set.

A.2.6.2.21 Examples

- i)  $x+y*z \text{ div } U - 2$
- ii)  $A \text{ als } 6 \text{ ior mask } 1 \text{ xor mask } 2$
- iii)  $A \text{ and } B < C$
- iv)  $-x \text{ rar } 5$
- v)  $A*(x+y*z)$  and  $A*x+y*z$  will have different results
- vi)  $(x+y*z)*A$  and  $x+y*z*A$  will have the same results
- vii)  $\equiv A^2 \equiv$

A.2.7 Blanks

One or more blanks appear anywhere except within a symbol, identifier, or operator.

A.2.8 Comments

Comments are any string of characters (except the symbol `*/`) between `/*` and `*/`. A comment has no effect on the program and may appear anywhere that blanks may occur.

`<comment>` ::= `<opening bracket><almost anything>`  
`<closing bracket>`

`<opening bracket>` ::= `/*`

`<closing bracket>` ::= `*/`

`<almost anything>` ::= {any string of valid ASCII characters which does not contain a `<closing bracket>`}

APPENDIX BHEWLETT PACKARD ASSEMBLY LANGUAGE INSTRUCTIONS AND  
THEIR MEANINGS

In this appendix the HP instructions (only those which have been used as operators or in compiler-generated codes) are given along with their meanings.

Notations used in representing the HP assembly language instruction is as follows:

m	Memory location
I	Indirect addressing locator
Comments	Optical comments
[ ]	Brackets defining a field or position of a field that is optional
{ }	Brackets indicating that one of the set may be selected
lit	literal

B.1 Memory Reference

Memory reference instructions perform arithmetic, logical and jump operations on the contents of the locations in core and the registers. An instruction may directly address the 2048 words of the current and base pages. If required, indirect addressing may be utilized to refer to all 27,777 words of memory. Expressions in the operand field may evaluate modulo  $2^{10}$ .

If the program is in relocatable form (which is the case here as HPCOM produces relocatable binary code), the operand field may contain relocatable expressions or absolute expressions which are less than  $100_8$  in value.

### B.1.1 Jump

Jump instructions may alter the normal sequence of program execution.

#### B.1.1.1 JMP

label JMP m[,I] comments

Jump to m. Jump indirect inhibits interrupt until the transfer of control is complete.

#### B.1.1.2 JSB

label JSB m[,I] comments

Jump to subroutine. The address for label+1 is placed into the location represented by m and control transfer to m+1. On completion of the subroutine, control may be returned to the normal sequence by performing a JMP m,I.

### B.1.2 Add, Load and Store

Add, Load and Store instructions transmit and alter the contents of memory and of the A- and B-registers. A literal, indicated by "lit" may be either = B, = D, = A or = I type.

#### B.1.2.1 ADA

label ADA {m[,i],  
lit} comments

Add the contents of m to A.

B.1.2.2 ADB

label    ADB    {<sup>m</sup>[,I]  
                  lit}    comments

Add the contents of m to B.

B.1.2.3 LDA

label    LDA    {<sup>m</sup>[,I]  
                  lit}    comments

Load A from m.

B.1.2.4 LDB

label    LDB    {<sup>m</sup>[,I]  
                  lit}    comments

Load B from m.

B.1.2.5 STA

label    STA    {<sup>m</sup>[,I]  
                  lit}    comments

Store contents of A in m.

B.1.2.6 STB

label    STB    {<sup>m</sup>[,I]  
                  lit}    comments

Store contents of B in m.

In each instruction, the contents of the sending location is unchanged after execution.

B.1.3 Logical Operations

The logical instructions allow bit manipulation and the comparison of two computer words.

B.1.3.1 AND

label    AND    {<sup>m</sup>[,I]  
                  lit}    comments

The logical product of the contents of m and the contents of A are placed in A.

B.1.3.2 XOR

label XOR {<sup>m[,I]</sup>  
lit} comments

The modulo-two sum (exclusive "or") of the bits in m and the bits in A is placed in A.

B.1.3.3 IOR

label IOR {<sup>m[,I]</sup>  
lit} comments

The logical sum (inclusive "or") of the bits in m and the bits in A is placed in A.

B.2 Register Reference

The register reference instructions include a shift-rotate group, an alter-skip group and NOP (no operation). With the exception of NOP, they have the capability of causing several actions to take place during one memory cycle. Multiple operations within a statement are separated by a comma.

B.2.1 Shift-Rotate Group

This group contains 19 basic instructions that can be combined to produce more than 500 different single cycle operations.

CLE	Clear E to zero
ALS	Shift A left one bit, zero to least significant bit. Sign unaltered.
BLS	Shift B left one bit, zero to least significant bit. Sign unaltered.
ARS	Shift A right one bit, extend sign; sign unaltered.
BRS	Shift B right one bit, extend sign; sign unaltered.

RAL	Rotate A left one bit
RBL	Rotate B left one bit
RAR	Rotate A right one bit
RBR	Rotate B right one bit
ALR	Shift A left one bit, clear sign, zero to least significant bit
BLR	Shift B left one bit, clear sign, zero to least significant bit
ERA	Rotate E and A right one bit
ERB	Rotate E and B right one bit
ELA	Rotate E and A left one bit
ELB	Rotate E and B left one bit
ALF	Rotate A left four bits
BLF	Rotate B left four bits
SLA	Skip next instruction if least significant bit in A is zero
SLB	Skip next instruction if least significant bit in B is zero.

These instructions may be combined as follows:

label	<table border="1" style="border-collapse: collapse; text-align: center;"> <tr><td>ALS</td></tr> <tr><td>ARS</td></tr> <tr><td>RAL</td></tr> <tr><td>RAR</td></tr> <tr><td>ALR</td></tr> <tr><td>ALF</td></tr> <tr><td>ERA</td></tr> <tr><td>ELA</td></tr> </table>	ALS	ARS	RAL	RAR	ALR	ALF	ERA	ELA	[,CLE]	[,SLA]	<table border="1" style="border-collapse: collapse; text-align: center;"> <tr><td>ALS</td></tr> <tr><td>ARS</td></tr> <tr><td>RAL</td></tr> <tr><td>RAR</td></tr> <tr><td>ALR</td></tr> <tr><td>ALF</td></tr> <tr><td>ERA</td></tr> <tr><td>ELA</td></tr> </table>	ALS	ARS	RAL	RAR	ALR	ALF	ERA	ELA	comments
ALS																					
ARS																					
RAL																					
RAR																					
ALR																					
ALF																					
ERA																					
ELA																					
ALS																					
ARS																					
RAL																					
RAR																					
ALR																					
ALF																					
ERA																					
ELA																					
label	<table border="1" style="border-collapse: collapse; text-align: center;"> <tr><td>BLS</td></tr> <tr><td>BRS</td></tr> <tr><td>RBL</td></tr> <tr><td>RBR</td></tr> <tr><td>BLR</td></tr> <tr><td>BLF</td></tr> <tr><td>ERB</td></tr> <tr><td>ELB</td></tr> </table>	BLS	BRS	RBL	RBR	BLR	BLF	ERB	ELB	[,CLE]	[,SLB]	<table border="1" style="border-collapse: collapse; text-align: center;"> <tr><td>BLS</td></tr> <tr><td>BRS</td></tr> <tr><td>RBL</td></tr> <tr><td>RBR</td></tr> <tr><td>BLR</td></tr> <tr><td>BLF</td></tr> <tr><td>ERB</td></tr> <tr><td>ELB</td></tr> </table>	BLS	BRS	RBL	RBR	BLR	BLF	ERB	ELB	comments
BLS																					
BRS																					
RBL																					
RBR																					
BLR																					
BLF																					
ERB																					
ELB																					
BLS																					
BRS																					
RBL																					
RBR																					
BLR																					
BLF																					
ERB																					
ELB																					

CLE,SLA, or SLB appearing alone or in any valid combinations with each other are assumed to be a shift-rotate machine instruction.

The shift-rotate instructions must be given in the order shown. At least one and up four are included in one statement. Instructions referring to the A-register may not be combined in the same statement with those referring to the B-register.

### B.2.2 No-Operation Instruction

When a no-operation is encountered in a program, no action takes place; the computer goes on to the next instruction. A full memory cycle is used in executing a no-operation instruction.

label      NOP      comments

A subroutine to be entered by a JSB instruction should have a NOP as the first statement. The return address can be stored in the location coupled by the NOP during execution of the program. A NOP statement causes the assembler to generate a word of zeros.

### B.2.3 Alter-Skip Group

The alter-skip group contains 19 basic instructions that can be combined to produce more than 700 different single cycle operations.



CLA	Clear the A-register
CLB	Clear the B-register
CMA	Complement the A-register
CMB	Complement the B-register
CCA	Clear, then complement the A-register (set to ones)
CCB	Clear, then complement the B-register (set to ones)
CLE	Clear the E-register
CME	Complement the E-register
CCE	Clear, then complement the E-register
SEZ	Skip next instruction if E is zero
SSA	Skip if sign of A is positive (0)
SSB	Skip if sign of B is positive (0)
INA	Increment A by one
INB	Increment B by one
SZA	Skip if contents of A equals zero
SZB	Skip if contents of B equals zero
SLA	Skip if least significant bit of A is zero
SLB	Skip if least significant bit of B is zero
RSS	Reverse the sense of the skip instructions. If no skip instruction precede in the statement, skip the next instruction.

These instructions may be combined as follows:

$$\text{label} \left[ \begin{array}{c} \text{CLA} \\ \text{CMA} \\ \text{CCA} \end{array} \right] [ , \text{SEZ} ] \left[ \begin{array}{c} \text{CLE} \\ \text{CME} \\ \text{CCE} \end{array} \right] [ , \text{SSA} ] [ , \text{SLA} ] [ , \text{INA} ] [ , \text{SZA} ] [ , \text{RSS} ] \text{ comments}$$

$$\text{label} \left[ \begin{array}{c} \text{CLB} \\ \text{CMB} \\ \text{CCB} \end{array} \right] [ , \text{SEZ} ] \left[ \begin{array}{c} \text{CLE} \\ \text{CME} \\ \text{CCE} \end{array} \right] [ , \text{SSB} ] [ , \text{SLB} ] [ , \text{INB} ] [ , \text{SZB} ] [ , \text{RSS} ] \text{ comments}$$

The alter-skip instructions must be given in the order shown. At least one and up to eight are included in one statement. Instructions referring to the A-register may not be combined in the same statement with those referring to the B-register. When two or more skip functions are combined in a single operation, a skip occurs if any of the conditions exist. If a word with RSS also includes both SSA and SLA (or SSB and SLB), a skip occurs only when sign and least significant bit are both set to (1).

### B.3 Extended Arithmetic Instructions

These instructions may be used with the EAU version of the Assembler or Extended Assembler to increase the computer's overall efficiency. The computer (HP/2100A) must include the Extended Arithmetic Unit option to obtain the resulting increase in available core storage and decrease in program run time.

Only two of these instructions have been used in the HPCOM and are considered here. Both are memory reference instructions.

#### B.3.1 MPY

```
label    MPY    {m[,I]  comments
             lit
```

The MPY instruction multiplies the contents of the A-register by the contents of m. The product is stored in registers B and A. B contains the sign of the product and

the 15 most significant bits; A contains the least significant bits.

### B.3.2 DIV

label	DIV	{ $m$ [,I] lit}	comments
-------	-----	--------------------	----------

The DIV instruction divides the contents of registers B and A by the contents of m. The quotient is stored in A and the remainder in B. Initially B contains the sign and the 15 most significant bits of the dividend. A contains the least significant bits.

## COMPILE-TIME TABLES FOR RESERVED WORDS USED IN PL/2100

C.1. Reserved Symbols and Associated Integer Token

In this section we display in the following table, the reserved symbols (used in PL/2100) along with the integer tokens (NO and CL) associated with them.

SYMBOL	NO	CL	SYMBOL	NO	CL
ID	1	0	ALS	24	1
INTEGER	2	1	BLS	24	2
CHAR	2	2	ARS	24	3
	5	1	BRS	24	4
NOT	5	1	RAR	24	5
*	6	1	RBR	24	6
!	6	2	RAL	24	7
^	6	3	RBL	24	8
AND	6	3	ALF	24	9
DIV	6	4	BLF	24	10
MOD	6	5	IOR	24	11
+	7	1	XOR	24	12
-	7	2	BEGIN	25	0
V	7	3	END	26	0
OR	7	3	IF	27	0
<	8	1	THEN	28	0
LT	8	1	ELSE	29	0
<	8	2	CASE	30	0
LE	8	2	OF	31	0
≥	8	3	REPEAT	32	0
GE	8	3	UNTIL	33	0
>	8	4	WHILE	34	0
≠	8	5	DO	35	0
NE	8	5	FOR	36	0
=	8	6	TO	37	1
EQ	8	6	DOWNTO	37	2
IN	8	7	GOTO	38	0
(	9	0	NIL	39	0
)	10	0	TYPE	40	0
[	11	0	ARRAY	41	0
]	12	0	RECORD	41	2
,	15	0	FILE	41	3
:	16	0	LABEL	42	0
.	17	0	CONST	43	0
:	21	0	VAR	45	0
:=	22	0	FUNCTION	46	0
/*	23	0	PROCEDURE	47	0
			VALUE	48	0
			WITH	49	0
			REGISTER	50	0
			SYN	51	0
			REGA	52	1
			REGB	52	2

## C.2 Tables of Reserved Symbols and Their Pointers

The reserved words of the language PL/2100A are stored in different tables according to the lengths of the words. The pointers associated with the top of each table, are also stored in a separate table. In the actual programming world, the tables of reserved words are stored in an array of characters and the table of pointers in an array of integer.

### C.2.1 Table of Reserved Words

Index	Reserved Words of PL/2100	Index	Reserved Words of PL/2100
1	IF	32	REGA
2	DO	33	REGB
3	TO	34	THEN
4	OF	35	ELSE
5	OR	36	GOTO
6	LT	37	CASE
7	LE	38	WITH
8	GT	39	TYPE
9	GE	40	FILE
10	NE	---	---
11	EQ	41	BEGIN
---	---	42	UNTIL
12	AND	43	WHILE
13	END	44	ARRAY
14	NIL	45	VALUE
15	FOR	46	CONST
16	DIV	47	LABEL
17	MOD	---	---
18	VAR	48	PACKED
19	SYN	49	REPEAT
20	ALS	50	DOWNTO
21	BLS	51	RECORD
22	ARS	---	---
23	BRS	52	FUNCTION
24	RAR	53	REGISTER
25	RBR	---	---
26	RAL	54	PROCEDURE
27	RBL		
28	ALP		
29	BLP		
30	IOR		
31	XOR		

C.2.2 Table of Pointers

Column 1	Column 2
INDEX	POINTER VALUE
1	1
2	1
3	12
4	32
5	41
6	48
7	52
8	52
9	54
10	55
11	55

It is important to note that the index values in the first column of this table correspond to the length of the reserved words stored in a table pointed to by the pointer value given in the second column.

This has been done to facilitate a faster search through the tables of reserved words.

C.2.3 Tables of Token Values of the Reserved Words

Two tables viz WNO and WCL (in the programming world, they are arrays of integer) have been used to store the token value NO and CL. These tables are given below.

WNO = (27,35,37,31,7,8,8,8,8,8,8,6,26  
 39,36,6,6,45,51,24,24,24,24,24,24,  
 24,24,24,24,24,52,52,28,29,38,30,49,40,  
 41,25,33,34,41,48,43,42,53,32,37,41,46,50,47)

WCL = (0,0,1,0,3,1,2,4,3,5,6,3,0,0,0,4,5,0,  
0,1,2,3,4,5,6,7,8,9,10,11,12,1,2,0,0,0,  
0,0,0,3,0,0,0,1,0,0,0,0,0,2,2,0,0,0)

APPENDIX D  
LISTING OF LEXICAL ANALYSER



```

*****
PROCEDURE NEXTCH ;
  READS NEXT CHAK OF FILE INPUT,
  READS EOL TO BLANK,
  PRINTS ERROR SUMMARY AFTER EACH LINE
  PRINTS ADDRESS AT THE BEGINNING OF LINE ;
BEGIN
  IF EOLFLAG THEN
  BEGIN
    IF ERRINX GE 0 THEN PRINTRR ^ PRINT ERROR ^ ;
    EOLFLAG := FALSE ; CHCNT := 0 ;
    OUTPUT ^ := E ^ ; PUT(OUTPUT) ;
    IF OP THEN OUIO(LC) ELSE OUIO(IC) ;
    OUIPUT ^ := E ^ ; PUT(OUIPUT) ;
  END ^ EOLFLAG ^ ;
  GET(INPUT) ; CH := INPUT ^ ; CHCNT := CHCNT + 1 ;
  IF CHCNT > 73 THEN
  BEGIN
    OUTPUT ^ := E ^ ;
    OUIPUT ^ := E ^ ;
    PUT(OUTPUT) ;
    OUIPUT ^ := E ^ ;
    PUT(OUIPUT) ;
  REPEAT
    GET(INPUT) ; CH := INPUT ^ ;
    OUTPUT ^ := CH ^ ;
    PUT(OUTPUT) ;
  UNTIL CH=EOL ;
  END ^ ELSE
  BEGIN
    OUTPUT ^ := CH ^ ; PUT(OUTPUT) ;
    IF CH=EOL THEN GO TO 10
  END ;
  CH := E ^ ; EOLFLAG := TRUE ;
  CH := E ^ ; NEXTCH ^ ;
*****
10: END ^
*****

```

```
PROCEDURE INSYMBOL ;
  LEXICAL ANALYSER +
```

```
VAR I,K ; INTEGER ;
    DIRTY ; ARRAY (0..4) OF INTEGER ;
```

```
1: WHILE CH<=E THEN NEXTCH ; IDENTIFIER +
    BEGIN
```

```
    KI=0 ;
```

```
    REPEAT
```

```
      KI:=K+1 ;
```

```
      A(K) := BLANK ;
```

```
    UNTIL K=10 ;
```

```
    K:=0 ; REPEAT IF K<ALFALENG THEN
```

```
      BEGIN
```

```
        KI:=K+1 ;
```

```
        A(K) := CH ;
```

```
      END ;
```

```
    UNTIL CH = NEXICH ;
```

```
    REPEAT
```

```
      FOR I:=HL(K) TO HL(K+1) -1 DO
```

```
        BEGIN
```

```
          CPARE(A(I),I,COMPARE) ;
```

```
          IF COMPARE THEN
```

```
            BEGIN NO :=HNO(I) ;
```

```
              CL:=MCL(I) ; IVAL:=0 ;
```

```
              GO TO 2 ;
```

```
            END ;
```

```
          NO:=1 ; CL:=K ;
```

```
          I:=0 ;
```

```
          REPEAT
```

```
            I:=I+1 ;
```

```
            AVAL(I) := BLANK ;
```

```
          UNTIL I=10 ;
```

```
          I:=0 ;
```

```
          REPEAT
```

```
            I:=I+1 ;
```

```
            AVAL(I) := A(I) ;
```

```
          UNTIL I=K ;
```

```
        END ;
```

```
      END ;
```

```
    END ;
```

```
2: END ;
```

```
IF CH <= E THEN
```

```
  BEGIN NO:=1 ; CL:=1 ; I:=0 ;
```

```
  END ;
```

```

WHILE CH IN DIGITS DO
  BEGIN
    IF I < 5 THEN
      DIGIT(I) := ORD(CH) - ORD('0');
      I := I + 1;
      NEXTCH :=
        END;
    IVAL := 0;
    IF CH = 'E' THEN
      BEGIN
        IF I > 5 THEN
          SOCIAL ERROR(2) ELSE
          FOR K := 0 TO I - 1 DO
            IVAL := 8 * IVAL + DIGIT(K);
          NEXTCH :=
            END ELSE
            BEGIN
              IF I > 4 THEN
                SOCIAL ERROR(2) ELSE
                FOR K := 0 TO I - 1 DO
                  BEGIN
                    IF IVALS * MAX10 THEN
                      IVAL := 10 * IVAL + DIGIT(K)
                    ELSE
                      BEGIN
                        SOCIAL ERROR(2);
                        IVAL := 0;
                      END;
                    END;
                  END;
                END;
              END;
            END;
          END;
        BEGIN
          IF SPECIAL CHARACTER CONSTANT THEN
            CH := CHAR CONSTANT;
            RT := FALSE;
            NU := 2;
            K := 0;
            NEXTCH :=
              BEGIN
                REPEAT
                  IF CH = 'E' THEN
                    BEGIN
                      NEXTCH :=
                        IF > BT1 THEN
                          IF K = ALLENG THEN
                            BEGIN
                              SOCIAL ERROR(54);
                              BT1 := TRUE;
                            END;
                          ELSE
                            BEGIN
                              K := K + 1;
                              A(K) := CH;
                              END;
                            UNTIL BT1;
                          TECH1 := A(K);
                          IVAL := ASCII(TECH);
                          IF K = 2 THEN
                            BEGIN
                              TECH := A(K);
                              TEMP := ASCII(TECH);
                              APPEND(IVAL, 8, TEMP);
                              END;
                            ELSE
                              BEGIN
                                NO := SYMNO(CH);
                                CL := SYMCL(CH);
                                IVAL := 0;
                                TEST FOR TWO CHARACTER SYMBOL;
                                IF CH = 'E' THEN

```

```

BEGIN NEXICH ;
IF CH#=# THEN
  BEGIN NO:= 22 ; NEXICH END ;
END ELSE
IF CH#=# THEN
  BEGIN NEXICH ;
  IF CH#=# THEN
    BEGIN NO:=21 ; NEXICH END;
  END ELSE
  IF CH#=# THEN
    BEGIN NEXICH ;
    IF CH#=# THEN
      * SKIP COMMENT *
    NEXICH ;
    WHILE CH#=# DO NEXICH ;
    NEXICH ; ELSE
      GOTO 3 ELSE
    NEXICH ; GOTO 1 ;
  END ELSE
  END ELSE
  NEXICH ;

```

```

END ; * SPECIAL CHARACTER *

```

```

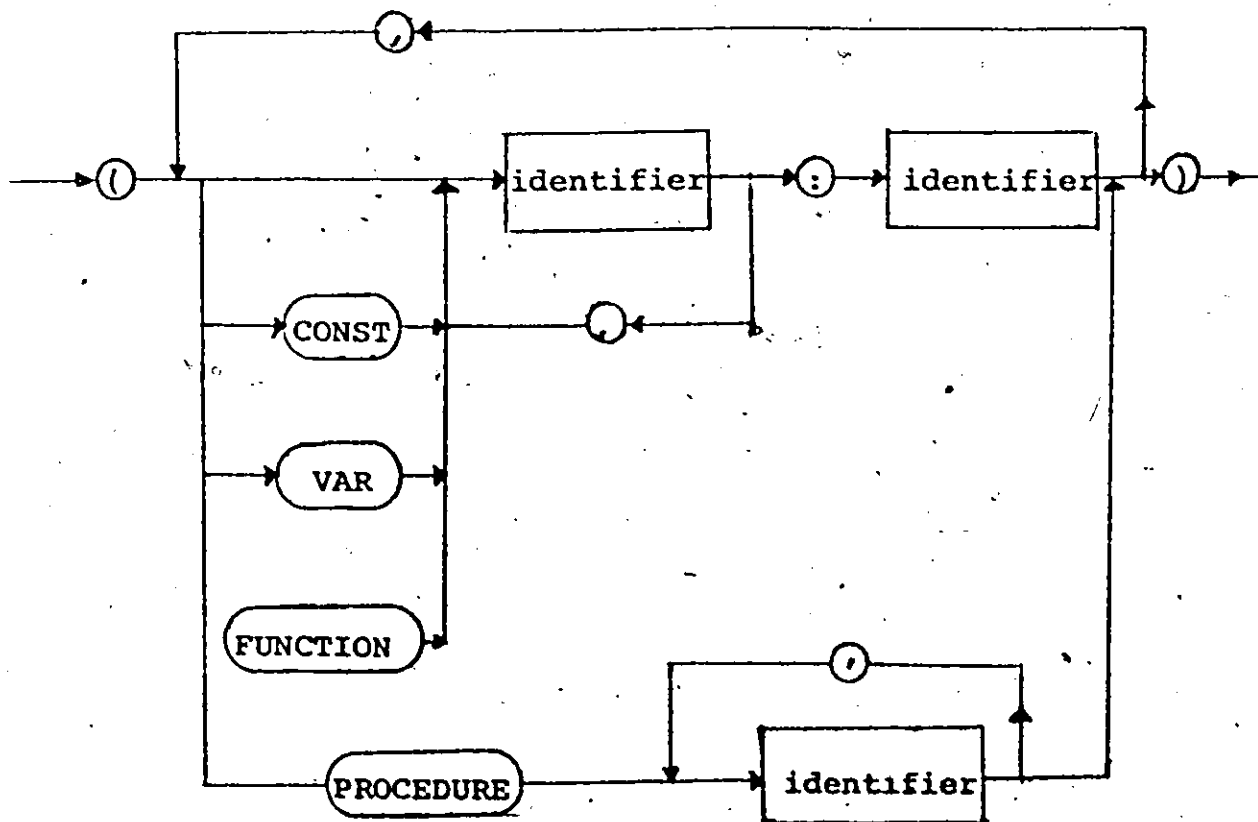
*****
END ; * INSYMBOL *
*****

```

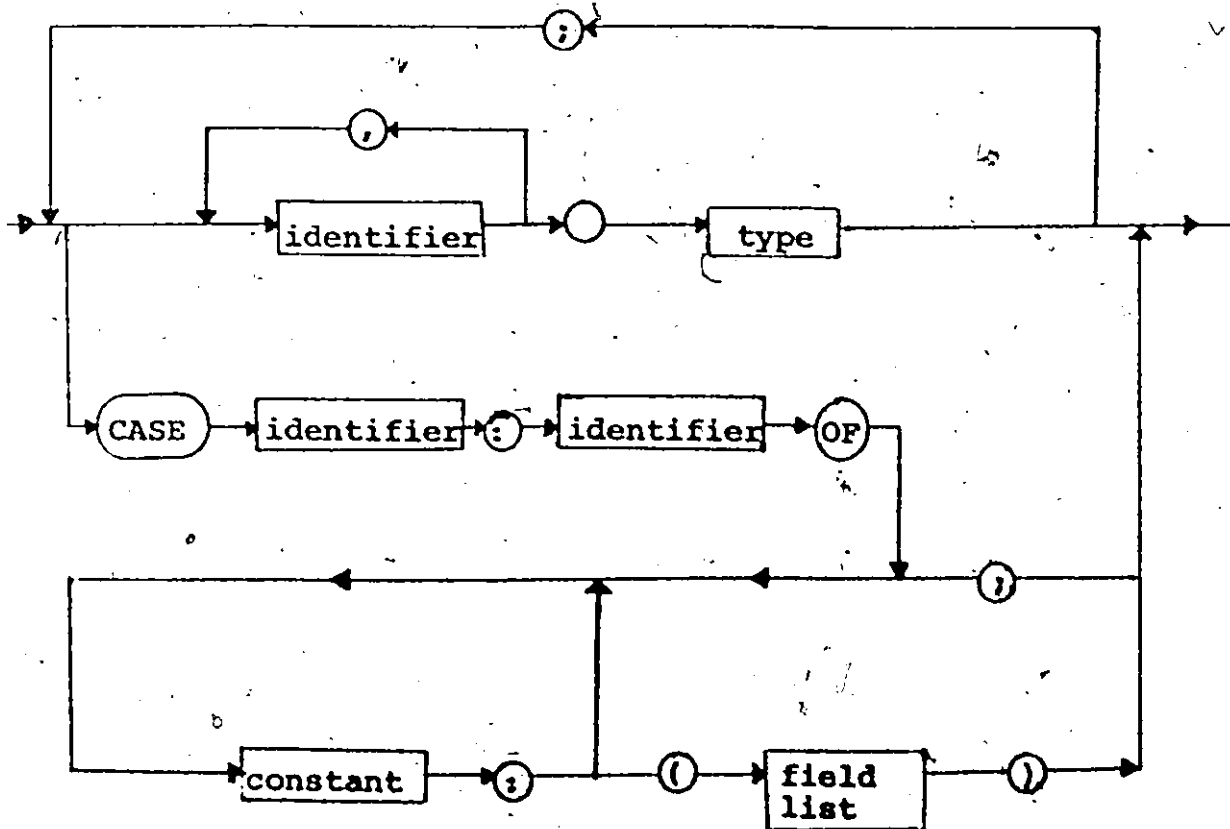
5A

APPENDIX E  
SYNTAX DIAGRAM OF PL/2100

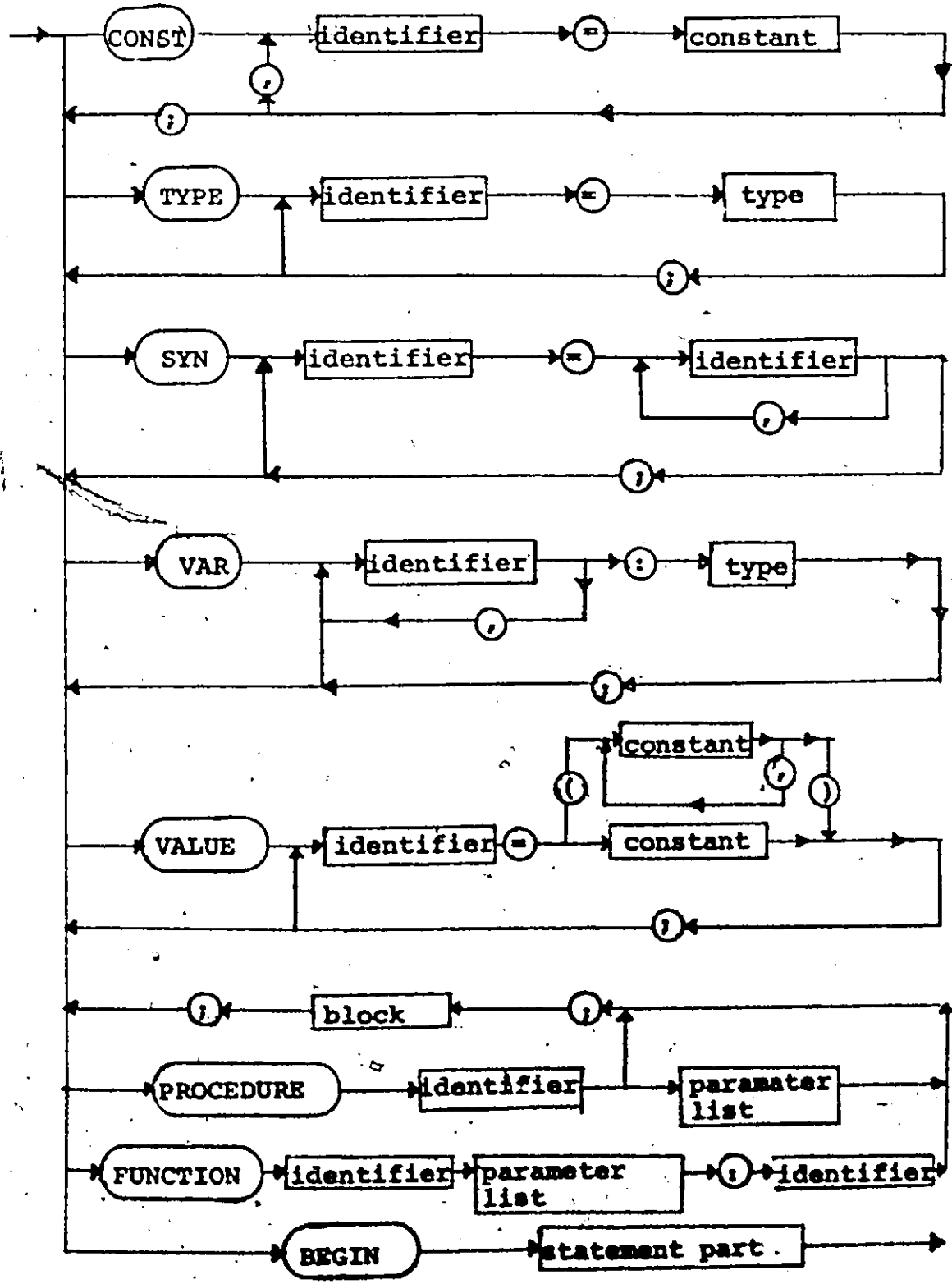
parameter list



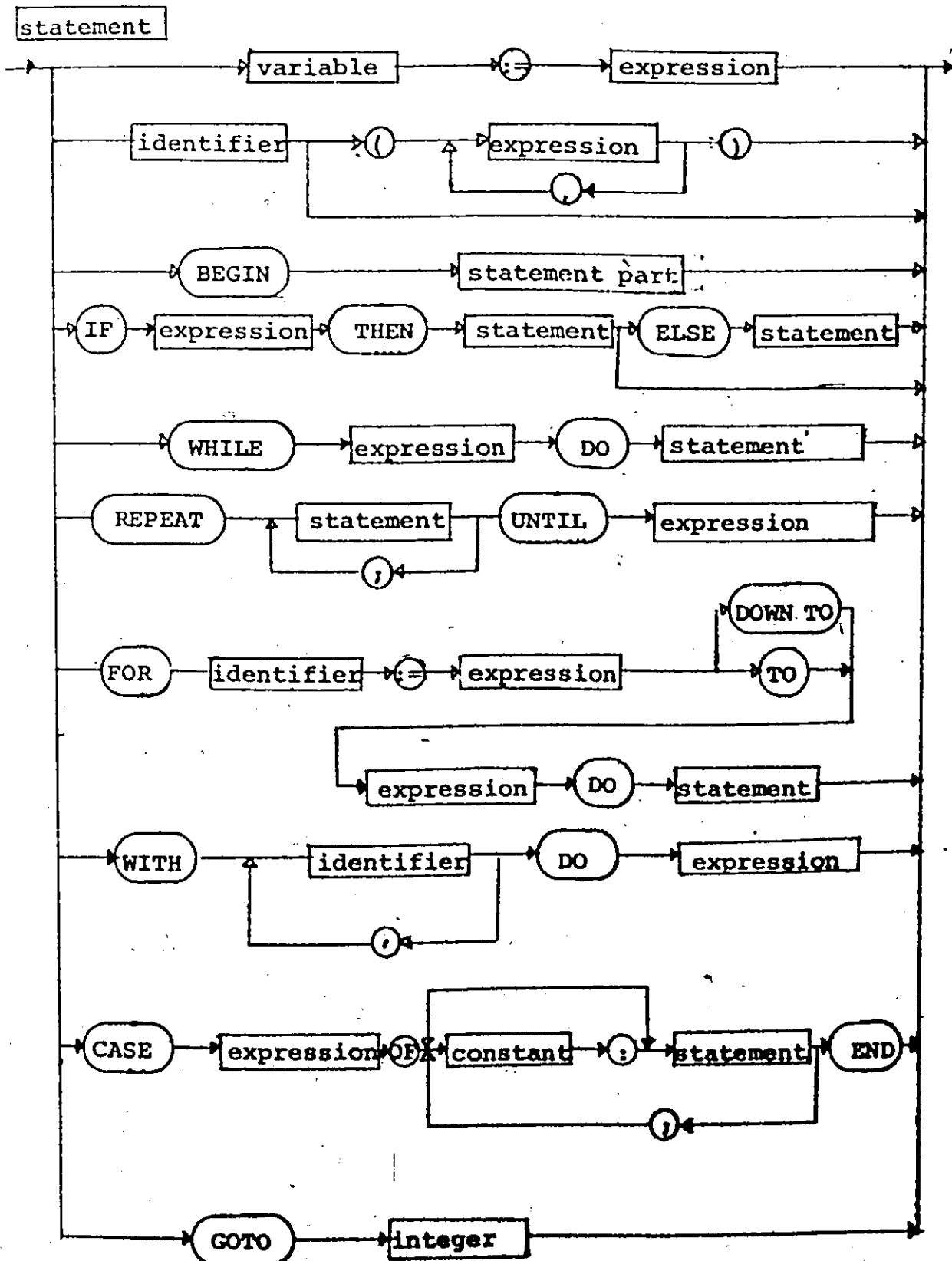
field list



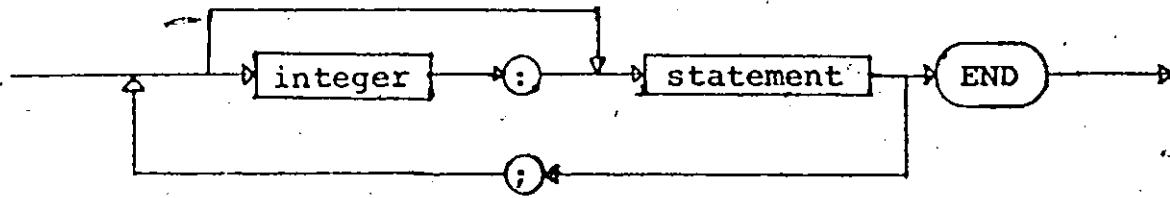
block



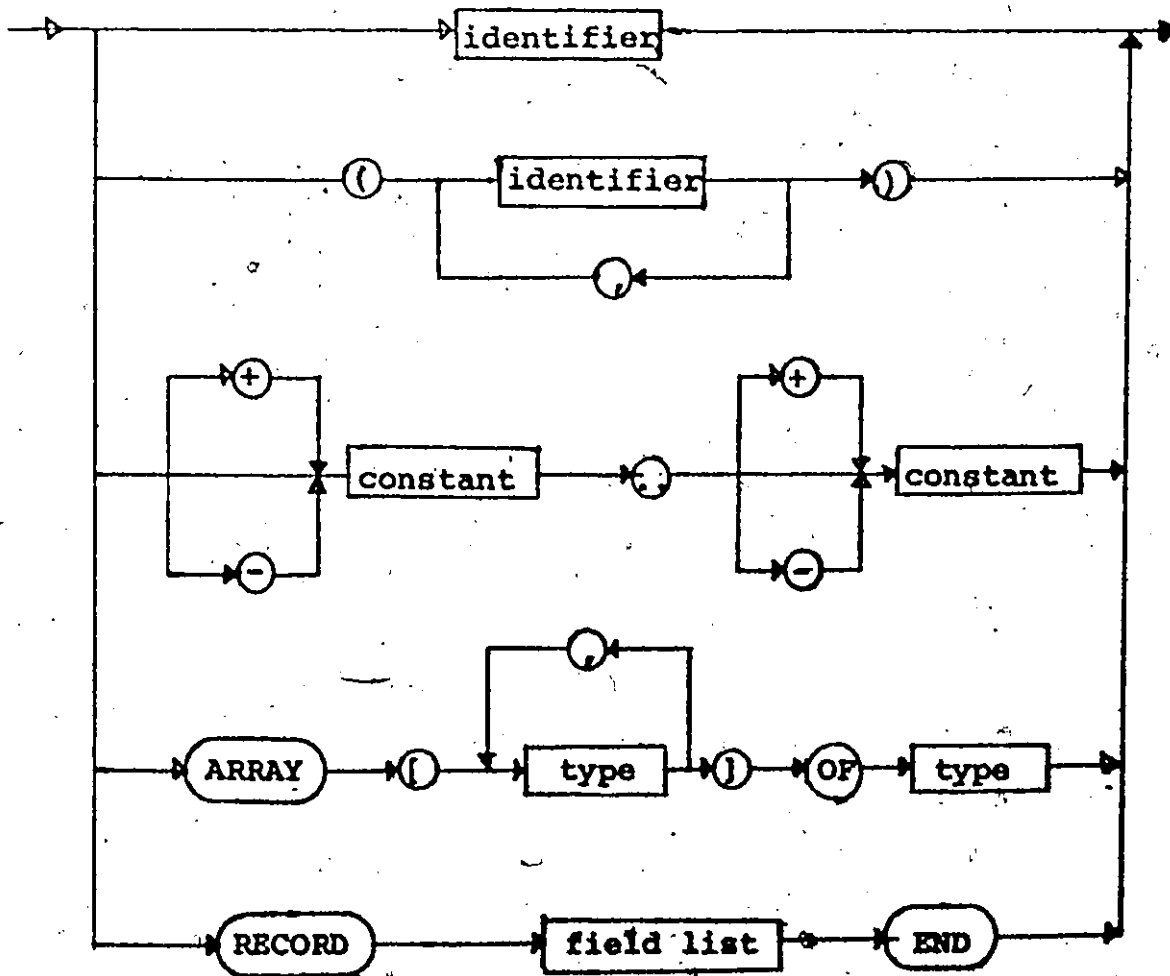




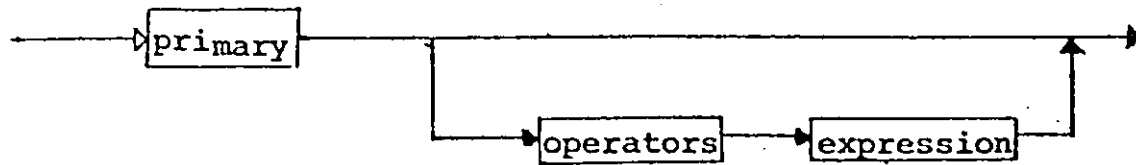
statement part



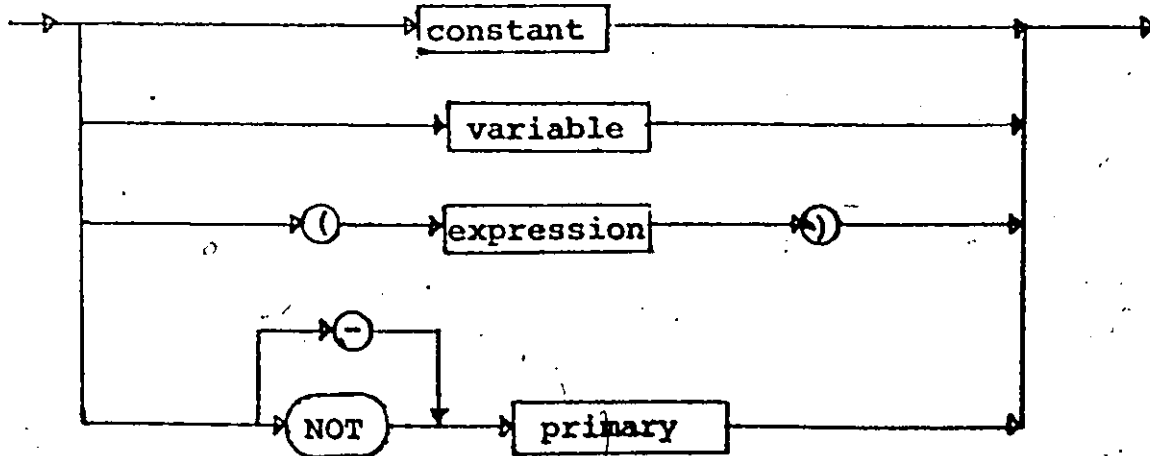
type



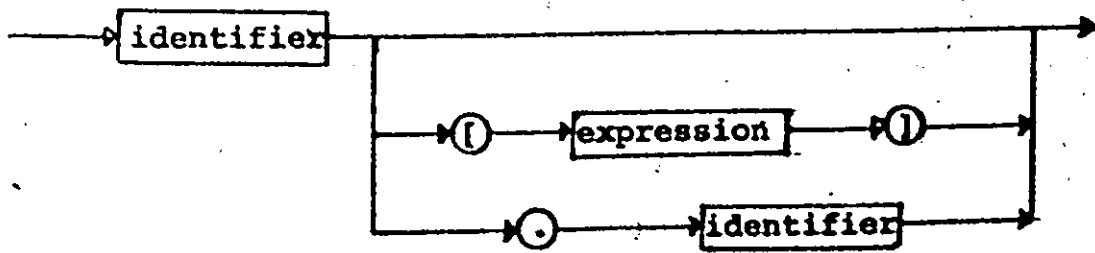
expression



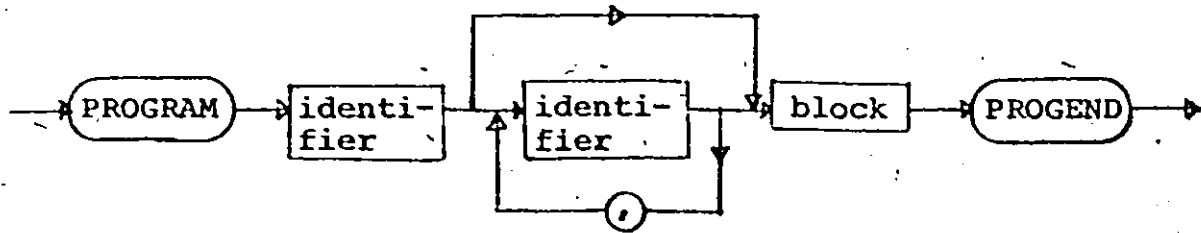
primary



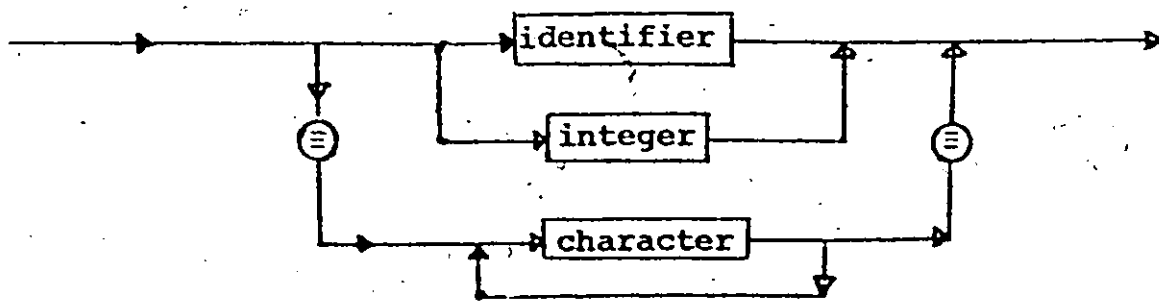
variable



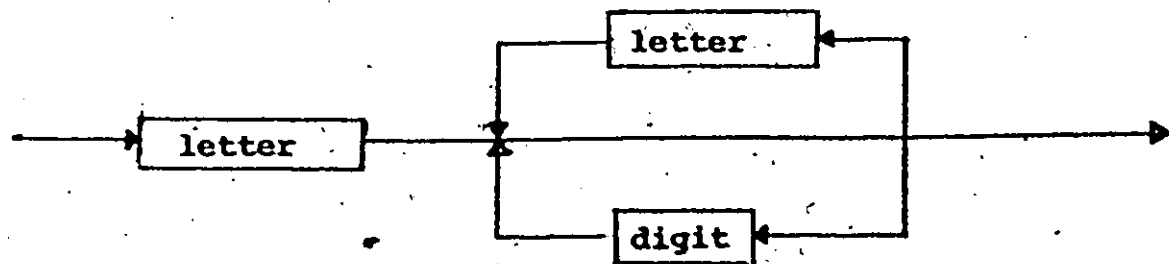
program



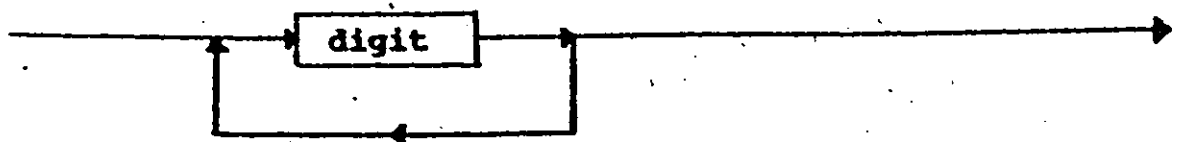
constant



identifier



integer



APPENDIX F  
LISTING OF I/O ROUTINES

```

0001 ASMB,L,R      NAM DR1,7
0002
0003 *
0004 *
0005 *
0006 *
0007 *
0008 *
0009 *
0010 *
0011 *
0012
0013
0014
0015 OPFLG
0016 GET
0017
0018
0019
0020
0021
0022
0023
0024
0025
0026
0027
0028
0029

THIS ROUTINE GETS THE INPUT IN THE BUFFER
RDCON----CONJD
READQ----REQUEST CODE = 1
BUFFS----BUFFER ADDRESS
BUFLS----BUFFER LENGTH
INPUT-----
B-REGISTER---ADDRESS OF THE TOP OF THE BUFFER

EXT EXEC
ENT GET
EQU 1000 /
EQU +113
NOP
STA BUFFS
LDA GET
INA
LDB 0,I
STB RDCON
JSB EXSET
JSB EXEC
DEF *+5
DEF READQ
DEF RDCON
DEF BUFFS,I
DEF BUFLS
JMP GET,I

STORE BUFFER ADDRESS
GET THE
LOGICAL UNIT
NUMBER IN RDCON
    
```

\* \* \* \* \*

```
0030      NOP      EXEC
0031      JSB      EXEC
0032      DEF      **2
0033      DEF      **2
0034      JMP      EXSET,I
0035      DEC      -19
0036      READQ   READQ
0037      RDCON   RDCON
0038      BUFLS   BUFLS
0039      OCT     72
0040      END     GET
**** LIST END ****
```

```

0001 ASMB,L,R      NAM DR2,7
0002
0003 *
0004 * THIS ROUTINE WRITES A VARIABLE LENGTH
0005 * BUFFER ONTO THE OUTPUT DEVICE
0006 * INPUT-----
0007 * A-REGISTER-----BUFFER LENGTH
0008 * B-REGISTER-----BUFFER ADDRESS
0009 *
0010 EXT EXEC
0011 ENT PUT
0012 EQU 100B
0013 EQU +113
0014 NOP
0015 STA BUFLS
0016 *
0017 * STORE BUFFER LENGTH
0018 *
0019 *
0020 *
0021 * STORE BUFFER ADDRESS
0022 *
0023 *
0024 * LDA PUT
0025 * INA LOGICAL UNIT
0026 * LDB 0,I NUMBER IN B-REGISTER
0027 * STB WRCON STORE IN WRCON
0028 * CLA
0029 * CLB
0030 * LDB OPFLG
0031 * JSB EXSET
0032 * JSB EXEC
0033 * DEF ++5
0034 * DEF WRITQ
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```





CONWD  
GET THE BUFFER ADDRESS.  
GET BUFFER LENGTH  
RETURN TO CALLING PROGRAM

0034	WRCON	DEF	WRCON
0035	BUFFS,I	DEF	BUFFS,I
0036	BUFLS	DEF	BUFLS
0037	PUT,I	JMP	PUT,I
0038	EXSET	NOP	
0039	EXEC	JSB	EXEC
0040	**+2	DEF	**+2
0041	**+2	DEF	**+2
0042	EXSET,I	JMP	EXSET,I
0043	-19	DEC	-19
0044	2	DEC	2
0045	WRCON	BSS	WRCON
0046	BUFFS	BSS	BUFFS
0047	BUFLS	BSS	BUFLS
0048	PUT	END	PUT
		****	LIST END ****

```

0001 ASMB,L,R      NAM DR3,7      *
0002 ENT ALLOK    *
0003 *           *
0004 *           *
0005 *           *
0006 *           *
0007 *           *
0008 *           *
0009 *           *
0010 *           *
0011 *           *
0012 *           *
0013 *           *
0014 *           *
0015 ALLOK      *
0016 *           *
0017 *           *
0018 *           *
0019 *           *
0020 *           *
0021 *           *
0022 *           *
0023 *           *
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0032 *           *
0033 *           *

```

THIS ROUTINE GETS THE ASCII CHARACTER FROM INPUT  
 BUFFER AND CONVERTS IT INTO INTEGER IF NECESSARY  
 INPUT-----  
 B-REGISTER-----BUFFER POINTER  
 E-REGISTER-----ZERO IF INTEGER  
 , ONE OTHERWISE  
 OUTPUT-----  
 A-REGISTER-----CONTAINS INTEGER OR CHARACTER  
 B-REGISTER-----CONTAINS NEW BUFFER POINTER

NOP  
 STB TEMPY  
 LDA BLANK  
 STA CHAR  
 SEZ L4  
 JMP L4  
 CLA  
 STA SIGN  
 STA DIG  
 LDA CRSW  
 STA CRFG  
 JSB NTBLK  
 CPA COMMA  
 JMP L1  
 CPA MINUS  
 RSS  
 JMP L2  
 LDB MINUS  
 STB SIGN

CHARACTER INPUT  
 GET NON-BLANK CHARACTER  
 YES  
 YES

LI

```

0034          JSB  GETCR
0035          JSB  DIGIT
0036          JSB  NTBLK
0037          CPA  COMMA
0038          JMP  L3
0039          LDB  DIG
0040          BLS, BLS
0041          ADB  DIG
0042          BLS
0043          STB  DIG
0044          JMP  L2
0045          LDA  DIG
0046          LDB  SIGN
0047          SZB
0048          CMA, INA
0049          LDB  TEMPY
0050          JMP  ALLOK, I
0051          *   GETCR  GETS NEXT CHARACTER
0052          *   NOP
0053          LDB  CRFG
0054          SSB
0055          JMP  GETC2
0056          LDA  TEMPY, I
0057          AND  CH2
0058          ALF, ALF
0059          *   GET - FIRST CHARACTER
0060          *   LDB  CRFG
0061          RBR
0062          STB  CRFG
0063          JMP  GETCR, I

          GET NEXT CHARACTER
          FORM THE DIGITS

          YES
          IF NOT COMMA
          MULTIPLY
          BY
          10
          STORE THE NUMBER IN DIG
          GET MORE DIGIT

          IF NEGATIVE NUMBER, TAKE 2'S COMPLEMENT

```

```

0064 * GETC2
0065 GET NEXT SECOND CHARACTER
0066 LDA TEMPV, I
0067 AND CH1
0068 LDB TEMPV
0069 INB
0070 STB TEMPV
0071 JMP GETC1
0072 * NTBLK GET NON-BLANK CHARACTER
0073 NTBLK
0074 NOP
0075 JSB GETCR
0076 CPA BLANK
0077 RSS
0078 JMP NTBLK, I
0079 JMP NTBL1
0080 * DIGIT
0081 DIGIT GETS THE DIGIT FROM ASCII CHARACTER
0082 NOP
0083 LDB SIXTY
0084 CMB, INB
0085 ADB 0
0086 ADB DIG
0087 STB DIG
0088 JMP DIGIT, I
0089 * CHARACTER INPUT
0090 CHARACTER INPUT
0091 JSB NTBLK
0092 CPA COMMA
0093 JMP L5
0094 STA GHAR
0095 JSB NTBLK
0096 CPA COMMA
0097 JMP L6
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```

0094          STA CHAR
0095          JMP L5
0096          LDA CHAR
0097          LDB TEMPV
0098          JMP ALLOK, ICA
0099          OCT 52525
0100          OCT 53
0101          OCT 54
0102          BSS 1
0103          BSS 1
0104          BSS 1
0105          BSS 1
0106          BSS 1
0107          OCT 077400
0108          OCT 177
0109          OCT 40
0110          OCT 60
0111          END ALLOK
**** LIST END ****

```

```

0094          CRSW
0095          MINUS
0096          L6
0097          COMMA
0098          TEMPV
0099          SIGN
0100          DIG
0101          CRFG
0102          CHAR
0103          CH2
0104          CH1
0105          BLANK
0106          SIXTY
0107          ****
0108          LIST END
0109          ****
0110          ****
0111          ****

```

```

0001      ASMB, L, R
0002      NAM   DR4, 7
0003      ENT  ALLOC
0004
0005
0006
0007
0008
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0010
0011
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0014
0015
0016
0017
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      THIS ROUTINE CONVERTS AN INTEGER INTO
      ASCII CHARACTER AND PUT IT INTO THE
      BUFFER. IF CHARACTER IS INPUT, IT IS PUT INTO
      THE BUFFER STRAIGHT.

      INPUT
      A-REGISTER-----INTEGER OR CHARACTER
                        TO BE WRITTEN OUT.
      B-REGISTER-----BUFFER POINTER.
      E-REGISTER-----IT IS ZERO FOR INTEGER
                        ONE OTHERWISE.

      OUTPUT
      A-REGISTER-----BUFFER LENGTH
      B-REGISTER-----NEW BUFFER POINTER

      NOP
      STA  TEMPT      STORE INTEGER OR CHARACTER
      STB  TEMPA      STORE BUFFER POINTER
      STB  TEMPA
      CLB
      STB  BUFL      INITIALIZE BUFFER LENGTH
      STB  KOUNT     AND KOUNT TO ZERO

```

\* \* \* \* \*

```

0032 SEZ CHARACTER INPUT
0033 JMP LAB6
0034 LDB BLANK
0035 STB SIGN
0036 SSA, RSS
0037 JMP LAB1
0038 CMA, INA
0039 STA TEMPT
0040 LDB F55
0041 STB SIGN
0042 LDA TEMPT
0043 CLB
0044 DIV TEN
0045 SZA, RSS
0046 JMP LAB2
0047 STA TEMPT
0048 LDA TEMP2
0049 ADA KOUNT
0050 ADB SIXTY
0051 STB 0, I
0052 * INCREMENT THE COUNT OF THE DIGIT *
0053 LDA KOUNT
0054 INA
0055 STA KOUNT
0056 JMP LAB1
0057 * LAB2 GET NEXT DIGIT
0058 LDA TEMP2
0059 ADA KOUNT
0060 ADB SIXTY
0061 * STORE LAST DIGIT IN TEMPORARY LOCATION *
0062 STB 0, I
0063 * STORE THE SIGN *
LDA SIGN

```

LAB1

LAB2

```

0064 LAB3 ALF,ALF *
0065 STA NUMB *
0066 JSB INBFL
0067 IOR NUMB
0068 * PUT SIGN, AND THE DIGITS IN THE BUFFER **
0069 * DECREMENT THE KOUNT TO GET THE PROPER DIGITS **
0070 JSB PUTBL
0071 CCA
0072 ADA KOUNT
0073 STA KOUNT
0074 SSA
0075 JMP LAB5
0076 SZA, RSS
0077 JMP LAB4
0078 JSB INBFL
0079 CCB
0080 ADB KOUNT
0081 STB KOUNT
0082 * GET NEXT DIGIT TO BE PUT IN THE BUFFER *
0083 JMP LAB3
0084 LAB4 JSB INBFL
0085 ALF,ALF
0086 IOR BLANK
0087 JSB PUTBL
0088 * PUT TWO BLANKS AFTER EACH OUTPUT *
0089 LAB5 LDA BLANK
0090 ALF,ALF
0091 IOR BLANK
0092 JSB PUTBL
0093 JMP ALLOC, I
0094 *
0095 * CHARACTER OUTPUT **

```



```

0096 *
0097 LAB6
0098
0099
0100
0101
0102
0103
0104 *
0105 *
0106 *
0107 *
0108 PUTBL
0109
0110
0111
0112
0113
0114
0115
0116 *
0117 *
0118 *
0119 *
0120 INBFL
0121
0122
0123
0124
0125 TEMP2
0126 TEMP

LDA BLANK
ALF, ALF
IOR BLANK
JSB PUTBL
LDA TEMPT
JSB PUTBL
JMP LAB5

STORE THE CONTENTS OF A-REGISTER IN THE BUFFER
UPDATE BUFFER LENGTH AND BUFFER POINTER

NOP
LDB BUFL
ADB TEMPA
STA 1, I
LDA BUFL
INA
STA BUFL
JMP PUTBL, I

INCREMENT THE COUNTER--KOUNT-- FOR THE DIGIT
ADJUST THE ADDRESS OF TEMPORARY LOCATION

NOP
LDB KOUNT
ADB TEMP2
LDA 1, I
JMP INBFL, I
DEF TEMP
BSS 20

STORE BLANK IN BUFFER
LOAD THE CHARACTER
STORE THE CHARACTER IN BUFFER

```

0127	TEMPA	BSS	1
0128	TEMPT	BSS	1
0129	SIGN	BSS	1
0130	KOUNT	BSS	1
0131	BUFL	BSS	1
0132	F55	OCT	55
0133	SIXTY	OCT	60
0134	TEN	DEC	10
0135	BLANK	OCT	40
0136	NUMB	BSS	1
0137		END	ALLOC

\*\*\*\* LIST END \*\*\*\*

APPENDIX G

LISTING OF THE HPCOM COMPILER

\*\*\*\*\* HPCOM \*\*\*\*\*  
\*  
\* AUTHOR : DILIP K. ROY.  
\*  
\* LANGUAGE : PASCAL (DECEMBER, 1972 VERSION)  
\*  
\* MACHINE : CDC-6400  
\*  
\* PLACE : MCMASTER UNIVERSITY  
\* HAMILTON, ONTARIO.  
\*  
\* DATE : JUNE 12, 1973  
\*  
\*\*\*\*\*

HPCOM IS WRITTEN FOR PROGRAMMING LANGUAGE 2100 (WRITTEN FOR THE MINICOMPUTER HP/2100A). THE COMPILER PRODUCES RELOCATABLE BINARY CODE SUITABLE FOR STANJAKU RELOCATABLE LOADER OF THE HP/2100A COMPUTER.

A PL/2100 PROGRAM ALWAYS STARTS WITH A PROGRAM CARD, NAME BY

PROGRAM NAME <PROGRAMNAME> (<OPTIONAL PARAMETERS>)

WHERE <OPTIONAL PARAMETERS> ARE :

L : WRITES OUT CODE STACK;

B : WRITES OUT THE RELOCATABLE CODE ON THE PUNCHES OUT THE RELOCATABLE CODE ON THE FILE

##HPOIP##

TO RUN HPCOM CONTROL CARDS NEEDED ARE :

1 : JOB CARD

2 : ATTACH (PASCAL, ID=GGQPASCAL, MR=1, CY=20)

3 : PASCAL (HPDIP)

4 : REQUEST (HPDIP, P8=CAB)

5 : DISPOSE (HPDIP)

6 : NOTE : TO PUNCH THREE OF THE CARDS NEEDED

NOTE : TO PUNCH THREE OF THE CARDS NEEDED

CODE ON HP COMMANDS NEEDED ARE :

PL/2100 PROGRAM DIRECTIVE TO STORE THE RELOCATABLE PROGRAM AND LAOER LOAD IT

PROGRAM DIRECTIVE TO LOAD THE PROGRAM DIRECTLY

PROGRAM DIRECTIVE TO RUN THE PROGRAM.

TO RUN A

1 :

2 :

A →

PSA,X++ CONST

CODMAX = 1000,  
KPMAX=50,  
MAXLEVEL = 10,  
JMPMAX=49,  
MAXEXLABS=10,  
UNUMAX=1000,  
CSYMAX = 40,  
FILLIMIT = 10,  
WORDLENGTH = 16,  
DISPLIMIT = 20,  
ALFALAB = 10,  
MAX10 = 77778 ;

TYPE

```

BITS = 0..1 ;
RTYP = 0..5 ;
BITRANGE = 0..16 ;
RG3 = 0..3 ;
ADDRESS = 0..1777B ; 1777B ! INTEGER ;
SHRINT3 = -1777B .. 1777B ! 1777B ! INTEGER ;
ARR = ARRAY [1..3] OF CHAR ;
AR = ARRAY [1..10] OF CHAR ;
DELARR = ARRAY [1..54, 1..10] OF CHAR ;
INTARR = ARRAY [1..39, 1..10] OF CHAR ;
OPIPHR = ( INOPER, PURER, POS, NEGR ) ;
YOKLASS = ( TYPES, KONST, FIELD, TAGFIELD,
            DUMHYCLASS ) ;
TYPEFORM = ( NUMERIC, SYMBOLIC, ARRAYS, RECORDS, FILES,
            REGISTER, MAL ) ;
IDKINDS = ( ACTUAL, FOR, WITH ) ;
WHERE = ( BLOCK, WITH, LVAL, LCOND ) ;
ATTIRKIND = ( VARBL, SVAL, LVAL, LCOND ) ;
ARGO3B = ARRAY [1..3] OF
  RECORD
  LOC ;
  INST, ADDR ;
  INTEGER ;
  TYP, RTYP ;
  MREF ;
  BOOLEAN ;
  END ;
  ATR = RECORD
  TYP, RTYP ;
  INTEGER ;
  END ;
  CASE KIND ;
  ATTIRKIND OF
  ACCESS, (ORCI, INDRCI, INXD) ;
  BRG, RG3 ;
  UP, LMI ;
  INTEGER ;
  CASE PKCO ;
  BOOLEAN OF
  FALSE ;
  TRUE ;
  (BITADR, BITSZ, BITRANGE) ;
  SVAL ;
  (VAL ; INTEGER) ;
  LVAL ;
  (CTERM ; INTEGER) ;
  LCOND ;
  (JMP ; 0..3 ; ARITH ; BOOLEAN)
  END ;

```

VAR

```

HPAS  ! ARCODE ! CODE STACK ! USED TO INDEX CODE STACK !
HCA  ! INTEGER ! USED TO INDEX CODE STACK !
BFLC  ! INTEGER ! THE LOCATION WHERE BUFFER ADDRESS IS STORED.
BFLC-- ! NUMBER OF RESERVED LOCATIONS AT THE TOP OF THE
        ! DATA STACK !
HPDIPL  ! FILE OF RELOCATION ROUTINES USED FOR THE RELOCATION
        ! OF CODE !
PRNAM  ! (1..60) OF INTEGER ;
        ! ARRAY OF INTEGERS ;
        ! TRANSFER TO IN PUNCH BIN, RLISIBIN ! BOOLEAN ! THESE VARIABLES
        ! OPTIONAL PARAMETERS ARE SET TRUE IF OPTIONAL PARAMETERS
        ! THESE VARIABLES ARE PRESENT.
XPRYEXYM ! INTEGER ! USED FOR EXTERNALS !
EXTX  ! ARRAY (1..30) OF
        ! RECORD XNAM ! XSYM ! INTEGER END !
        ! EXTX IS USED TO STORE THE NAME OF THE EXTERNALS !
LABTAB ! ARRAY (1..MAXLAB) OF
        ! PACKED RECORD !
        ! CLABIX ! INTEGER ! SO FAR IN THE BODY OF THE PROCEDURE
        ! ACTUALLY BEING COMPILED, TOGETHER WITH INFORMATION WHETHER
        ! LABEL DEF. ((LABEL)) ALREADY FOUND OR NOT (OLD2)
        ! IN THE FORMER CASE FLD3 CONTAINS THE CORRESPONDING
        ! ADDRESS WHERE IN THE LATTER CASE FLD3 CONTAINS AN INDEX
        ! INTO UNDLAB WHERE THE OCCURENCE ARE CHAINED !
STORE  ! ARRAY (1..KPMAX) OF
        ! RECORD !
        ! SIRP ! INTEGER ! STPL ! INTEGER !
        ! END !
        ! ARRAY (1..10) OF
        ! RECORD !
        ! SIOP ! INTEGER ! SICL ! INTEGER !
        ! END !
        ! RPHAX, RP, KX ! INTEGER ! USED WITH STORE AND STRE !
        ! STORE AND STRE ARE USED BY ROUTINES STIMP AND LDIRP.
        ! THEY STORE COMPILE TIME INFORMATION TO BE USED TO
        ! UPDATE THE CODE STACK. !

```



```

ERRLIST : ARRAY (1..10) OF
PACKED
RECORD POS,NMR : SHRINT END ;
ERRNRS : ARRAY (0..3) OF SET OF 0..3 ;
ERRINX : SHRINT ;
ERRORLIST : ARRAY (1..10) OF INTEGER ;
ERRRKOUNT : INTEGER ;
ERR : BOOLEAN ;
CHCNT : INTEGER ;
ERRLIST : IN INTEGER ;
ERRLIST : CONTAINS THE POSITION AND NUMBERS OF THE
ERRON ONE LINE AND
ERRINX = TOP OF ERRLIST, PRINTED ERROR MARK (*),
POS = POSITION OF LAST READ CHARACTER,
CHCNT = POSITION OF LAST CHCNT, EOLFLAG --- ALL ARE
USED BY NEXT AND TRACK OF THE NUMBER OF ERRORS
ERRRKOUNT -- IN A PROGRAM.
ERRORLIST : KEEPS THE ERROR NUMBER
ERR ---- IS SET WHENEVER ERROR HAS BEEN CALLED AND IS
TESTED (AND AND RESET) BY SEVERAL PROCEDURE +

JMPTAB : ARRAY (0..JMPTAB) OF INTEGER ;
JMPBX : SHRINT ;
TRANSFER VECTOR FOR CALLS OF FORWARD DECLARED
PROCEDURES AND GOTO STATEMENTS LEADING OUT OF PRO ; +

CSTTB : ARRAY (1..CSTMAX) OF
RECORD VALU:INTEGER; INX:SHRINT END;
LCX : SHRINT ;
CONTAINS ALL CONSTANTS OCCURRING IN THE
PROCEDURES ACTUALLY BEING COMPILED TOGETHER
AN INDEX INTO UNDLAB WHERE THEIR OCCURENCE IN
THE CODE OF THIS PROCEDURE ARE CHAINED +

EXTAB : ARRAY (1..MAXEXLABS) OF
PACKED
RECORD EXVAL,JMPBX : SHRINT END;
CXTABIX : SHRINT ;
CONTAINS EXPLICITLY DECLARED LABELS OF ALL
PROCEDURES NOT YET CLOSED, TOGETHER WITH THEIR
CORRESPONDING INDEX INTO JMPTAB +

UNDLAB : ARRAY (1..UNDMAX) OF
RECORD SUCC,PLACE:INTEGER END;

```



FSTIX ; INTEGER ; POINTING TO CONTEXTTABLE ;  
\* AUXILIARY VARIABLES , USED IN MAIN PROG. AND SEVERAL

PROF ; DOUBLE ;  
INDEX ; SHRINT ; SHRINT ;  
NEXT ; CIP ; ARRAY ; (0 ; DISPLAY ;  
DISPLAY RECORD CASE OCCUR ;  
BLOCK ; (CUI ; INTEGER ; CLEV ; RG3) ;  
WITH ; (VOSP ; INTEGER) ;

END ; INTEGER ;  
TOP ; DISX ; INTEGER ;  
LEVEL ; INTEGER ;  
\* CONTEXTTABLE CONTAINS THE IDENTIFIERS LOG WITH  
\* OTHER ATTRIBUTES



```

PROG--- CONTAINS PROGRAM
PROGEND--- CONTAINS PROGEND
WNO--- INITIALIZE TO ALL THE RESERVED WORDS OF PL/2100
SYMNO, MCL--- CONTAINS IN INTEGER TOKENS NO AND CHARACTER SET
SYMNO, SYMCL--- TOKENS IN NO AND CL FOR BCD CHARACTER SET
INITNAM--- OF THE RESERVED WORDS OF PL/2100
ASCII CODE--- USED TO INITIALIZE THE CONTEXT TO THE BCD CHARS
DD--- USED IN ROUTINES FOR RELOCATION

```

```

PROGEND ; AR ;
BLANK ; AR ;
SPLIT ; STAT ; CHAR ;
ERRCL ; TERRCL ; ARRAY [1..52] OF INTEGER ;
WNO ; DEL ; ARRAY [1..54] OF INTEGER ;
SYMNO ; SYMCL ; ARRAY [1..52] OF INTEGER ;
INITNAM ; IN ; ARRAY [1..52] OF INTEGER ;
ASCII CODE ; IN ; ARRAY [1..52] OF INTEGER ;
DD ; ARRINT3 ;

```









\*\*\*\*\*  
 RESERVED WORD AND SYMBOLS USED IN HP-2100  
 SYMBOL NUMBERS  
 \*\*\*\*\*

RESERVED WORD SYMBOL	NUMBERS	CONST
INTEGER	1	CONST
CHAR	2	CONST
NOT	3	
/	4	
AND	5	
DIV	6	
MOD	7	
+	8	
-	9	
>	10	
OR	11	
<	12	
LT	13	
LE	14	
GE	15	
GT	16	
NE	17	
=	18	
EQ	19	
LT	20	
LE	21	
GE	22	
GT	23	
NE	24	
=	25	
EQ	26	
LT	27	
LE	28	
GE	29	
GT	30	
NE	31	
=	32	
EQ	33	
LT	34	
LE	35	
GE	36	
GT	37	
NE	38	
=	39	
EQ	40	
LT	41	
LE	42	
GE	43	
GT	44	
NE	45	
=	46	
EQ	47	
LT	48	
LE	49	
GE	50	
GT	51	
NE	52	
=	53	
EQ	54	
LT	55	
LE	56	
GE	57	
GT	58	
NE	59	
=	60	
EQ	61	
LT	62	
LE	63	
GE	64	
GT	65	
NE	66	
=	67	
EQ	68	
LT	69	
LE	70	
GE	71	
GT	72	
NE	73	
=	74	
EQ	75	
LT	76	
LE	77	
GE	78	
GT	79	
NE	80	
=	81	
EQ	82	
LT	83	
LE	84	
GE	85	
GT	86	
NE	87	
=	88	
EQ	89	
LT	90	
LE	91	
GE	92	
GT	93	
NE	94	
=	95	
EQ	96	
LT	97	
LE	98	
GE	99	
GT	100	
NE	101	
=	102	
EQ	103	
LT	104	
LE	105	
GE	106	
GT	107	
NE	108	
=	109	
EQ	110	
LT	111	
LE	112	
GE	113	
GT	114	
NE	115	
=	116	
EQ	117	
LT	118	
LE	119	
GE	120	
GT	121	
NE	122	
=	123	
EQ	124	
LT	125	
LE	126	
GE	127	
GT	128	
NE	129	
=	130	
EQ	131	
LT	132	
LE	133	
GE	134	
GT	135	
NE	136	
=	137	
EQ	138	
LT	139	
LE	140	
GE	141	
GT	142	
NE	143	
=	144	
EQ	145	
LT	146	
LE	147	
GE	148	
GT	149	
NE	150	
=	151	
EQ	152	
LT	153	
LE	154	
GE	155	
GT	156	
NE	157	
=	158	
EQ	159	
LT	160	
LE	161	
GE	162	
GT	163	
NE	164	
=	165	
EQ	166	
LT	167	
LE	168	
GE	169	
GT	170	
NE	171	
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EQ	173	
LT	174	
LE	175	
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GT	177	
NE	178	
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EQ	180	
LT	181	
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GE	183	
GT	184	
NE	185	
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EQ	187	
LT	188	
LE	189	
GE	190	
GT	191	
NE	192	
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EQ	194	
LT	195	
LE	196	
GE	197	
GT	198	
NE	199	
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EQ	201	
LT	202	
LE	203	
GE	204	
GT	205	
NE	206	
=	207	
EQ	208	
LT	209	
LE	210	
GE	211	
GT	212	
NE	213	
=	214	
EQ	215	
LT	216	
LE	217	
GE	218	
GT	219	
NE	220	
=	221	
EQ	222	
LT	223	
LE	224	
GE	225	
GT	226	
NE	227	
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EQ	229	
LT	230	
LE	231	
GE	232	
GT	233	
NE	234	
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EQ	236	
LT	237	
LE	238	
GE	239	
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EQ	243	
LT	244	
LE	245	
GE	246	
GT	247	
NE	248	
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EQ	250	
LT	251	
LE	252	
GE	253	
GT	254	
NE	255	
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EQ	257	
LT	258	
LE	259	
GE	260	
GT	261	
NE	262	
=	263	
EQ	264	
LT	265	
LE	266	
GE	267	
GT	268	
NE	269	
=	270	
EQ	271	
LT	272	
LE	273	
GE	274	
GT	275	
NE	276	
=	277	
EQ	278	
LT	279	
LE	280	
GE	281	
GT	282	
NE	283	
=	284	
EQ	285	
LT	286	
LE	287	
GE	288	
GT	289	
NE	290	
=	291	
EQ	292	
LT	293	
LE	294	
GE	295	
GT	296	
NE	297	
=	298	
EQ	299	
LT	300	
LE	301	
GE	302	
GT	303	
NE	304	
=	305	
EQ	306	
LT	307	
LE	308	
GE	309	
GT	310	
NE	311	
=	312	
EQ	313	
LT	314	
LE	315	
GE	316	
GT	317	
NE	318	
=	319	
EQ	320	
LT	321	
LE	322	
GE	323	
GT	324	
NE	325	
=	326	
EQ	327	
LT	328	
LE	329	
GE	330	
GT	331	
NE	332	
=	333	
EQ	334	
LT	335	
LE	336	
GE	337	
GT	338	
NE	339	
=	340	
EQ	341	
LT	342	
LE	343	
GE	344	
GT	345	
NE	346	
=	347	
EQ	348	
LT	349	
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GT	352	
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EQ	355	
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GE	358	
GT	359	
NE	360	
=	361	
EQ	362	
LT	363	
LE	364	
GE	365	
GT	366	
NE	367	
=	368	
EQ	369	
LT	370	
LE	371	
GE	372	
GT	373	
NE	374	
=	375	
EQ	376	
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LE	378	
GE	379	
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NE	381	
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EQ	383	
LT	384	
LE	385	
GE	386	
GT	387	
NE	388	
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EQ	390	
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LE	392	
GE	393	
GT	394	
NE	395	
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EQ	397	
LT	398	
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GT	401	
NE	402	
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EQ	404	
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GE	407	
GT	408	
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EQ	411	
LT	412	
LE	413	
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GT	415	
NE	416	
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EQ	418	
LT	419	
LE	420	
GE	421	
GT	422	
NE	423	
=	424	
EQ	425	
LT	426	
LE	427	
GE	428	
GT	429	
NE	430	
=	431	
EQ	432	
LT	433	
LE	434	
GE	435	
GT	436	
NE	437	
=	438	
EQ	439	
LT	440	
LE	441	
GE	442	

24	4	BRS
24	5	RBR
24	6	RRL
24	7	RAL
24	8	RBL
24	9	ALF
24	10	BLF
24	11	IOR
24	12	X
25		BEGIN
26		END
27		IF
28		THEN
29		ELSE
30		ENDIF
31		REPEAT
32		UNTIL
33		WHILE
34		DO
35		FOR
36		TO
37		UNTIL
38		DO
39		GOTO
40		TYPE
41		ARRAY
41		ARECORD
41		FILE
42		LABEL
43		CONST
44		FUNCTION
45		PROCEDURE
46		PARAMETER
47		VALUE
48		WITH
49		REGISTER
50		SYN
51		REGA
52		REGB
52		REG

.....

```

*****
PROCEDURE LINES (N: INTEGER) ;
VAR I: INTEGER ;
FOR I:=1 TO N DO WRITE(EOL,E I)
END LINES ;
*****
PROCEDURE OUTCH(C:CHAR) ; PUT(OUTPUT) END ;
BEGIN OUTPJT I:=C ;
*****
PROCEDURE OUTO(VAL:INTEGER) ;
PRINT VAL OUTALONE BLANK AND 6 DIGITS ;
VAR K,M: INTEGER ; J,S: INTEGER ;
BEGIN IF VAL=0 THEN FOR J:=1 TO 6 DO OUTCH(E0E) ELSE
BEGIN M:=78; S:=45;
FOR J:=15 TO 20 DO
BEGIN K:=VAL; APPEND(K,S,M) ;
OUTCH(ORD(E0E)-K) ;
S:=S+3 ;
END ;
END ;
OUTO ;
END ;
*****

```

```

PROCEDURE COMP (VAR A,B:AR; VAR COMPARE:BOOLEAN) ;
  * COMPARE TWO ARRAYS A AND B, COMPARE IS TRUE IF THEY ARE
  EQUAL, OTHERWISE IT IS FALSE. ↓
VAR
  K: INTEGER ;

```

```

BEGIN
  K:= 1 ;
  IF A[K] = B[K] THEN
    BEGIN
      K:= K+1 ;
      IF K ≤ 10 THEN GOTO 1
    ELSE COMPARE := TRUE ;
    END
  ELSE COMPARE := FALSE ;

```

```

*****
END COMP ;
*****
PROCEDURE SPARE (VAR A:AR; VAR B:DELARRY; M: INTEGER ;
  * COMPARE TWO ARRAYS A AND B, COMPARE IS TRUE IF THEY ARE
  EQUAL, OTHERWISE IT IS FALSE. ↓

```

```

VAR
  K: INTEGER ;
BEGIN
  K:= 1 ;
  IF A[K] = B[M,K] THEN
    BEGIN
      K:= K+1 ;
      IF K ≤ 10 THEN GOTO 1
    ELSE COMPARE := TRUE ;
    END
  ELSE COMPARE := FALSE ;

```

```

*****
END SPARE ;
*****

```

```

FUNCTION LOG2(VAL1 : INTEGER) : INTEGER ;
VAR E : INTEGER ; VAL : INTEGER ;
BEGIN
  E := 0 ;
  WHILE VAL > 0 DO
    BEGIN
      VAL := VAL DIV 2 ; E := E + 1 ; END ;
    END ;
  LOG2 := E ;
END ;
PROCEDURE MULOPT(VAL1 : INTEGER ; VAR EXP1, EXP2 : INTEGER ;
  OPT : PUREP
  ; OPT : POSP
  ; OPT : NEGP
  ; OPT : NOOPT) ;
  VAR VAL : INTEGER ;
  BEGIN
    EXP1 := 2 ** EXP1 ;
    EXP2 := 2 ** EXP2 ;
    IF VAL = EXP1 THEN
      VAL := VAL * EXP2 ;
    ELSE
      VAL := VAL / EXP2 ;
    END ;
  END ;
VAR E1, E2 : INTEGER ;
BEGIN
  EXP1 := 0 ; EXP2 := 0 ;
  IF VAL > 0 THEN
    BEGIN
      WHILE VAL > 0 DO
        BEGIN
          VAL := VAL DIV 2 ; E1 := E1 + 1 ; END ;
        END ;
      BEGIN
        WHILE VAL > 0 DO
          BEGIN
            VAL := VAL DIV 2 ; E2 := E2 + 1 ; END ;
          END ;
        END ;
      BEGIN
        REPEAT VAL := VAL DIV 2 ; E2 := E2 + 1 ;
          UNTIL VAL = 0 THEN OPT := NOOPT ELSE
            BEGIN OPT := NEGP ;
              EXP2 := E2 ; EXP1 := E1 ;
            END ;
          END ;
        BEGIN
          REPEAT VAL := VAL DIV 2 ; E2 := E2 + 1 ;
            UNTIL VAL = 0 THEN OPT := NOOPT ELSE
              BEGIN OPT := POSP ;
                EXP2 := E2 ; EXP1 := E1 ;
              END ;
            END ;
          END ;
        END ;
      END ;
    END ;
  END ;

```



```

PROCEDURE PRIERR ; SHRINT ;
VAR I,K,POSIT ;
BEGIN
  OUTCH(=) ; DO OUTCH(=E) ;
  FOR I:=1 TO 4 DO
    I:=I+1 ; K:=1 ;
  REPEAT
    POSIT := ERRLIST[K].POS ;
    WHILE I LT POSIT DO
      BEGIN
        OUTPUT := E ; PUT(OUTPUT) ;
        I := I+1 ; K := K+1 ;
      END ;
    OUTPUT := E ; PUT(OUTPUT) ;
    I := I+1 ; K := K+1 ;
  UNTIL K GT ERRINX ;
  WHILE I LE 80 DO
    BEGIN
      OUTPUT := E ; PUT(OUTPUT) ;
      I := I+1 ;
    END ;
  FOR I := 1 TO ERRINX DO WRITE(ERRLIST[I].NMR) ;
  OUTCH(=) ; ERRINX:=0 ; POSIT:=0 ;
  END ; PRIERR ; *****
*****
PROCEDURE ERROR(I:SHRINT) ;
BEGIN
  IF ERRKJNI EQ 0 THEN
    BEGIN
      ERRORCOUNT := ERRORCOUNT+1 ;
      ERRORLIST[ERRORCOUNT] := I ;
    END
  ELSE
    BEGIN
      J:=0 ;
      REPEAT
        J:=J+1 ;
        ERRLIST[J].EQ I THEN GOTO 1 ;
      UNTIL J EQ ERRORCOUNT ;
      ERRORCOUNT := ERRORCOUNT+1 ;
      IF ERRORCOUNT EQ 10 THEN GOTO 1 ;
      ERRLIST[ERRORCOUNT] := I ;
    END ;
  ERRNRSI DIV 321 := ERRNRSI DIV 321 OR (I MOD 321) ;
  IF ERRINX GT 9 THEN
    WITH ERRLIST(10) DO

```





```

*****
PROCEDURE ERRMSG ; INTEGER I ; *****
VAR I, J, K, L, M, N, O, P, Q, R, S, T, U, V, W, X, Y, Z ; *****
PROCEDURE ERRMSG1 ; *****
BEGIN
  CASE
  OF
  1 : TYPE OF ; *****
  2 : TYPE OF ; *****
  3 : TYPE OF ; *****
  4 : TYPE OF ; *****
  5 : TYPE OF ; *****
  6 : TYPE OF ; *****
  7 : TYPE OF ; *****
  8 : TYPE OF ; *****
  9 : TYPE OF ; *****
  10 : TYPE OF ; *****
  11 : TYPE OF ; *****
  12 : TYPE OF ; *****
  13 : TYPE OF ; *****
  14 : TYPE OF ; *****
  15 : TYPE OF ; *****
  16 : TYPE OF ; *****
  17 : TYPE OF ; *****
  18 : TYPE OF ; *****
  19 : TYPE OF ; *****
  20 : TYPE OF ; *****
  21 : TYPE OF ; *****
  22 : TYPE OF ; *****
  23 : TYPE OF ; *****
  24 : TYPE OF ; *****
  25 : TYPE OF ; *****
  26 : TYPE OF ; *****
  27 : TYPE OF ; *****
  28 : TYPE OF ; *****
  29 : TYPE OF ; *****
  30 : TYPE OF ; *****
  *****
  PROCEDURE ERRMSG2 ; *****
  BEGIN
  CASE
  OF
  31 : TYPE OF ; *****
  32 : TYPE OF ; *****
  33 : TYPE OF ; *****
  *****

```







```

A(K) := BLANK ;
UNTIL K=10 ;
K:=0 ; IF K<ALFALENG THEN
REPEAT BEGIN
  K:=K+1 ;
  A(K) := CH ;
END ;
NEXTCH ;
UNTIL CH >= 9 ;
FOR I:=HL(K) TO HL(K+1) -1 DO
  BEGIN
    CPARE(A,HD,I,COMPARE) ;
    IF COMPARE THEN
      BEGIN NO :=WNO(I) ; IVAL:=0 ;
        CL:=HCL(I) ; IVAL:=0 ;
        GOTO 2 ;
      END ;
    END ;
  END ;
  NO:=1 ; CL:=K ;
  REPEAT
    I:=I+1 ;
    AVAL(I) := BLANK ;
  UNTIL I=10 ;
  I:=0 ;
  REPEAT
    I:=I+1 ;
    AVAL(I) := A(I) ;
  UNTIL I=K ;
20 END ELSE
  BEGIN
    NO:=9 ; CL:=1 ; I:=0 ; NUMBER +
    WHILE CH IN DIGITS DO
      BEGIN
        DIGIT(I) := ORU(CH) - ORD(0) ;
        I:=I+1 ; NEXTCH ;
      END ;
    IVAL:=0 ;
    IF CH = 9 THEN
      BEGIN
        IF I > 5 THEN ERROR(2) ELSE
          FOR K:=0 TO I-1 DO
            IVAL := 8 * IVAL + DIGIT(K) ;
          NEXTCH ;
        END BEGIN ELSE
          IF I > 4

```

```

THEN ERROR(2) ELSE
FOR K:=0 TO I-1 DO
  BEGIN IF IVAL<MAX10 THEN
    IVAL:=10+IVAL+DIGIT(K)
  ELSE BEGIN ERROR(2)
        IVAL:=0; END ;
END ;

```

```

END ;
BEGIN IF SPECIAL=NO THEN
  BEGIN CHARACTER CONSTANT THEN
    CH:=A(K); BT:=FALSE ;
  ELSE REPEAT
    BEGIN NEXTCH ; THEN CH:=CH; BT:=CH; END ;
  IF BT THEN
    BEGIN IF ALLENG THEN
      ERROR(54) ;
    END ;
  END ;

```

```

UNTIL BT ;
BEGIN KI:=K+1; A(K) := CH ; END ;
CL:=2; TECH:=A(1); IVAL:=ASCII(TECH) ;

```

```

BEGIN NH:=A(2); TEMP:=ASCII(TECH);
APPEND(IVAL,8,TEMP);

```

```

END ; ELSE

```

```

BEGIN IVAL:=SYMN(CH) ; CL:=SYML(CH) ;
IVAL:=IVAL+NO ;
IF TEST=1 THEN
  BEGIN FOR TWO CHARACTER SYMBOL
    BEGIN IF CH=
      THEN
        BEGIN NO:=22 ; NEXTCH ;
    END ;

```

```

END ; ELSE

```

```

IF CH=
  THEN
    BEGIN

```

```

      IF CH=
        THEN
          BEGIN NO:=21 ; NEXTCH ;
        END ;

```

```

      END ; ELSE

```

```

      IF CH=
        THEN
          BEGIN
            IF CH=
              THEN
                SKIP COMMENT ;
          END ;

```

```
BEGIN NEXTCH ;
3  WHILE CH# E#E DO NEXTCH ;
   NEXTCH ; IF CH#E/E THEN
   GOTO 3 ELSE
   NEXTCH ; GOTO 1 ;
END ;
END ELSE
NEXTCH ;
END ; END ; SPECIAL CHARACTER ↓
***** INSYMBOL *****
*****
```

```

PROCEDURE SRCHREC (P:INTEGER) ;
  * SEARCHES ONE BLOCK , RETURNS CTPTR ↓
  LABEL I ;
  BEGIN
    CTPTR := P ;
    WHILE CTPTR ≠ 0 DO
      BEGIN
        COMP ( CONTEXTTABLE[CTPTR].NAME, AVAL, COMPAR ) ;
        IF COMPAR THEN GOTO 1 ;
        ELSE CTPTR := CONTEXTTABLE[CTPTR].NXTEL ;
      END ;
    *****
  *****
PROCEDURE SEARCH ;
  * SEARCHES CONTEXTTABLE , RETURNS CTPTR AND
  DISX=INDEX TO DISPLAY ↓
  LABEL I ;
  VAR I: INTEGER ;
  BEGIN
    FOR I:=TOP DOWNTO 0 DO
      BEGIN
        CTPTR := DISPLAY[I].FNAME ;
        WHILE CTPTR NE 0 DO
          BEGIN
            COMP (CONTEXTTABLE[CTPTR].NAME, AVAL, COMPAR) ;
            IF COMPAR THEN GOTO 1 ;
            ELSE CTPTR := CONTEXTTABLE[CTPTR].NXTEL ;
          END ;
        END ;
      END ;
    *****
  *****
  END P SEARCH ↓
  *****
  *****

```



```

PROCEDURE INCONST(V,P:INTEGER; NXT:INTEGER);
VAR SIGN, BOOLEAN; PT: INTEGER;
BEGIN
SIGN := FALSE; P := 0;
IF NO=7 THEN
BEGIN SIGN:=CL=2;
IF CL<2 THEN
BEGIN INSYMBOL; IF NO=1 THEN ERROR(3) END;
END;
IF NO=2 THEN
BEGIN CASE CL OF
P:=INPTR;
P:=CHARPTR;
END;
END SIGN THEN V:=-IVAL ELSE V:=IVAL;
INSYMBOL;
ELSE
END NO=1 P:=CTPTR;
SEARCHREG(NXT);
IF CTPTR=0 THEN SEARCH(12) ELSE
IF CTPTR=1 THEN ERROR(CTPTR) DO
WITH CONTEXT(KLASS*CONST) V(CONKIND=FORMAL)
BEGIN IF THEN
ERROR(42) ELSE
BEGIN P:=CONTYPE; V:=VALUES END;
END;
P:=PT; INSYMBOL;
END ELSE ERROR(3);
END INCONST;
*****

```

```

PROCEDURE SKIP(FNO: INTEGER) ;
BEGIN
  WHILE (ERRCL(N0) EQ 2) AND (FNO NE NO) DO
  IF (NO EQ 41) AND (GL EQ 2) THEN RECORD
  BEGIN
    REPEAT INSYMBOL ;
    SKIP(49) ;
    UNTIL (NO IN (16,30)) ;
    IF NO EQ 26 THEN INSYMBOL ;
    END ELSE INSYMBOL ;
    END SKIP ;
  END ;
  *****
PROCEDURE FINDSEMICOLON ;
BEGIN
  IF NO NE 16 THEN
  BEGIN ERROR(39) ; SKIP(16) ;
  END ; INSYMBOL ;
  END ; FINDSEMICOLON ;
  *****

```

```

PROCEDURE SEG1 ; INITIALIZATION →
BEGIN
  DIGITS := (E0E,E1E,E2E,E3E,E4E,E5E,E6E,E7E,E8E,E9E) ;
  OP := IRUE ;
  INITIALIZE CONTEXTTABLE →
  WITH CONTEXTTABLE[0] DO TYPE OF NIL →
  BEGIN
    I := 1 ;
    REPEAT
      NAME[I] := BLANK ;
      I := I+1 ;
    UNTIL I=10 ;
    NXTEL := 0 ;
    FORM := NUMERIC ;
    SIZE := 0 ;
  END ;

  CIPTR := 1 ;
  WITH CONTEXTTABLE[CIPTR] DO TYPE OF MEM →
  BEGIN
    I := REPEAT
      NAME[I] := BLANK ;
      I := I+1 ;
    UNTIL I=10 ;
    KLASS := TYPES ;
    NXTEL := 1368 ;
    FORM := ARRAYS ;
    LO := 0 ;
    HI := 1778 ;
    OPTIYP := PUREP ;
  END ;

  CHARPTR := 10 ;
  WITH CONTEXTTABLE[CHARPTR] DO CHAR →
  BEGIN
    I := 0 ;
    REPEAT
      I := I+1 ;
      NAME[I] := INITNAM(26,I) ;
      UNTIL I=10 ;
      NXTEL := 5 ;
      KLASS := TYPES ;
      SIZE := 1 ;
      FORM := SYMBOLIC ;
      BITSIZE := 7 ;
    END ;
  END ;

  WITH CONTEXTTABLE[2] DO →
  BEGIN
    I := 1 ;
    REPEAT
      NAME[I] := BLANK ;
      I := I+1 ;
    UNTIL I=10 ;
    NXTEL := CHARPTR ;
    CONKIND := ACTUAL ;
    VALUES := 63 ;
    SUCC := 0 ;
  END ;

  WITH CONTEXTTABLE[CHARPTR].FCONST := 2 ;
  WITH CONTEXTTABLE[BOOLPTR] DO BOOLEAN →
  BEGIN

```

```

I:=0 ;
REPEAT I:=I+1; NAME[I] := INITNAM[27,I] ;
UNTIL I=EQ 10 ;
NXTTEL:= CHARPTR ; KLASS:= TYPES ; SIZE :=1 ;
FORM := SYMBOLIC ; BITSIZE:= 2 ;

END ;
WITH CONTEXTTABLE[3] DO REOL↓
BEGIN
I:=1 ;
REPEAT NAME[I] := INITNAM[20,I] ;
I:=I+1 ;
UNTIL I=10 ;
NXTTEL:= BOOLPTR ;
KLASS:= KONST ; CONTYPE := CHARPTR ; CONKIND := ACTUAL ;
VALUES:= 0 ; SUCC:= 0 ;

END ;
CIPTR:= 0 ;
NEXT I:= 27 ;
NEXT PT := 4 ;
FOR I:=0 TO 1 DO FALSE↑RJE ↓
BEGIN
WITH CONTEXTTABLE[PT+II] DO
REPEAT I:=0 ;
I:=I+1 ; NAME[I] := INITNAM[II+28,I] ;
UNTIL I=EQ 10 ;
NXTTEL:= NEXT ;
KLASS:= KONST ; CONTYPE:= BOOLPTR ;
CONKIND:=ACTUAL ; VALUES:=IT ; SUCC:= 0 ;

END ;
NEXT I:= 4 ;

END ;
CONTEXTTABLE[BOOLPTR]. FCONST := 5 ;
INTPTR := 12 ;
WITH CONTEXTTABLE[INTPTR] DO INTEGER ↓
BEGIN
REPEAT I:=0 ;
I:=I+1 ; NAME[I] := INITNAM[30,I] ;
NXTTEL:= 800 ;
UNTIL I=EQ 10 ;
VALUES:= 1 ; FORM:= NUMERIC ;
BITS:= 100 ; MAX:= 777778 ;
MOROLENTH := MOROLENTH ;

END ;
BYTPTR := 13 ;

```

```

WITH CONTEXTTABLE[BYTPTR] DO BYTE →
BEGIN := 0;
REPEAT
  I:=I+1;
  NAME[I] := INITNAM[37,I];
UNTIL I=10;
NEXTEL := INIPTR; SIZE := 1;
FORM := SYMBOLIC; BITSIZE := 4;
END;
WRDPTR := 1;
WITH CONTEXTTABLE [WRDPTR] DO WORD →
BEGIN := 0;
REPEAT
  I:=I+1;
  NAME[I] := INITNAM[38,I];
UNTIL I=10;
NEXTEL := BYTPTR; CLASS:= TYPES;
SIZE := 1; FORM := SYMBOLIC;
BITSIZE := WORDLENGTH;
END;
UNDCLPTR:=15;
WITH CONTEXTTABLE[UNDCLPTR] DO
BEGIN
  I:=1;
  REPEAT
    NAME[I] := BLANK;
    I:=I+1;
  UNTIL I=10;
  NXTEL:= 0; CLASS:= VARS; VTYPE:= 0;
  VKIND:= ACTUAL; VADDR:= 0; VLEVEL:= 0;
END;
PT := 6;
WITH CONTEXTTABLE[PT] DO MEM →
BEGIN
  I:=1;
  REPEAT
    NAME[I] := INITNAM[34,I];
    I:=I+1;
  UNTIL I=10;
  NXTEL:= 0; CLASS:= VARS;
  VTYPE:= CIPTR; VKIND:= ACTUAL;
  VADDR:= 0; VLEVEL:= 0;
END;
CIPTR := 7;
WITH CONTEXTTABLE[CIPTR] DO TYPE OF INPUT AND OUTPUT →
BEGIN
  I:=1;
  REPEAT
    NAME[I] := BLANK;

```

```

I:=I+1 ;
UNTIL I=10 ; KLAS := TYPES ;
NXTEL := 0 ; FORM := FILES ;
SIZE := 113 ;
END ;
PT := 8 ;
FOR IT := 22 TO 23 DO
  BEGIN
    WITH CONTEXTTABLE[PT] DO
      BEGIN
        I:=1 ;
        REPEAT NAME[I] := INITNAM[IT,I] ;
              I:=I+1 ;
        UNTIL I=10 ;
        NXTEL := CTP[R ; VKIND := ACTUAL ;
        VLEVEL := 0 ;
        END ;
        PT := PT + 1 ;
      END
    NEXT := 1 ;
    PT := 13 ;
    FOR IT := 0 TO 11 DO * TEXT TO WRITE *
      BEGIN
        WITH CONTEXTTABLE[PT] DO
          BEGIN
            I:=0 ;
            REPEAT
              I:=I+1 ;
              NAME[I] := INITNAM[IT-I,I] ;
              UNTIL I EQ 10 ;
              NXTEL := NEXT ; KLAS := PROC ; PROCTYPE:=PT ;
              FORMALS:=0 ; PROCADDR:=0 ; PROCLEVEL:=0 ;
              SEGSIZE:=I ; PROCKIND := ACTUAL ;
            END ;
            * NEXT :=PT ; PT:=PT+1 ;
          END
        FOR I := 1 TO 5 DO
          WITH EX[IT] DO
            BEGIN
              I:=0 ;
              REPEAT
                I:=I+1 ;
                XNAM[I] := INITNAM[IT,I] ;
                UNTIL I EQ 10 ;
                XSYM := I ;
              END ;
              WITH DISPLAY[0] DO

```

```

BEGIN FNAME := 14 ; OCCUR := BLOCK END ;
FOR IT := 1 TO UNDMAX-1 DO
  UNDLAB(UNDHMAX) ; SUCC := IT+1 ;
  EKRIX := 0 ; POSI := 0 ;
  JMPIX := 0 ; NEXTABIX := 0 ; CHNIX := 1 ;
  EXSYM := 5 ;
  PREDEFPTR := 27 ;
  DISPLAY(1) ; FNAME := 0 ; DISPLAY(1) ; OCCUR := BLOCK ;
  TOP := 1 ; LEVEL := 0 ;
  CHCNT := 0 ;
  LC := 0 ;
  VLC := 0 ;
  NEXT := 14 ;
  INDEX := 27 ; P := FALSE ;
  EOLFLAG := FALSE ;
  PUNCHBIN := 0 ; LISTBIN := FALSE ; RLISTBIN := FALSE ;
  TRANSFR(1) := 0 ; TRANSFR(2) := 1 ; TRANSFR(3) := 0 ;
  INSYMBOL := AVAL, COMPARE ;
  COMP(PPROG) ; AVAL, COMPARE ;
  IF COMP(PPROG) THEN INSYMBOL ELSE CERR := TRUE ;
  REPEAT
    I := I + 1 ; PRNAM(I) := AVAL(I)
  UNTIL I EQ 5 ;
  INSYMBOL := INSYMBOL ;
  REPEAT
    I := I + 1 ; INSYMBOL := INSYMBOL ;
  UNTIL I EQ 15 ;
  REPEAT
    I := I + 1 ; INSYMBOL := INSYMBOL ;
  UNTIL I EQ 15 ;
  THEN LISTBIN := TRUE ELSE
  THEN PUNCHBIN := TRUE ELSE
  THEN RLISTBIN := TRUE ELSE
  CERR := TRUE ;
  INSYMBOL := INSYMBOL ;
  UNTIL I EQ 15 ;
  IF CERR THEN
    BEG LINES(1) ; WRITE(=CONTROL CARD ERRORS) ; LINES(1)
  END ;
  KPA := 0 ;
  RPMAX := 0 ;
  SURRCPTR := 0 ;
  UNDECCTR := 0 ;
  HCA := 0 ;

```





```

PROCEDURE SEG2 ;
LABEL AND CONSTANT DECLARATION +
BEGIN
  IF NO EQ 42 THEN + LABEL +
  BEGIN
    REPEAT
      INS YMBOL ;
      IF (NO EQ 2) AND (CL EQ 1) THEN
        BEGIN
          II := FSIX TO CEXTABIX DO
          FOR EXTAB(II).EXVAL = IVAL THEN
            BEGIN ERROR(48) ; GOTO 2 END ;
          IF CEXTABIX = MAXEXLABS THEN ERROR(49) ELSE
            BEGIN
              CEXTABIX := CEXTABIX + 1 ;
              IF JMPIX GT JHPMAX THEN ERROR(53) ELSE
                BEGIN
                  WITH EXTAB(CEXTABIX) DO
                    BEGIN EXVAL := IVAL ; JMPBIX := JMPIX END ;
                  JMPIX := JMPIX + 1 ;
                END ;
            END ;
          INS YMBOL ;
          END IF (NO = 2) AND (CL = 1) + ELSE
            BEGIN ERROR(41) ; GOTO 3 END
          UNTIL NO NE 15 + + ;
          FIND SEMICOLON ;
        END ;
      IF NO = 3 THEN + CONST +
      BEGIN
        INS YMBOL ;
        WHILE NO EQ 1 DO
          BEGIN REPEAT
            SRC := NEXT ;
            IF CTR NE 0 THEN ERROR(8) ;
            INDEX := INDEX + 1 ;
            WITH CONTEXTTABLE(PI) DO
              BEGIN NO := 0 ;
                REPEAT
                  II := II + 1 ;
                  NAME(II) := AVAL(II) ;
                UNTIL IS10 ;
              END ;
            END ;
          END ;
        END ;
      END ;
    END ;
  END ;

```





```

SRCHREC(NEXT) ; THEN
IF CTPTR NE 0
  BEGIN
  IF NOT(GO)CONTEXTTABLEE(SYPTR),SYNCELL) THEN
  ERROR(8) ELSE
  BEGIN
  P := CTPTR ;
  Q := CONTEXTTABLE[P].SYNPTR ;
  SYNDECL ;
  GOTO 6
  END ;
INDEX := INDEX+1 ;
END ;
WITH CONTEXTTABLE(P) DO
BEGIN
SYNCELL := TRUE ;
I := 0 ;
REPEAT
  I := I+1 ;
  NAME(I) := AVAL(I) ;
  UNTIL I=10 ;
  NXL := NEXT ;
END ;
NEXT := P ;
Q := Q ;
SYNDECL ;
IF NO NE 16 THEN ERROR(39) ;
INSYMBOL ;
END ;
END *SYV* ;
END *SEG* ;
*****

```

6-1



```

PROCEDURE SKIPT(FNO;INTEGER) ;
BEGIN
WHILE (IERRCL(NO)=0) ^ (FNO#NO) DO
IYSYMBOL ;
END *SKIPT(I;INTEGER) ;
PROCEDURE TYPERR(I;INTEGER) ;
BEGIN
I:=0 ; P:=0 ;
ERROR(I) ; SKIPT(49) ;
WRITE(I;IERR) ; SKIPT(49) ;
END *TYPERR ;
*****
PROCEDURE SUBRANGE(VAR VAL1,VAL2 ;INTEGER;N1;INTEGER;
P;INTEGER) ;
*THE FIRST SYMBOL HAS BEEN READ
*THE PROCEDURE RETURNS TWO BOUND VALUES IN VAL1,VAL2
*AND RETURNS N1=POINTER TO TYPE OF CONSTANTS.
*ERROR: TYPES DO NOT AGREE, I TYPE IS NOT INTEGER ,CHAR,OR
*P INDICATES BEGINNING OF SEARCH FOR FIRST SYMBOL
IF I IS AN ID *
VAR N2 ; INTEGER ; P POINTER *
BEGIN INCONST(VAL1,N1,P) ;
IF N1#0 THEN ERR:=TRUE ELSE
BEGIN IF NO#21 THEN ERROR(10) ELSE
INSYMBOL(VAL2,N2,NEXT) ;
INCONST(VAL2,N2,NEXT) ;
IF N1#N2 THEN ERR:=TRUE ELSE
IF VAL1>VAL2 THEN ERROR(10)
END *SUBRANGE *
*****
PROCEDURE SUBTYPE( VAR I,J;INTEGER ; P;INTEGER) ;
* EITHER A TYPE .ID FOR A SUBRANGE OR AN EXPLICIT SUBRANGE
ARE PROCESSED
RETURNS I=LOWBOUND, J=HIGHBOUND,
P=POINTER TO TYPE OF CONSTANTS. *
VAR JTEM ; INTEGER ;
BEGIN IF NO#1 THEN
BEGIN SRCHREC(NEXT) ;
IF CTPR#0 THEN SEARCH ;

```



```

JNITL JJ=10 ;
VXTEL:=0 ; KLASSTYPES ;
SIZE J=1 ; FORM I= NUMERIC ;
MIN I=1 ; MAX J=J ;
IF ABS(I) > ABS(J) THEN
  BITS I= LOG2(ABS(J)) ELSE
  BITS J= LOG2(ABS(I))
ENJ
I=1 ; P1=P
END ELSE
END SCALDECL
*****

```



```

PROCEDURE FIELDIS (VAR MAXSIZE, VARPTR, NXTF, INTEGER) ;
VAR MXL, MINSIZE, CASEBITS, I, INTEGER ;
VAR P, PP1, PP2, NXC, NXC, CPT, CPT, INTEGER ;
TAGL, TAGR, BOOLEAN ; *****
PROCEDURE RECCR (I, INTEGER) ;
BEGIN EXROK(I) ;
END ; RECCR ; *****
PROCEDURE ADJUST ;
MOVE LAST FIELD TO RIGHT ; IF IT IS THE ONLY FIELD ;
CHANGE TO NONPACKED ; IF LAST FIELD IS TAUFIELD THEN ;
DO NOT MOVE ;
INCREASE DISPL, RESET BDISPL ;
BEGIN ;
TAGFLAG THEN ;
WITH CONTXTTABLE(LASTFLU) .00 ;
BEGIN ;
BITDISPL=0 ; THEN IN WORD ;
ONLY ONE FIELD ;
BITWIDTH := 0 ; ELSE ;
BITDISPL := WORDLENGTH - BITWIDTH ;
END ;
DISPL := DISPL + 1 ; BDISPL := 0 ;
ADJUST ;
*****
BEGIN ;
NXTF := NXTF ;
REPEAT ;
IF I=0 ;
IF I=NO ;
BEGIN ERROR(11) ;
IF TERRCL(NO)=1 THEN GOTO 11 ;
GOTO 12 ;
END ;
RECCR(NXTF) ;
IF SKCPT(I)=0 THEN ERROR(5) ELSE ;
BEGIN ;
INDEX := I + INDEX + 1 ;
INJEX := INJEX ;
WITH CONTEXTTABLE(P) DO ;
BEGIN ;
REPEAT ;
NAMEL(JJ) := AVAL(JJ) ;
JJ:=JJ+1 ;

```

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```

UNTIL JJ=10 ;
NXTEL :=NXT ;
KLASSE FIELD ;
FOTYPE := 0 ;
END ;
NXT := P ;
END ;
INSYMBOL ; THEN ;
IF NO INSYMBOL ; GOTO 1 END ;
BEGIN IN 21 THEN NOT ERROR(10) ;
IF NO 21 INSYMBOL ;
SELECT(L,P) ;
IF 21 THEN TABLE(P).FORM > RECORDS THEN
IF ERROR(24) ELSE
BEGIN
IF PACKFLAG THEN
IF I GT 1 REVERSE POINTERS THEN
BEGIN
PP1:=NXT ;
FOR I:=1 DOWNTO 1 DO
BEGIN
PP1 := CONTEXTTABLE(PP).NXTEL ;
PP2:=PP ;
PP:=PP1 ;
END ;
CONTEXTTABLE(NXT).NXTEL := PP ;
NXT := PP2 ;
END REVERSE ;
PP1:=CONXT ;
IF CONXT THEN
CONTEXTTABLE(P).FORM LE SYMBOLIC THEN
BEGIN
CASE CONTEXTTABLE(P).FORM OF
LL:=CONTEXTTABLE(P).BITS ;
LL:= CONTEXTTABLE(P).BITSIZE ;
END ;
REPEAT
IF BOISPL+LL GT HORJLENGTH THEN AJJUST ;
IF LL EQ HORJLENGTH THEN
BEGIN
CONTEXTTABLE(PP1).BITWIDTH :=0 ;
CONTEXTTABLE(PP1).FLOADDR:=DISPL ;

```

NUMERIC ;  
SYMBOLIC ;

```

DISPL:=DISPL+1 ;
END ELSE
BEGIN
CONTEXTTABLE[PP1].BITWIDTH:=LL;
CONTEXTTABLE[PP1].BITDISPL:=BDISPL;
CONTEXTTABLE[PP1].FLOADOR:=DISPL;
BDISPL:=BDISPL+LL;
END ;
CONTEXTTABLE[PP1].FLOTYPE := P ;
LASTFLO := PP1 ; TAGFLAG:=FALSE ;
LPP1 := CONTEXTTABLE[PP1].NXTEL ;
UNTIL PP1=PP ;
END *FORM LE SYMBOLIC+ ELSE
BEGIN
BDISPL NE 0 THEN ADJUST ;
TAGFLAG := FALSE ;
REPEAT
WITH CONTEXTTABLE[PP1] DO
BEGIN
BITWIDTH:=0 ;
FLOTYPE:=P ;
FLOADOR:=DISPL ;
END ;
DISPL:=DISPL+LL ;
LASTFLO := PP1 ;
LPP1 := CONTEXTTABLE[PP1].NXTEL ;
UNTIL PP1=PP ;
END ;
END *PACKFLAG+ ELSE
LL:=I+L+DISPL ;
DISPL := LL ;
PP := NXI ;
FOR I:=1 DOWNTO 1 DO
BEGIN
CONTEXTTABLE[PP].FLOTYPE := P ;
LL:=LL ;
CONTEXTTABLE[PP].FLOADOR := LL ;
CONTEXTTABLE[PP].BITWIDTH := 0 ;
PP:= CONTEXTTABLE[PP].NXTEL ;
END ;
END *FORM < RECORDS +
IF NOF16 THEN INSYMBOL
UNTIL (TERRC(LNOI=2) ^ (NO#30)) ;
IF BDISPL NE 0 THEN ADJUST ;
MAXSIZE := DISPL ; VARPIR := 0 ;
GOTO 9 ;

```

22 :

```

21 * CASE *
  INSYMBOL; THEN ERROR(6) ELSE
  BEGIN SPCREG(NXT);
  IF CPTR NE 0 THEN ERROR(5) ELSE
  BEGIN
    INJEX := INDEX + 1;
    := INDEX;
    WITH CONTEXTTABLE(P) DO
    BEGIN
      J:=1;
      REPEAT
        NAME{J}:= AVAL{J};
        J:=J+1;
      UNTIL J=10;
      NXLTEL:= NXT;
      FLOTTYPE := 0;
      END;
      ENJ;
    END;
  INSYMBOL;
  END NO NE 21 THEN ERROR(10) ELSE
  INSYMBOL; THEN ERROR(11) ELSE
  BEGIN SPCREG(NXT);
  IF CPTR NE 0 THEN SEARCH;
  IF CPTR=0 THEN ERROR(12) ELSE * TYPES)
  IF (CONTEXTTABLE{CPTR}.FORM = SYMBOLIC)
  THEN ERROR(7);
  INSYMBOL; THEN ERROR(14);
  IF NO := CPTR;
  IF CPTR NE 0 THEN
  IF SPCREG=0 THEN
  BEGIN
    CASE CONTEXTTABLE{CPTR}.FORM OF
      LL := CONTEXTTABLE{CPTR}.BITS;
      LL := CONTEXTTABLE{CPTR}.BITSIZE;
    END;
    IF BOISPL + LL > WORDLENGTH THEN ADJUST;
    CONTEXTTABLE{P}.BITWIDTH := BOISPC;
    CONTEXTTABLE{P}.FLDADR := DISPL;
    BOISPL := BOISPL + LL;
    LASTFLO := P; CASEBITS := BOISPL;
  NUMERIC;
  SYMBOLIC;

```





```

* TYPEDECL +
BEGIN
  PACKFLAG := FALSE ;
  IF NO. EQ 53 THEN *PACKED +
  BEGIN PACKFLAG := TRUE ; INSYMBOL END ;
  IF NO=1 THEN SRCHREC(NEXT) ;
  BEGIN CIPTR=0 THEN SEARCH ;
  IF CIPTR = 0 THEN
    BEGIN ERROR(12) ;
    P:=0 ; SKIPT(16) ;
  END ELSE
  BEGIN IF CONTEXTTABLE(CIPTR).KLASS=YPES THEN
    * TYPE-ID +
    BEGIN IL:=CONTEXTTABLE(CIPTR).SIZE ;
    P:=CIPTR ; INSYMBOL
  END ELSE
    IF CONTEXTTABLE(CIPTR).KLASS = KONST THEN
      SCALDECL(CIPTR) ELSE
        TYPERR(11) ;
  END ;
  END * IU +
  ELSE SYMBOLIS +
  IF NO=9 THEN *SYMBOLIS +
  BEGIN CV:=0 ; LERR:=ERR ; ERR:=FALSE ;
  INDEX := INDEX + 1 ;
  P := INDEX ;
  WITH CONTEXTTABLE(P) DO
    BEGIN
      NAME(IJJ) := BLANK ;
      IJJ := JJ+1 ;
      UNTIL JJ=10 ;
      UNTIL := 0 ; KLASS:= TYPES ;
      FORM := SYMBOLIC ;
      END ;
      REPEAT INSYMBOL ;
      REPEAT UNTIL THEN
        BEGIN ERROR(11) ;
        SKIPT(15) ; GOTO 2. ;
      END ;
      SRCHREC(NEXT) ;
      IF CIPTR # 0 THEN ERROR(8) ELSE
        BEGIN
          INDEX := INDEX + 1 ;
          P := INDEX ;
        WITH CONTEXTTABLE ( P) DO

```

```

BEGIN JJ:=1 ;
REPEAT
  NAME(JJ) := AVAL(JJ) ;
  JJ := JJ+1 ;
UNTIL JJ=10 ;
NEXTEL := NEXT ;
CONTYPE := RTYP ;
VALUES := CV ;
END ;
CV := CV+1 ;
NEXT := P ;
NEXTC := P ;
END ;
INSYMBOL ;
UNTIL NO=15 ;
WITH CONTEXTTABLE(RIYP) DO
  BEGIN FCONST := NEXT ; SIZE := 1 ;
  BEGIN SIZE := 2 ;
  END ;
  THEN P1 := 0 ELSE
  IF ERR ERR := LERR ; P1 := RTYP ; END ;
  YL := 1 ;
  IF NO=10 THEN TYPERR(9) ELSE INSYMBOL ;
  END ;
  IF (NO=2) THEN TYPERR(10) ELSE
  BEGIN LERR := LERR ; THEN SUBRANGE +
  IF LERR THEN ERR := FALSE ;
  ELSE SUBRANGE THEN LERR
  END ;
  IF NO=4 THEN SUBRANGE + STRUCTURED TYPES +
  CASE
  * ARRAY + 1 ;
  BEGIN INSYMBOL ;
  IF NO=1 THEN
  BEGIN TYPERR(57) ; THEN TYPEDECL(I,CTPTR) ;
  IF TYPERR(NO)=1
  GOTO 19 ;
  END ;
  NEXTEL := 0 ;
  REPEAT
  INDEX := INDEX + 1 ;
  WITH CONTEXTTABLE(P) DO
  BEGIN
  REPEAT NAME(JJ) := BLANK ;
  JJ := JJ+1 ;
  UNTIL JJ=10 ;
  NEXTEL := 0 ;
  KLASSE := TYPES ;

```



```

FOR HI= ARRAYS ; AELTYPE != NXTA ;
  AELTYPE TEMP. LINKS SJBARRAYS ;
  INSYMBOL ;
  ERR:=ERR; ERR:=FALSE ;
  SUBTYPE(I,J,P) ;
  IF ERR THEN
    BEGIN SKIP(15) ; I:=0; J:=0; P:=0
  END ELSE
    ERR:=LEERR ;
  HIGH CONTEXTTABLE(NXTA) DO
  BEGIN
    IF I LT 127 THEN LO:=I ELSE
      BEGIN
        LO:=I DIV 10 +1 ;
        SIZE:=TRUE ;
      END ;
    THEN HI:=J ELSE
      BEGIN
        HI:=J DIV 10 +1 ;
        SIZE := TRUE ;
      END ;
    INXTYPE:= P ;
  END ;
UNTIL NO#15 ;
IF NO#12 THEN
  BEGIN ERROR(31) ;
  SKIP(11) ;
  IF TERRCL(NO)=0 THEN GOTO 11 ;
  IF NO#31 THEN
    BEGIN INSYMBOL ; GOTO 11 END ;
  IF NO#12 THEN
    BEGIN PI:=0 ; TL:=0 ; GOTO 19 END ;
  END ;
INSYMBOL ;
IF NO#11 THEN ERROR(14) ELSE INSYMBOL ;
TYPEDECL(T,CIPTR) ;
IF CIPTR#0 THEN
  IF CONTEXTTABLE(CIPTR).FORM > REGISTERS
    THEN BEGIN ERROR(24) ; CIPTR:=0 END
  ELSE
    BEGIN
      CIPTR := CIPTR ;
      REPEAT
        WITH CONTEXTTABLE(NXTA) DO

```

```

BEGIN
  MULOPT(IL,E1,E2,OPT) ;
  OPTTYP := OPT ; EXP1 := E1 ;
  EXP2 := E2 ;
  IL := IL * (HI-LO+1) ;
  SIZE := TL ;
  P := AELTYPE ; AELTYPE := CIPTR ;
END ;
CIPTR := NXTA ; NXTA := P ;
UNTIL NXTA=0 ;
  * NOW TL IS THE SIZE OF THE ARRAY,CIPTR
  POINTS TO IT ↓
END ;
PI:=CIPTR ;
IF PACKFLAG THEN
  BEGIN
    CIPTR := CONTEXTTABLE(CIPTR).AELTYPE ;
    CASE CIPTR := CONTEXTTABLE(CIPTR).FORM OF
      TEMP := CONTEXTTABLE(CIPTR).BITS ;
      TEMP := CONTEXTTABLE(CIPTR).BITSIZE ;
    END ;
    TEMP := WORDLENGTH DIV TEMP ;
    WITH CONTEXTTABLE(PI) DO
      BEGIN
        IF (SIZE MOD TEMP) EQ 0 THEN SIZE:=SIZE DIV TEMP ;
        ELSE SIZE := SIZE DIV TEMP + 1 ;
      END ;
    END ;
  END ;
END ;
END ARRAY ↑ ;
19 RECORD → BEGIN INDEX := INDEX+1 ;
2 RECORD → P := INDEX ;
  WITH CONTEXTTABLE(PI) DO
    BEGIN REPEAT
      NAME(JJ) := BLANK ;
      JJ := JJ+1 ;
    UNTIL JJ=10 ;
    KLAS := TYPES ;
    NXTEL := 0 ;
    FORM := RECORDS ;
    END SYMBOL ;
    NXTF := 0 ;
    DISPL := 0 ;
    BDISPL := 0 ;
    LERR := ERR ;
    ERR := FALSE ;
    FIELD := ISIL,P,NXIF ;
    IF NO#26 THEN ERROR(15) ;

```



PROCEDURE SEG3 ; VARIABLE DECLARATION +

```

VAR AT ; ADDRESS ;
      J,K ; SHRIN ;
      JJ ; INTEGER ;
      AS ; ARRAY [1..10] OF INTEGER ;
BEGIN SYMBOL ;
  WHILE NO = 0 DO
    BEGIN I = 0 ;
      REPEAT
        SR = REG(NEXT) ;
        IF CIPTR NE 0 THEN
          BEGIN CONTEXTTABLE(CIPTR).SYNCELL THEN
            BEGIN CONTEXTTABLE(CIPTR).SYNCELL := FALSE ;
              J := J+1 ; CIPTR ;
              AS(J) := CIPTR ;
            END ELSE ERROR(8) ;
          END ELSE
            BEGIN INDEX := INDEX+1 ;
              PI := INDEX ;
              WITH CONTEXTTABLE(PI) DO
                BEGIN JJ = 0 ;
                  REPEAT
                    JJ = JJ+1 ; AVAL(JJ) ;
                    NAME(JJ) := NEXT ;
                    UNTIL JJ = 10 ;
                    NXL = NEXT ;
                    KLASS := VARS ; VKIND := ACTUAL ;
                    VTYPE := 0 ; VLEVEL := 0 ;
                  END
                NEXT := P ;
                I := I+1 ;
              END
            END SYMBOL ;
          IF NO EQ 15 THEN
            BEGIN
              IN SYMBOL ;
              IF NO NE 1 THEN
                BEGIN ERROR(11) ; GOTO 10 END ;
            END
          END
        END
      END
    END
  END

```

```

END ELSE
IF NJ NE 21 THEN ERROR(10)
UNTIL NO NE 1
IF NO EQ 21 THEN INSYMBOL
ELSE IF NO(1ERR) THEN ERROR(10)
N := P
ERR := FALSE
TYPEDECL(TL, CIPTR)
IF PERR THEN GOTO 10
LC := LC+(I+J)*IL
LL:=LC
FOR I:= 1 DOWNTO 1 DO
BEGIN
LL:=LL-TL
WITH CONTEXT(TABLE(N)) DO
BEGIN
VTYPE := CIPTR
VLEVEL := LEVEL
VADDR := LL
N := NXTEL
END
FOR I := 1 WITH N
FOR J := J DOWNTO 1 DO
BEGIN
N:=AS(J)
LL:=LL-TL
WITH CONTEXT(TABLE(N)) DO
BEGIN
VTYPE := VARS
VLEVEL := ACTUAL
VTYPE := CIPTR
VLEVEL := 0
VLEVEL := LEVEL
VADDR := LL
END
N := SYNTPR
SYNTPR := 0
N := WITH N
GOTO 1
JJ:=JJ+1
END
END
FINDSEMICOLON
END WHILE NO=1
END ! SEG3

```

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.....

```

PROCEDURE SEGS ; * VALUE DECLARATION *
VAR I1: INTEGER ;
BEGIN
  IF NO EQ 48 THEN * VALUE *
  BEGIN
    VALPI1:= TRUE ;
    I1:=0 ;
    IF LEVEL NE 0 THEN ERROR(18) ;
    INSYMBOL ;
    WHILE NO EQ 1 DO
    BEGIN
      SACHREC(NEXT) ;
      IF CIPTR EQ 0 THEN SEARCH ;
      IF CTPTR EQ 0 THEN
        BEGIN
          ERROR(12) ;
          FINOSEMICOLON ;
          GOTO 20
        END ;
      CONTEXTTABLE(CPTR).KLASS NE VARS THEN
        BEGIN
          ERROR(22) ; FINOSEMICOLON ;
          GOTO 20
        END ;
      WITH CONTEXTTABLE(CIPTR) DO
        BEGIN
          * VADOR ;
          IF VALPT THEN
            BEGIN
              IF VLC EQ 0 THEN
                BEGIN
                  VLC := IT ; LL:= IT ; AT := IT
                END ;
              IF LL THEN ERROR(20) ELSE
                WHILE II GT AT DO
                  BEGIN
                    I1:=I1+1 ; VAR(I1)=0 ; END ;
                    INSYMBOL ;
                    IF (NO NE 6) OR (CL NE 6) THEN ERROR(4)
                  ELSE
                    INSYMBOL ;
                    IF NO EQ 9 THEN *LIST*
                  BEGIN
                    REPEAT
                      INSYMBOL ; INCONSI(I,PT,NEXT) ; I1:= 1 ;
                    UNTIL (NO EQ 6) AND (CL EQ 1) THEN *
                  BEGIN

```



```
*****  
PROCEDURE GENRE(A,B,INTEGER; C; RTYP; D,BOOLEAN);  
  * PUTS THE CODE GENERATED IN GLOBAL VARIABLE--HCODE*  
  BEGIN  
    IC:=IC+1; HCAI:=HCAI+1;  
    WITH HPAS(HCAI) DO  
      BEGIN  
        LOC:=IC; INST:=A; ADDR:=B; TYP:=C; MREF:=D  
      END  
    END * GENRE*  
*****
```



PROCEDURE RLEXP ;  
\* GENERATES CODE FOR RELATIONAL OPERATORS \*

BEGIN  
CASE LADOPCL OF

1: \*LT\* BEGIN  
GENRE(2021B,0,0,FALSE) ;

GENRE(2003B,0,0,FALSE) ;  
GATTR.JMP I=0

END ;

3: \*LE\* BEGIN

GENRE(2022B,0,0,FALSE) ;

GATTR.JMPI=1 END ;

4: \*GT\* BEGIN

GENRE(2023B,0,0,FALSE) ;

GATTR.JMPI=0 END ;

5: \*GE\* BEGIN

GENRE(2002B,0,0,FALSE) ;

GATTR.JMPI=1 END ;

6: \*EQ\* BEGIN

GENRE(2401B,0,0,FALSE) ;

GENRE(2404B,0,0,FALSE) ;

END ;

RLEXP ;

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PROCEDURE LOTMP ;
  VAR RLX : 300LEAN ;
      DLX : 000LEAN ;
  BEGIN
    IF RB EQ 0 THEN GO TO 1 ;
    STY := FALSE ;
    RLX := FALSE ;
    DLX := FALSE ;
    WITH STRIP DO
      BEGIN OP := STOP ; LADOPCL := SICL ; END ;
    BEGIN
      CASE LADOPCL OF
        042000,0,1,TRUE) ; *ADA+
        BEGIN
          GENRE(042000,0,0,0,FALSE) ; *CMA,INA+
          GENRE(042000,0,1,TRUE) ; *ADA+
        END ;
        032000,0,1,TRUE) ; *IOR+
        END * CASE CL OF
      END * NO EQ 7 * ELSE
      IF OR EQ 6 THEN
        BEGIN
          CASE LADOPCL OF
            102000,0,0,FALSE) ; *MPY+
            GENRE(0,0,1,FALSE) ; *LOCATION FOR MPY+
          END ;
            012000,0,1,TRUE) *AND+ END ;
            BEGIN
              GENRE(72000,0,1,TRUE) ; *STA+
              GENRE(62000,0,1,TRUE) ; *LDA+
              DLX := TRUE ;
            END ;
          END * CASE CL OF
        END * NO EQ 6 * ELSE
        BEGIN
          GENRE(003000,0,0,FALSE) ; *CMA,INA+
          GENRE(042000,0,1,TRUE) ; *ADA+
          RLX := TRUE ;
          END * ELSE ERROR(54) ;
        END * KP+
      IF KP GT KPMAX THEN BEGIN ERROR(48) END ;

```

```

WITH STORE[KP] DO
BEGIN STIR:=RP; STPL:= HCA END ;
IF RLX THEN
BEGIN
END ;
IF DLX THEN
BEGIN
DLX:=FALSE;
GENRE(164008,0,0,FALSE); *CLB+
GENRE(20208,0,0,FALSE); *SSA+
GENRE(70048,0,0,FALSE); *CMB INB+
GENRE(100408,0,1,FALSE); *DIV+
GENRE(8FLC+4,0,1,FALSE); *POP FOR DIV+
END ;
RP := RP-1 ;
END * LDIMP+ ;
*****
PROCEDURE STMP ;
BEGIN
CY := TRUE ;
RP:=RP+1 ;
IF RP GT RMAX THEN RMAX := RP ;
KP := KP+1 ;
IF KP GT KPMAX THEN BEGIN ERROR(24); GOTO 1 END ;
WITH STORE[KP] DO
BEGIN STIR:= RP ; STPL := HCA END ;
WITH STIR DO
BEGIN STOP := OP; STCL:= LAOOPCL END ;
END * STMP+ ;
*****

```

```

*****
* CALLED AT PROCEDURE END , UPDATING ITS CODE (INSERTING
* ADDRESSES OF THE CONSTANTS ) AND WRITING OUT CODE TOGETHER
* WITH THE CONSTANTS USED IN THIS PROCEDURE ↓
*****
PROCEDURE WRITEM ;
VAR FLC: INTEGER ;
BEGIN
  FLC := IC ; RPMAX DO IC:=IC+1 ;
  FOR I:=1 TO KP DO
    WITH STORE(I) DO HPAS(STPL).ADDR := FLC+STRP ;
    IC:=IC+1 ; STORAGE FOR BUFFER↑
  LC:=IC ;
  END WRITEM ↑ ;
*****
PROCEDURE WRITOUT ;
VAR IF2 : SHRINT ;
BEGIN
  FOR I:=1 TO LCX DO
    WITH CS(I) DO
      BEGIN
        GENRE(VALU,0,0,FALSE) ; *DEC↑
        IF I:=INX ;
        REPEAT
          WITH UNLAB(I) DO
            BEGIN
              WITH HPAS(PLACE) DO ADDR:= IC ;
              I:=I+1 ; I:=SUCC
            END
          UNTIL I:=EQ 0 ;
          UNLAB(I) ; SUCC := CHNIX ; CHNIX := INX
        END WRITOUT↑ ;
      END WRITOUT↑ ;
*****

```

```

PROCEDURE LOCST(FVAL; INTEGER) ;
  * ENTER CONSTANTS WITH VALUE FVAL INTO CONSTANT TABLE CSTIB
  IFF NOT YET PRESENT * ELSE CHAINS OCCURENCE OF FVAL IN CODE
  * THROUGH UNLAB*
  VAR I; SHRINK;
  BEGIN
    IF I = 1 TO LCX DO
      WITH CSTIB(I) DO
        IF VAL = FVAL THEN
          BEGIN CHNIX=0 THEN ERROR(47) ELSE
            BEGIN WITH UNLAB(CHNIX) DO
              BEGIN I:=SUCC; SUCC:=INX; INX:=CHNIX; PLACET:=HCA
                END;
              CHNIX := I;
            END;
            GOTO 10
          END;
          IF LCX EQ CSIMAX THEN BEGIN ERROR(52); GOTO 10 END;
          IF CHNIX EQ 0 THEN BEGIN ERROR(47); GOTO 10 END;
          LCX := LCX + 1;
          WITH CSTIB(LCX) DO
            BEGIN
              VAL:=FVAL; INX:=CHNIX
            END;
            WITH UNLAB(CHNIX) DO
              BEGIN I:=SUCC; SUCC:=0; PLACET:=HCA END;
            CHNIX := I;
          END;
        END * LOCST;
      *****
    10; *****
  *****

```

```

PROCEDURE ADDRESSVAR(FCIP:INTEGER; VAR FATR:ATTR) ;
*BUILD UP ATTRIBUTE IN FATR AND VARIABLE IS POINTED TO BY FCIP*
BEGIN
  WITH CONTEXTTABLE(FCIP), FATR DO
    BEGIN
      KIND := VARBL;
      IF KCLASS = VARS THEN
        BEGIN
          VTYPE := VTYPE; PCKD := FALSE ;
          IF VTYPE = ACTUAL THEN
            BEGIN
              ACCESS:=DRCT; DPLMT:=VADDR+BLC+BFLC+1 ;
              BREG := VLEVEL
            END ELSE
              BEGIN
                WRITE(=,=FORMALE) ;
              END ELSE
                IF KCLASS = FIELD THEN
                  BEGIN
                    WITH DISPLAY(DISK) DO
                      BEGIN
                        IF OCCUR=CH WITH THEN
                          BEGIN
                            ACCESS := DRCT ; BREG := CLEVEL ;
                            DPLMT := FLOADDR + CDISP
                          END ELSE
                            BEGIN
                              WRITE(=,=FORMALE)
                            END
                          IF BITWIDTH NE 0 THEN
                            BEGIN
                              PCKD := TRUE; BITADR := BITDISPL ;
                              BYSIZE := BITWIDTH
                            END ELSE
                              PCKD := FALSE
                            END
                          ELSE
                            BEGIN
                              FIELD
                            END
                              BEGIN
                                PROCEDURE
                                BEGIN
                                  VTYPE := PROCTYPE ; ACCESS := DRCT ;
                                  BREG := PROCLEVEL + 1 ; DPLMT := 2 ; PCKD := FALSE ;
                                  IF PROCKIN=FORMAL THEN ERROR(62)
                                  ELSE IF LEVEL NE
                                END
                              WITH FCIP,FATR
                            END * ADDRESSVAR ;

```



```

PROCEDURE VARIABLE ;
VAR LATR ; ATTR
TEMP, TEMP1 ; INTEGER ;
BEGIN
ADDRESSVAR(CIPR, LAIR) ;
INSYMBOL ;
IF NO EQ 17 * THEN
BEGIN
INSYMBOL ;
IF NO EQ 1 THEN *ID*
BEGIN
LATR.TYPR NE 0 THEN
BEGIN
TEMP := LATR.TYPR ;
IF CONTEXTTABLE(TEMP).FORM EQ RECORDS THEN
WITH LATR DO
BEGIN
TEMP := CONTEXTTABLE(TYPR).FSIFLD ;
SRCHREC(TEMP) ;
IF CIPR EQ 0 THEN
BEGIN ERROR(27) ; CIPR := UNDECPR END ;
WITH CONTEXTTABLE(CIPR) DO
BEGIN
TYPR := FLOIYPE ;
OPLMT := OPLMT + FLOADDR ;
IF BITWIDTH NE 0 THEN
BEGIN
PCKD := TRUE ; BITADR := BITDISPL ;
BITS := BITWIDTH ;
END ELSE PCKD := FALSE
END WITH LATR * ELSE
BEGIN
ERROR(26) ; LATR.TYPR := 0
END
TYPR NE 0 * ELSE
BEGIN ERROR(28) ; LATR.TYPR := 0 END ;
INSYMBOL
END * NO = 1 *
END * IF NO = 17 * ELSE
BEGIN
GENRE(16400B, 0, 0, FALSE) ; * CLB *
GENRE(176000B, BFLC+4, 1, TRUE) ; * STB IN BFLC * *
REPEAT
WITH LATR DO

```



```

IF TYPR NE 0 THEN
IF CONTEXTTABLE(TYPR).FORM NE ARRAYS THEN
BEGIN ERROR(63); TYPR=0 END ;
INSYMBOL;
IF CIX THEN EXPRESSION ELSE BEGIN SIMP;
EXPRESSON END ;
IF GATR.TYPR NE 0 THEN
BEGIN
TEMP1:= GATR.TYPR ; TEMP1:= LATR.TYPR ;
IF CONTEXTTABLE(TEMP).FORM GT SYMBOLIC THEN ERROR(64);
IF TEMP1 NE 0 THEN
BEGIN
CONEXIABLE(TEMP1).INXTYPE;
IF ((CONTEXTTABLE(TEMP).FORM EQ SYMBOLIC)
OR (CONTEXTTABLE(TEMP1).FORM EQ SYMBOLIC))
AND (TEMP NE 0) THEN ERROR(31) ;
WITH KIND EQ SVAL THEN
BEGIN
IF (CONTEXTTABLE(TEMP1).LO GT VAL)
OR (CONTEXTTABLE(TEMP1).HI LE VAL)
THEN ERROR(57) ;
END ;
LATR.CONTEXTTABLE(TYPR) DO
BEGIN
TEMP:= CONTEXTTABLE(AELTYPE).SIZE;
GENRE(1002009,0,0,FALSE) ; *MPY*
GENRE(TEMP) ;
DPLMT := DPLMT - LO * TEMP + 10000B ;
GENRE(42000B,0,1,TRUE) ; * ADA DISPLACEMENT *
GENRE(420000B,BFLC+4,1,TRUE) ; * ADA TO BFLC+4 *
GENRE(73000B,BFLC+4,1,TRUE) ; * STA IN BFLC+4 *
IF ACCESS EQ DRCT THEN ACCESS := INXD ;
END ;
LATR.TYPR NE 0 ;
END ;
GATR.TYPR NE 0 ;
IF LATR.TYPR := CONTEXTTABLE(TEMP1).AELTYPE ;
UNTIL NO NE 15 ; *
IF NO NE 12 THEN * ;
CIX := TRUE ;
END ; IF NO EQ 11 ;
CALL LAYG ;
END VARIABLE ;

```



```

PROCEDURE PRIMARY ;
BEGIN NO EQ 1 THEN
  BEGIN
    SPC(HREC(NEXT)) ;
    IF CTPTR EQ 0 THEN
      BEGIN ERROR(31) ; CTPTR := UNDECPTR END ;
    CASE CONTEXTTABLE(CTPTR).KLASS OF
      BEGIN ERROR(45) ;
      BEGIN GATTR.TYPR:=0;INSYMBOL
    END;
    WITH GATTR,CONTEXTTABLE(CTPTR) DO
      BEGIN
        TYPR:=CONTYPE;
        IF CONKIND=ACTUAL THEN
          BEGIN
            KIND :=SVAL; VAL := VALUES
          END ELSE
            BEGIN
              KIND := VARBL; ACCESS:=DRCT; BREG:=CLEVEL;
              DLMIL=CADDR; PKD:=FALSE
            END;
            INSYMBOL;
          END;
        END;
      END;
    PROC1
    VARS, FIELDS ; VARIABLE ; CLASS OF ;
    END ; IF NO=1 ; ELSE
    IF NO EQ 2 THEN
      BEGIN WITH GATTR DO
        BEGIN
          KIND:=SVAL; CTY:=TRUE; VAL:=IVAL ;
          CASE CL OF
            GATTR:=INTPYR;
            TYPR:=CHARPYR;
          END ; CASE ;
          END ; GATTR ;
          INSYMBOL ;
          END ; IF NO=2 ; ELSE
          BEGIN
            WHILE NO EQ 9 DO INSYMBOL ;
            IF CTY THEN EXPRESSION ELSE
              BEGIN SLMPL; EXPRESSION END ;
            IF NO NE 10 THEN ERROR(7) ELSE

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```

BEGIN
  LOIMP
  IF RP EQ 0 THEN BEGIN WHILE NO EQ 10 DO INSYMBOL END
  ELSE INSYMBOL

```

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END
  IF NO=9+ ELSE
  BEGIN ERROR(29) GATTR.TYPTRI=0 END
  END PRIMARY
  *****

```



```

PROCEDURE ASSMB(A,B,C; INTEGER);
  * PRODUCES CODE FOR THE SHIFT OPERATOR *
  BEGIN
    IF A EQ 3 THEN

```

```

      BEGIN

```

```

        GENRE(B;0,0,FALSE);
        GENRE(C;0,0,FALSE);

```

```

      END ELSE

```

```

        IF A EQ 2 THEN GENRE(B;0,0,FALSE) ELSE
        IF A EQ 1 THEN GENRE(C;0,0,FALSE) ;

```

```

      END * ASSMB *

```

```

      * * * * *

```

```

PROCEDURE MODE(A,B; INTEGER); VAR C; INTEGER;

```

```

  * FINDS C := A MOD B AND 0 := A DIV B *
  BEGIN

```

```

    C := A MOD B;
    D := A DIV B ;

```

```

  END * MODE *

```

```

  * * * * *

```

```

PROCEDURE SYSOP ;
VAR TEMP,TEMP1,I,J,II: INTEGER ;
BEGIN
  OP:=NO; LADOPCL:=CL; INSYMBOL;
  IF (LADOPCL EQ 11) OR (LADOPCL EQ 12) THEN
    BEGIN
      RATR.TYPTR := GATR.TYPTR;
      IF NO NE 1 THEN ERROR(26) ELSE
        BEGIN
          PRIMARY;
          IF (RATR.TYPTR NE 0) AND (GATR.TYPTR NE 0) THEN
            BEGIN
              RATR.TYPTR EQ GATR.TYPTR THEN
                BEGIN
                  WITH GATR DO
                    BEGIN
                      CASE KIND OF
                        BEGIN
                          IF LADOPCL EQ 11 THEN
                            GENRE(32000B,DPLMI,1,TRUE) * IOR+ ELSE
                            GENRE(22000B,OPLHI,1,TRUE) * XOR+ ;
                        END;
                      BEGIN
                        IF LADOPCL EQ 11 THEN
                          GENRE(32000B,0,1,TRUE) * IOR+ ELSE
                          LDCSY(VAL) ;
                        END;
                      CASE *
                        END ELSE WITH GATR+
                        END *TYPTR NE 0+ ELSE ERROR(32) ;
                      END * NO EQ 1+
                      END * CL=11 OR CL=12+ ELSE
                        BEGIN
                          IF (NO NE 2) AND (CL NE 1) THEN ERROR(33) ELSE
                            BEGIN
                              IF (LADOPCL EQ 9) OR (LADOPCL EQ 10) THEN
                                BEGIN
                                  MODE(IVAL,TEMP,TEMP1) ;
                                  IF LADOPCL EQ 9 THEN *ALF+
                                  ASSMB(TEMP,17278,1700B) ELSE
                                  ASSMB(TEMP,57278,5700B) ;
                                END;
                              END * CL=9 OR CL=10+ ELSE
                                BEGIN
                                  MODE(IVAL,16,TEMP,TEMP1) ;
                                END;
                            END;
                        END;
                    END;
                END;
            END;
          END;
        END;
      END;
    END;
  END;

```

IF (LADOPCL EQ 7) OR (LADOPCL EQ 8) THEN

BEGIN MODE (TEMP, I, J) ;  
IF LADOPCL EQ 7 THEN  
BEGIN ASSMB(J, 17278, 17008) \*ALE\* ;  
ASSMB(I, 57278, 57008) \*RAL\* ;  
END ELSE \*CL=8\* ;

BEGIN GENRE(33008, 0, 0, FALSE) ; \*LDB 0\*  
ASSMB(J, 57278, 57008) ; \*BLF\*  
ASSMB(I, 52228, 52008) ; \*RBL\* ;  
END ; \*CL=7 OR CL=8\* ELSE

BEGIN MODE (TEMP, I, J) ;  
FOR I=1 TO J DO  
BEGIN

CASE LADOPCL OF  
GENRE(10208, 0, 0, FALSE) ; \*ALS, ALS\*  
BEGIN  
GENRE(33008, 0, 0, FALSE) ; \*LDB 0\*  
GENRE(50208, 0, 0, FALSE) ; \*BLS, BLS\*  
END ;  
GENRE(11218, 0, 0, FALSE) ; \*ARS, ARS\*  
BEGIN  
GENRE(33008, 0, 0, FALSE) ; \*LDB 0\*  
GENRE(51218, 0, 0, FALSE) ; \*BRS, BRS\*  
END ;  
GENRE(13238, 0, 0, FALSE) ; \*RAR, RAR\*  
BEGIN  
GENRE(33008, 0, 0, FALSE) ;  
GENRE(53238, 0, 0, FALSE) ; \*RBR, RBR\*  
END ;  
END CASE ;  
END \*FOR\* ;  
END \*CL=8 OR CL=10\* ;  
END \*NO EQ 2 AND NO EQ 1\* ;  
END \*SHIFT OPERATOR\* ;  
END \*SYSP\* ;  
\*\*\*\*\*

11  
21

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41

51  
61

```

PROCEDURE EXPRESSION ;
VAR LFG:BOOLEAN;
    BT1:BOOLEAN;
    BT2:BOOLEAN;
BEGIN
    LFG:=FALSE;
    IF NOT EQ 7 THEN *ADDDP+
    BEGIN
        OP:=NO;
        IF CL EQ 2 THEN LFG:=TRUE
        ELSE IF CL EQ 3 THEN ERROR(33);
        INSMBOL;
    END;
    IF NO EQ 5 THEN *NOI+
    BEGIN
        OP := NO; IF CL EQ 1 THEN LFG:= TRUE ELSE ERROR(33);
        INSMBOL;
    END;
    PRIMARY;
    IF CIFY THEN
    BEGIN
        GATR:=YPIR-NE 0 THEN
        BEGIN
            CIV:= FALSE;
            WITH GATR DO
                BEGIN
                    CASE KIND OF
                    BEGIN
                        GENRE(062000B,0,1,TRUE); *CLDA+
                        LDCS(1,1); *CIV:=FALSE;
                    END;
                    BEGIN
                        IF ACCESS EQ ORCY THEN GENRE(62000B,DPLMT,1,TRUE)
                        ELSE GENRE(162000B,BFLC,4,1,TRUE);
                    END;
                    CASE +
                    END;
                    GATR+;
                    YPIR+;
                END;
            IF LFG THEN
            BEGIN
                IF OP EQ 5 THEN GENRE(3000B,0,0,FALSE) *CMA+ ELSE
                GENRE(003004B,0,0,FALSE); *CMA,INA+
            END;
            LAITR:=GATR;
            WHILE (NO EQ 7) OR (NO EQ 6) OR (NO EQ 4) OR (NO EQ 2) DO
            BEGIN

```





```

18      CASE LADOPCL OF
      BEGIN
      IF ACCESS EQ DRCT THEN
      BEGIN
      GENRE(1002008,0,0,FALSE);MPY;
      GENRE(0PLMI,0,1,FALSE);AD.FOR MPY;
      GENRE(300018,0,0,FALSE);PIOR;
      END ELSE
      BEGIN
      GENRE(1620008,0FLC+4,1,TRUE);LDA;
      LDIMP;
      END ;
      END ;
20      ;
30      ERROR(32) ;
31      BEGIN
32      IF ACCESS EQ DRCT THEN
      BEGIN
      GENRF(64008,0,0,FALSE);CLB;
      GENRE(20208,0,0,FALSE);SSA;
      GENRE(70048,0,0,FALSE);CHB;INB;
      GENRE(100408,0,0,FALSE);DIV;
      GENRE(0PLMI,0,1,FALSE);OP.FOR DIV;
      END ELSE
      BEGIN
      GENRE(1620008,0,1,TRUE);LDA;
      LDIMP;
      END ;
      END ;
50      ;
      END CASE CL;
      END VARL;
      BEGIN
      IF OP EQ 7 THEN
      BEGIN
      CASE LADOPCL OF
      BEGIN GENRE(0420008,0,1,TRUE);ADA;
      LDCST(VAL)
      END ;
      BEGIN
      GENRE(660008,0,1,TRUE);LDB;LDCST(VAL);
      GENRE(0070048,0,0,FALSE);CMB;INB;
      GENRE(040018,0,0,FALSE);ADA TO B;
      END ;
      ERROR(32) ;

```

18

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32

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SVAL;

18

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38

```

11      END * CASE CL *
12      END * NO=7 * ELSE
13      BEGIN
14      CASE LADOPCL OF
15      BEGIN
16      * GENRE(10020008,0,0,FALSE); *MPY*
17      * GENRE(0,0,1,FALSE); *OPERAND FOR MPY *
18      * LDCST(VAL);
19      * GENRE(0300018,0,0,FALSE) ; *IOR*
20      END;
21      * ERROR(32) ;
22      * BEGIN
23      * GENRE(00640008,0,0,FALSE) ; *CLB*
24      * GENRE(00202008,0,0,FALSE) ; *SSA*
25      * GENRE(0070048,0,0,FALSE) ; *SCMB,IN8*
26      * GENRE(10040008,0,0,FALSE) ; *DIV*
27      * GENRE(0,0,1,FALSE); *OPERAND FOR OIV *
28      * LDCST(VAL);
29      * END;
30      *
31      * END * CASE CL *
32      * END * NO NE 7 *
33      * END * SVAL *
34      * END * CASE KIND OF *
35      * END * GATR *
36      * END * RT1 AND BT2 * ELSE
37      * BEGIN
38      * IF LATR,YPTR = GATR,YPTR THEN
39      * BEGIN
40      * IF (GATR.YPTR=BOOLPTR) AND (LADOPCL EQ 3) THEN
41      * BEGIN
42      * WITH GATR DO
43      * BEGIN
44      * CASE KIND OF
45      * BEGIN
46      * IF OP EQ 7 THEN
47      * BEGIN
48      * GENRE(0320008,0PLMT,1,TRUE) ; *IOR*
49      * END * ELSE
50      * BEGIN
51      * GENRE(0120008,0PLMT,1,TRUE) ; * AND *
52      * END;
53      * END;
54      * BEGIN
55      * IF OP EQ 7 THEN

```

VARBL ;

SVAL ;

```

BEGIN (032000B,U,1,TRUE) ; *IOR↑
LOCSE(VAL) ;
END ELSE
BEGIN
GENRE(012000B,0,1,TRUE) ; *AND ↑
LOCSE(VAL) ;
END
END * SVAL↑ ;
END * CASE KIND OF ↑
END * GATTR↑
END * BOOLPTR↑ ELSE ERROR(50)
END * TYPTR↑
END ;
END * TYPTR NE 0 ↑ ;
* TRUE ;
END * STV=TRUE ↑ ;
END * NO=7 OR NU=6 ↑ ;
IF NO EQ 8 THEN *RELOP↑
BEGIN
OP=NO; LAOPCL=CL ;
WITH RATR DO
BEGIN TYPTR= GATTR.TYPTR; KIND=LCOND END ;
BEGNSYMBOL ; IF NO EQ 9 THEN EXPRESSION ELSE PRIMARY ;
IF STV THEN BEGIN
IF (RATR.TYPTR NE 0) AND (GATTR.TYPTR NE 0) THEN
BEGIN
IF RATR.TYPTR EQ BOOLPTR THEN RATR.ARITH=FALSE
ELSE RATR.ARITH=TRUE * ARITHHELIC↑ ;
IF RATR.TYPTR EQ GATTR.TYPTR THEN
BEGIN WITH GATTR DO
BEGIN
CASE KIND OF
BEGIN
IF ACCESS EQ DRCT THEN
GENRE(003000B,0,0,FALSE) ; *CHAINED↑
GENRE(042000B,DPLMT,1,TRUE) ; *ADA↑
ELSE
END
BEGIN
GENRE(162000B,BFLC,4,1,TRUE) ;
LDTMP
END ;
END ;
END * BEGIN
SYVAL↑

```

```

GENRE(0030048,0,0,FALSE) ; CHA,INA
GENRE(0420008,0,1,TRUE) ; ADA
LOCS(VAL) ;
END CASE KIND OF ;
RLEXP ; WITH GATR ;
END ELSE ERROR(45) ;
END ELSE ERROR(32) ;
END STY ;
SY := TRUE ;
RATR:TYPTR := BOOLPTR ;
GATR:RATR ;
END NO=6 ;
IF NO EQ 24 THEN BEGIN SYOP ; INSYMBOL END ;
END NO=7 OR NO=6 OR NO=24 ;
END EXPRESSION ;
.....

```

```

PROCEDURE ASSIGN ;
BEGIN NO EQ 1 THEN SRCHREC(NEXT) ELSE
BEGIN ERROR(33); SKIP(53) END ;
VARIABLE AATTR ; GATTR ;
IF NO NE 22 THEN *3=
BEGIN
IF GATTR.TYPTR NE 0 THEN ERROR(34);
SKIP(22);
IF NO NE 22 THEN
BEGIN
GATTR.TYPTR EQ 0 THEN ERROR(34) ;
GO TO ;
END;
END ; AATTR DO
WITH ACCESS EQ INXD THEN GENRE(1720008,BFLC+5,1,TRUE);
WITH SYMBOL ; EXPRESSION ;
IF GATTR.TYPTR EQ 0 THEN SKIP(53) ELSE
IF AATTR.TYPTR NE 0 THEN
BEGIN
IF AATTR.TYPTR NE GATTR.TYPTR THEN ERROR(35)
ELSE BEGIN
WITH AATTR DO
BEGIN
IF ACCESS EQ INXD THEN GENRE(1720008,BFLC+5,1,TRUE)
ELSE GENRE(1720008,DPLMT,1,TRUE) ;
END ;
END ELSE ERROR(32) ;
END ; ASSIGN ;
*****
11 *****
*****

```

```

PROCEDURE IFSTAT ;
VAR LCA1,LCA2; SHRINT ;
BEGIN
IF GATTR.TYPTR NE 0 THEN
BEGIN
GATTR.TYPTR NE 900LPTR THEN ERROR(36) ;
GENRE(020038,0,0,FALSE) ; *SZA,RSS*
GENRE(0260008,0,1,TRUE) ; *JMP*
LCA1:=HCA ;
END ;
IF NO NE 28 THEN *SV*THEN*
BEGIN
GATTR.TYPTR NE 0 THEN ERROR(37) ; SKIP(28) ;
IF NO NE 28 THEN
BEGIN
GATTR.TYPTR NE 0 THEN ERROR(37) ;
IF ERRCLINO EQ 2 THEN GOTO 30 ; GOTO 20 ;
END ;
ENDSYMBOL ;
STATEMENT ;
IF NO NE 29 THEN *SV*ELSE *
BE GENRE(0,0,0,FALSE) ; *NOP*
HPASILLCALL:ADRR := IC ;
END ELSE
BEGIN
GENRE(0260008,0,1,TRUE) ; *JMP*
LCA2:=HCA ;
GENRE(0,0,0,FALSE) ; *NOP*
HPASILLCALL:ADRR := IC ;
INSYMBOL ; STATEMENT ;
GENRE(0,0,0,FALSE) ; *NOP*
HPASILLCALL:ADRR := IC ;
END ;
END ; IFSTAT ;
*****

```

30:

```

PROCEDURE REPEATSTAT ;
VAR LJPADDR ; ADDRESS ;
BEGIN
  GENRE(0,0,0,FALSE) ; NOP
  LJPADDR := 0 ;
  REPEAT
  BEGIN
    INSYMBOL ;
    STATEMENT ;
    IF ERRCL(N) EQ 1 THEN BEGIN ERROR(39) ; GOTO 20 END ;
    IF NO EQ 29 THEN * ELSE
    BEGIN ERROR(36) ; INSYMBOL ; GOTO 20 END ;
  END
  UNTIL NO NE 16 ; * SY# ;
  IF NO VE 33 THEN * SY#UNTIL ERROR(44) ELSE
  BEGIN INSYMBOL ; EXPRESSION ;
    IF GATTR.TPTR NE 0 THEN
    BEGIN
      GENRE(0020038,0,0,FALSE) ; SZARSS ; LJPADDR ;
      GENRE(0260008,LJPADDR,TRUE) ; AJHP TO LJPADDR ;
      GENRE(0,0,0,FALSE) ; * NOP ;
    END ELSE SKIP(53)
  END
  END REPEATSTAT ;

```

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\*\*\*\*\* REPEATSTAT \*\*\*\*\*





```

PROCEDURE MHILESTAT ;
VAR LJPADDR ; ADDRESS ; LCA ; SHRINT ;
BEGIN
  GENRE(0,0,0,0,FALSE) ; * NOP +
  LJPADDR := IC ;
  INSYMBOL ; EXPRESSION ;
  BE GIN
    GENRE(0020038,0,0,FALSE) ; * SZA,RSS +
    GENRE(0260008,0,0,1,TRUE) ; * JMP +
    LCA := HCA ;
  END ;
  IF NO NE 35 THEN *SY:004
  BE GIN
    GATTR.TYPR EQ 0 THEN ERROR(40) ; SKIP(35) ;
    IF NO NE 35 THEN
    BE GIN
      IF GATTR.TYPR EQ 0 THEN ERROR(40) ;
      IF ERCL(10) EQ 2 THEN GOTO 20 ; GOTO 10
    END ;
  END ;
  INSYMBOL ;
  STATEMENT(0000,LJPADDR,1,TRUE) ; * JMP TO LJPADDR +
  GENRE(0,0,0,FALSE) ; * NOP +
  HPAS(LCA).ADDR := IC ;
  END ; MHILESTAT ;
*****

```

201

101

\*\*\*\*\*

```

PROCEDURE GOTOSTAT ;
BEGIN
  INSYMBOL ;
  IF (NO NE 2) OR (CL NE 1) THEN
    BEGIN ERROR(41); SKIP(53) END ELSE
    BEGIN
      IF IVAL GE MAX10 THEN ERROR(58) ;
      * SEARCH THRU LABELTABLE OF CURRENT BLOCK*
      FOR I := 1 TO CLABIX DO
        WITH LABTAB(I) DO
          BEGIN
            LABVAL EQ IVAL THEN * LABEL ALREADY OCCURED *
            BEGIN *IF DECL OCC* *GENERATE CODE ELSE CHAIN OCC*
              IF FLD2 EQ 0 THEN
                GENRE(026000B,FLD3,1,TRUE) * JMP TO FLD3* ELSE
                BEGIN
                  GENRE(J26000B,0,1,TRUE) ; *JMP*
                  IF CHNIX EQ 0 THEN
                    BEGIN ERROR(47); GOTO 20; END ;
                  WITH UNLAB(CHNIX) DO
                    BEGIN
                      IT1:=SUCC; SUCC:=FLD3; FLD3:=CHNIX;PLACE:=HCA
                      END ;
                      CHNIX := IT1;
                      END ;
                      GOTO 20 ;
                    END * LABVAL = IVAL*
                    END * WITH LABTAB* ;
                    * LABEL NOT YET MET * ENTER IT INTO LABELTABLE *
                    GENRE(026000B,0,1,TRUE); * JMP*
                    IF CLABIX EQ MAXLAB THEN BEGIN ERROR(46); GOTO 20 END ;
                    IF CHNIX EQ 0 THEN BEGIN ERROR(47); GOTO 20 END ;
                    CLABIX := CLABIX + 1 ;
                    WITH LABTAB(CLABIX),UNLAB(CHNIX) DO
                      BEGIN
                        IT1:=SUCC; LABVAL:=IVAL; SUCC:=0; FLD2:=CHNIX;
                        FLD3:= CHNIX ; PLACE := HCA ;
                        END ;
                        CHNIX := IT1;
                        INSYMBOL ;
                        END * IF NO=2 AND CL=1 * ;
                        END * GOTOSTAT* ;
                        *****
          END ;
        END ;
      END ;
    END ;
  *****

```

```

PROCEDURE FORSIAT;
VAR LATR:ATTR; LCCLASS, LCA: INTEGER;
    LJPADD: ADDRESS; LOF: BOOLEAN;
BEGIN
    INSYMBOL;
    IF NO NE 1 THEN
        BEGIN ERKOR(11); GATTR.TYPTR:=0 END ELSE
        BE SRCHREC(NEXT);
        IF CTPTR EQ 0 THEN BEGIN ERROR(23); CTPTR:= UNDECPTR END;
        VARIABLE;
    END;
    LATT: GATTR;
    IF NO NE 22 THEN * SY: NE * *
        BEGIN
            GATTR.TYPTR NE 0 THEN ERROR(33);
            SKIP(22);
            IF NO NE 22 THEN
                BEGIN
                    IF GATTR.TYPTR EQ 0 THEN ERROR(33);
                    IF ERRCLNO1 EQ 0 THEN GOTO 20; GOTO 10;
                END;
            END;
            INSYMBOL; EXPRESSION;
            WITH LATR DO GENRE(72000B, DPLMT, 1, TRUE); * STA IDENTIFIER *
            IF NO NE 37 THEN * SY: NE 10/DOWNT0 *
                BEGIN
                    GATTR.TYPTR NE 0 THEN ERROR(70); SKIP(37);
                    IF NO NE 37 THEN
                        BEGIN
                            IF GATTR.TYPTR EQ 0 THEN ERROR(44);
                            IF ERRCLNO1 EQ 0 THEN GOTO 20; GOTO 10;
                        END;
                    END;
                    LCCLASS:=GL; INSYMBOL; EXPRESSION;
                    IF (GATTR.TYPTR NE 0) AND (LATR.TYPTR NE 0) THEN
                        BEGIN
                            GENRE(72000B, BFLC+3, 1, TRUE); * STA IN BFLC+3 *
                            LJPADD:= IC+1;
                            WITH LATR DO
                                BEGIN
                                    LCCLASS EQ 1 THEN
                                        BEGIN
                                            GENRE(62000B, DPLMT, 1, TRUE); * LOA IDENTIFIER *
                                            GENRE(3006B, 0, FALSE); * CHA: INAL *
                                            GENRE(42000B, BFLC+3, 1, TRUE); * ADA TEMP *
                                        END;
                                    END;
                                END;
                            END;
                        END;
                    END;
                END;
            END;
        END;
    END;

```

```

END ELSE * DOWNTO *
BEGIN
  GENRE(620008,8FLC+3,1,TRUE); *LOA TEMP*
  GENRE(39048,0,0,FALSE); *CMA,INA*
  GENRE(420008,DPLMT,1,TRUE); *ADA IDENTIFIER*
END
END * LATTR *
GENRE(20228,0,0,FALSE); *SSA,SZA*
GENRE(260008,0,1,TRUE); *JMP OUT OF THE LOOP*
LCA = HCA
END * LATTR AND GATTR NE 0 *
IF NO NE 35 THEN *SY NE 00 *
BEGIN
  GATTR.TYPTR NE 0 THEN ERROR(40); SKIP(31);
  IF NO NE 31 THEN
  BEGIN
    GATTR.TYPTR EQ 0 THEN ERROR(40);
    IF ERRCLINO EQ 0 THEN GOTO 20 ; GOTO 10;
  END * 00 *
  END SYMBOL *
  STATEMENT *
  IF LATTR.TYPTR NE 0 THEN
  BEGIN
    H LATTR DO
    BEGIN
      GENRE(620008,DPLMT,1,TRUE) ; *LOA IDENTIFIER*
      IF LCLASS EQ 1 THEN GENRE(20048,0,0,FALSE) ELSE
      *BEGIN GENRE(74008,0,0,FALSE); *GENRE(400018,0,0,FALSE)
      *ADA TO B REGISTER* END;
      GENRE(720008,UPLMT,1,TRUE); *SIA IDENTIFIER*
    END
    GENRE(260008,LJPADDR,1,TRUE); *JMP BACK TO LOOP*
    GENRE(0,0,0,FALSE); *NOP *
    HP AS(LCA).ADDR = IC ;
    END * LATTR.TYPTR NE 0 *
  END FORSLA
  *****
  100 *****
  *****

```

200

100

```

PROCEDURE SRCHX (VAR P; INTEGER) ;
  * SEARCHES TABLES OF EXTERNALS ;

```

```

BEGIN
  CIPTR := P ;
  WHILE CIPTR NE 0 DO

```

```

  BEGIN
    COMP(EXTX(CIPTR),XNAM,AVAL,COMPARI) ;
    IF COMPAR THEN GOTO 1 ELSE CIPTR := CIPTR-1 ;

```

```

  END ; SRCHX ;
  *****
  *****
  *****

```

```

PROCEDURE EXTERN ;

```

```

BEGIN
  SRCHX(XPTR) ;
  IF COMPAR THEN
    BEGIN CIPTR EQ 0 THEN BEGIN ERROR(61) ; GOTO 1 END ;

```

```

  END ELSE

```

```

  BEGIN
    XPTR := XPTR+1 ;
    WHILE EXTX(XPTR) DO

```

```

  BEGIN

```

```

  I := 0 ;

```

```

  REPEAT

```

```

  UNTIL I EQ 10 ;

```

```

  EXSYM := EXSYM+1 ;

```

```

  XSYM := EXSYM ;

```

```

  END

```

```

END * COMPAR ;

```

```

END * EXTERN ;
  *****
  *****
  *****

```

PROCEDURE WRITEIR ;  
VAR ST-HR : INTEGER ;  
BEGIN

WRFL := TRUE ;  
INSYMBOL ;  
IF NO NE 9 THEN BEGIN ERROR(50) ; SKIP(53) ; GOTO 1 END ;  
INSYMBOL ;  
IF (NO NE 2) OR (CL NE 1) THEN BEGIN ERROR(59) ; SKIP(53) ;  
BEGIN ST-HR :=IVAL ; INSYMBOL END ;  
IF GOTO 1 END ELSE THEN \* → BEGIN ERROR(51) ; SKIP(53) ; GOTO 1  
IF NO NE 15 THEN \* → BEGIN ERROR(51) ; SKIP(53) ; GOTO 1  
IF NO NE 6) OR (CL NE 1) THEN BEGIN ERROR(60) ; SKIP(53) ;  
GOTO 1 END ELSE BEGIN INSYMBOL ; IF NO NE 15 THEN

ERROR(113) END ;  
GENRE(00260008,0,0,FALSE) ; \* CLA → STA →  
GENRE(00720008,BFLC+2,1,TRUE) ; \* STA →  
REPEAT

IF TRUE ;  
INSYMBOL ; EXPRESSION ;  
IF GATR.TYPTR EQ 0 THEN ERROR(75) ELSE  
BEGIN

GENRE(06600008,BFLC+1,TRUE) ; \* LDB →  
GENRE(04600008,BFLC+2,1,TRUE) ; \* ADB →  
IF GATR.TYPTR EQ CHARGE THEN \* CME →  
GENRE(02200008,0,0,FALSE) ; \* JSB →  
ELSE (GENRE(00210008,0,0,FALSE) ; \* CLE →  
GENRE(01600038,0,0,FALSE) ; \* ADA →  
GENRE(00720008,BFLC+2,1,TRUE) ; \* STA →  
GENRE(07200008,BFLC+2,1,TRUE) ; \* STA →  
END TYPTR NE 0 →

UNTIL NO NE 15 THEN \* → ERROR(78) ELSE INSYMBOL ;  
IF NO NE 10008,BFLC+2,1,TRUE) ; \* ADA →  
GENRE(06600008,BFLC+1,TRUE) ; \* LDB →  
GENRE(01600028,0,0,FALSE) ; \* JSB →  
GENRE(00180000,0,0,FALSE) ; \* ASS →  
GENRE(00180000,0,0,FALSE) ; \* LOGICAL UNIT NO. →  
END WRITEIR ;  
\*\*\*\*\*

```

PROCEDURE VARIAB ;
BEGIN
  IF NO NE 1 THEN
    BEGIN ERROR(11); GATTR.TYPTR:=0 END ELSE
    BEGIN
      SRCHREC(NEXI);
      IF CTPTR EQ 0 THEN
        BEGIN ERROR(31); CTPTR:= UNDECPTR END ELSE VARIABLE;
      END ; VARIAB+1 ; *****
*****
PROCEDURE READR ;
VAR STR ; INTEGER ;
BEGIN
  WRF:=TRUE; INSYMBOL; THEN BEGIN ERROR(50); SKIP(53); GOTO 1 END;
  IF NO NE 9 THEN BEGIN ERROR(50); SKIP(53); GOTO 1 END;
  INSYMBOL;
  IF (NO NE 2) OR (CL NE 1) THEN BEGIN ERROR(59) ; SKIP(53);
    GOTO 1 END ELSE BEGIN STR:=IVAL; INSYMBOL END;
  IF NO NE 15 THEN STR+1; BEGIN ERROR(51); SKIP(53); GOTO 1
  END; ELSE INSYMBOL;
  IF (NO NE 6) OR (CL NE 1) THEN BEGIN ERROR(60) ; SKIP(53);
    GOTO 1 END ELSE BEGIN INSYMBOL; IF NO NE 15 THEN
      ERROR(51) END ;
      GENRE(660003,BFLC,TRUE); * LDB BUFFER ADDRESS+
      GENRE(160018,0,4,FALSE); * JSB GET+
      GENRE(120018,0,0,FALSE); * RSS+
      GENRE(150018,0,0,FALSE); * LOGICAL UNIT NUMBER+
      GENRE(160018,BFLC,1,TRUE); * LDB BUFFER POINTER+
      GENRE(1760003,BFLC+1,1,TRUE) ; *SIB BUFFER POINTER +
    REPEAT
      CTV IS TRUE;
      INSYMBOL; VARIAB;
      IF GATTR.TYPTR EQ 0 THEN ERROR(47) ELSE
        BEGIN
          GATTR.TYPTR EQ CHAPTR THEN GENRE(22008,0,0,FALSE)
          *CME+ ELSE GENRE(21008,0,0,FALSE); * CLE+
          GENRE(660008,BFLC+1,1,TRUE) ; * LDB BUFFER POINTR +
          GENRE(150048,0,4,FALSE); * JSB ALLOK+
          WITH GATTR DO
            BEGIN
              IF ACCESS EQ DRCT THEN
                GENRE(1720008,BPLMT,1,TRUE) *STA IN VARIABLE+ ELSE
                GENRE(1720008,BFLC+4,1,TRUE); * STA ARRAY ELEMENT +
            END
          GENRE(1760008,BFLC+1,1,TRUE) ; *STB THE BUFFER POINTER +
        END
      END
    END
  END

```

```
1: *****
      END * TYPTR NE 0 *
      UNTIU NO NE 15 * * * * *
      IF NO NE 10 THEN * * * * * ERROR(7) ELSE INSYMBOL;
      END * READIR *
*****
```



PROCEDURE COMPSTAT ;

BEGIN

REPEAT

BEGIN

INSYMBOL ;

STATEMENT ;

IF NO EQ 29 THEN ELSE ;

BEGIN ERROR(36) ; INSYMBOL ; GOTO 1 END ;

END ;

UNTIL NO NE 16 ;

IF NO NE 26 THEN

END ; COMPSTAT ;

END ;

1\*

\*\*\*\*\*

```

PROCEDURE STATEMENT ;
VAR LPSM ; SHRINT ;
BEGIN (NO EQ 2) AND (CL EQ 1) THEN LABEL →
BEGIN
  GENRE(0,0,0,FALSE) ; *NOP→
  IF IVAL GE MAXIO THEN ERROR(56) ;
  FOR I1=1 TO CLABIX DO
  WITH LABVAL(I1) DO
  BEGIN LABVAL EQ IVAL THEN *FOUND→
  BEGIN FLD2 EQ 0 THEN *MULTIDEF→ ERROR(48) ELSE *FIXUP→
  BEGIN
  I11:= FLD3 ;
  REPEAT
  WITH UNLAB(I1) DO
  BEGIN
  WITH HPAS(PLACE) DO ADDR := IC ; I11:=SUCC
  END
  UNTIL I1 EQ 0 ;
  I1:=FLD2 ; UNLAB(I11) ; SUCC :=CHNIX ;
  CHNIX := FLD3 ; FLD2:=0 ; FLD3:=IC ;
  END ;
  GOTO 1 ; FOR, WITH → ;
  END * IF, FOR, WITH → ;
  * NEW LABEL →
  IF CLABIX EQ MAXLAB THEN ERROR(46) ELSE
  BEGIN
  CLABIX := CLABIX + 1 ;
  WITH LABVAL(CLABIX) DO
  BEGIN LABVAL :=IVAL ; FLD2:=0 ; FLD3:=IC END ;
  END ;
  INSYMBOL := 21 THEN *1→
  IF NO NE
  BEGIN ERROR(10) ; SKIP(53) ; END ELSE INSYMBOL ;
  END * IF NO=2 AND CL=1 →
  *BASED ; SIVL:=TRUE ; CIVL:=TRUE ;
  *CASE SPLIT(SIAT(1)) OF
  *PASS→ 1 ; *PO OR 2→ 1
  *SRCHREC(NEXT) ;
  *BEGIN
  IF CIPTR EQ 0 THEN BEGIN ERROR(23) END ;
  WITH CONTEXT TABLE(CIPTR) DO
  IF KLASSE LE KONST THEN ERROR(54) ELSE
  IF (KLASSEBROCK) AND (PROCYBRE=CIPTR) OR
  (PROCTYPE EQ 0) THEN

```



```

* FOLLOWING ROUTINES ARE USED TO PROVIDE CODES IN RELOCATABLE
  FORMAT
*****
PROCEDURE PUNCHRECORD(RECNAME;ALFA; RL(INTEGER) ;
  VAR I,N,K,C,BEN(INTEGER) ;
  GLOBAL M
BEGIN
  IF LISTBIN THEN *LIST BINARY RECORD *
  BEGIN
    N:=0; WRITE(RECNAME);LINES(2);
    FOR I:=1 TO RL DO
      BEGIN
        ALFA(I):=OUTO(M(I)); WRITE(E);
        IF N EQ 6 THEN BEGIN N:=0; LINES(1) END ;
      END ;
    LINES(4);
  END;
  PUNCHBIN THEN * PUNCH BINARY RECORD, RELOCATABLE FORMAT *
  BEGIN
    FOR I:=RL+1 TO 60 DO W(I):=0;
    C:=0;
    REPEAT
      REM:=0; K:=0;
      FOR I:=1 TO 4 DO
        BEGIN
          N:=REM;
          FOR J:=C+1 TO C+3 DO APPEND(N,16,W(J)) ;
          K:=K+4 ;
          IF K LT 16 THEN
            BEGIN
              C:=C+4;
              J:=W(C) DIV 00(I);
              REM:= W(C)-J*00(I) ;
              APPEND(N,16-K,J);
            END ELSE C:=C+3;
            HPDIP+:=N; PUT(HPDIP)
          END
        UNTIL C EQ 60 ;
      END
    PUNCHRECORD * ;
  END
*****
*****
*****

```

```

FUNCTION CHECKSUM( RL ; INTEGER ) ; INTEGER ;
VAR I, CS ; INTEGER ; *GLOBAL--M
BEGIN
  CS := HI(2) ;
  FOR I := 1 TO RL DO CS := CS + W(I) ;
CHECKSUM := CS MOD 200000B
END * CHECKSUM * *****
*****
PROCEDURE PACKIN( VAR NIM ; AR ; VAR B ; ARRINT3 ) ;
VAR I, J, N ; INTEGER ;
BEGIN
  NIM(6) := 3 ;
  FOR I := 1 TO 3 DO
    BEGIN
      J := 2 * I - 1 ;
      BIT := ASCII CODE( NIM( J ) ) ;
      IF ( ( J + 1 ) EQ 6 ) THEN N := 0 ELSE N := ASCII CODE( NIM( J + 1 ) ) ;
      APPEND( B( I ), B, N ) ;
    END
  END * PACKIN * *****
*****
PROCEDURE NAMRECORD * GLOBAL - W, PRNAM, LC
VAR P ; ARRINT3 ;
BEGIN
  W(1) := 010400B ; * RECORD LENGTH, LEFT SHIFTED 8 BITS
  W(2) := 20000B ; * IDENT
  PACKIN( PRNAM, P ) ; * W(4..6) CONTAINS PROG. NAME
  FOR I := 1 TO 3 DO W( I + 1 ) := P( I ) ;
  W(7) := LC ;
  FOR I := 6 TO 17 DO W( I ) := 0 ;
  W(3) := CHECKSUM( 17 ) ;
  PUNCH RECORD( ENAM, RECORDE, 17 ) ;
  END * NAMRECORD * *****
*****
PROCEDURE EXTRECORD ;
VAR I ; K ; AL ; H1 ; H2 ;
*GLOBAL W, EXTIX, EXSYM
BEGIN
  H1 := 1 ;
  IF EXSYM LE 19 THEN BEGIN MORE := FALSE ; N2 := EXSYM END ELSE
  BEGIN MORE := TRUE ; H2 := 19 END ;
  * NEW EXT RECORD
  AL := 3 * H2 + 3 ; * RECORD LENGTH
  W(1) := 0 ; * INSERT IRL, B, W(1)
  W(2) := H2 + 100000B ; * ENTRIES * IDENT

```

```

K8=3;
FOR I3=M1 TO M2 00
  BEGIN
    PACK INDEX I3, XNAM, P; APPEND (P(3), 0, I3);
    FOR J3=1 TO 3 DO WK=J3; I3=I3; K8=K8+1;
  END;
  CHECKSUM(RL); CHECKSUM +
  PUNCH RECORD (TEXT RECORD=RL);
  IF MORE THEN
  BEGIN
    M18=M2+1; M21=M2+20;
    IF M2 GE EXSYM THEN BEGIN M2:=EXSYM; MORE:=FALSE END;
    GO TO 1;
  END;
  EXT RECORD +; *****
  END *****
*****
PROCEDURE OBL RECORD (PG, BITS);
  VAR M, I, J, K, L, N, NI; INTEGER; ENDC; BOOLEAN;
  BEGIN
    REPEAT WHEN RECORD EVERY LOOP +
    L:=4; NI:=0; ENDC:=FALSE;
    M:=1; I:=HPAS(1).LOC; RELOCATABLE LOAD ADDRESS +
    WHILE (L/LT 50) AND (NOT ENDC) DO
      BEGIN
        L:=L+1; J:=L; W(J) := 0;
        M:=M+1; W(J) CONTAINS RELOCATION INDICES FOR UP TO
        P NEXT FIVE WORDS +
        REPEAT
          M:=M+1; L:=L+1;
          IF HPAS(I).MREF THEN MEMORY REFERENCE +
          BEGIN
            APPEND (W(J), 3, 5); INST;
            CASE HPAS(I).TYP OF
              1: K8=0; 2: K8=1; 3: K8=2;
            END;
            APPEND (W(L), 0, K);
            L:=L+1; W(L) := HPAS(I).ADDR;
            END ELSE NO MEMORY REFERENCE +
            BEGIN
              HPAS(I).INST EQ 0 THEN W(L) := HPAS(I).ADDR ELSE
              W(L) := HPAS(I).INST;
              APPEND (W(J), 3, HPAS(I).TYP);
            END;
            I:=I+1; IF I GT MCA THEN ENDC:=TRUE;
          END;
        UNTIL M=5;
      END;
    NI:=NI+1;
  END;

```

```

UNTIL (M EQ 5) OR ENDC OR (L GE 59) ;
APPEND(W(J), (5-M)*3+1, 0) ;
NI=NI+M ;

```

```

END ;
W(1) := 0 ; INSERT(L, 8, W(1)) ;
W(2) := 500008+PG*1008+NI ;
W(3) := CHECKSUM(L) ;
PUNCH RECORD(EDBL RECORD=L) ;
UNTIL ENDC ;

```

```

END * DBLRECORD * ; ***** * ***** * ***** *

```

```

PROCEDURE ENDRECORD ;
VAR I:INTEGER ; * GLOBAL * H, TRANSFR

```

```

BEGIN
W(1) := 20008 ; * RECORD LENGTH=4, SHIFTED TO 8 BITS *
W(2) := 1200008+TRANSFR(1)+ TRANSFR(2) ;
W(3) := TRANSFR(3) * * MAIN PROGRAM IT IS 0 *
W(4) := (W(2)+W(4)) MOD 2000008 ;
PUNCH RECORD(=END RECORD=L) ;
END * ENDRECORD * ;

```

```

*****

```

```
PROCEDURE ENTERBODY ;  
  * GENERATES PROCEDURE PROLOG CODE AND  
  * PROCEDURE ENTRY CODE ↓
```

```
  VAR I: INTEGER ;  
  BEGIN
```

```
    LCX := 0 ; HCA := 0 ;
```

```
    IF CLABIX = 0 THEN  
      BEGIN * PROCEDURE ENTRY CODE ↓
```

```
        WRITE(=, EDOLATERE, EOL)
```

```
      END  
    ELSE
```

```
      BEGIN * MAIN ↓ BLCIES;
```

```
        BLC := 2 ;
```

```
        GENRE(0,0,0,FALSE) ; * NOP ↓
```

```
        GENRE(260008,LC+BLC+BFLC+1,1,TRUE) ; * JMP ↓
```

```
        GENRE(0,0,1,FALSE) ;
```

```
        FOR I:=1 TO BLC+LC DO GENRE(0,0,0,FALSE) ;
```

```
        IF VALP THEN
```

```
          BEGIN
```

```
            I:=1 ;
```

```
            FOR J:=VLC TO AI-1 DO
```

```
              BEGIN GENRE(0,VARB(I),0,FALSE) ; I:=I+1 END ;
```

```
              FOR I:=AI+1 TO LC DO GENRE(0,0,0,FALSE) ;
```

```
            END ;
```

```
          END * ENTERBODY ↓ ;
```

```
*****  
*****  
*****  
*****  
*****  
*****  
*****
```



```

PROCEDURE EXITBODY | TO HALT THE EXECUTION OF THE PROGRAM |
  * GENERATES CODE |
  BEG |
    GENRE (16 00 50, 0, 1, FALSE) ; * JSB EXEC *
    GENRE (0, 1, 0, 1, 1, FALSE) ; * DEF **2 |
    GENRE (0, 1, 0, 1, 1, FALSE) ; * NOP *
    GENRE (0, 0, 0, 0, 1, FALSE) ; * DEC 16 *
    GENR (EXITBODY) *
  END |
  * * * * *

```



```

BEGIN
  MAIN PROCEDURE OF HPCOM CALLING DIFFERENT PARTS OF THE
  COMPILER AS NECESSARY
  CH := INPUT
  SEG1 := INITIALIAZIIION
  IF (NO EQ 42) OR (NO EQ 43) THEN SEG2 :=
  *CONSTANT OR LABEL DECLARATION PART*
  IF NO EQ 40 THEN * TYPE
  BEGIN INSYMBOL ; DO
  WHILE NO EQ 41 DO
  BEGIN SRCHR(CINEXT) ; THEN ERROR(8)
  I := 0
  REPEAT I := I+1 ;
  UNTIL I=10 ;
  INSYMBOL ;
  IF (NO NE 8) OR ( CL NE 6) THEN ERROR(4)
  ELSE INSYMBOL ;
  ERR := FALSE ; IYREDECL(FL,8) ;
  IF TERR THEN
  BEGIN
  COMP(CONTEXTIABLEIP) NAME, BLANK, COMPAR) ;
  IF NOT(COMP) THEN ERROR(55) ELSE
  BEGIN WITH CONTEXTTABLEIP) DO
  BEGIN REPEAT
  I := I+1 ;
  UNTIL I=10 ;
  NAME(I) := IYPI(I) ;
  UNTIL I=10 ;
  NXTL := NEXT ;
  END ;
  NEXT := P ;
  END ;
  END ; FINDSEMICOLON ;
  END ;
  END ;
  END ; TYPE ;
  IF NO EQ 51 THEN SEG4 := *SYNONYM DECLARATION*
  IF NO EQ 45 THEN SEG3 := *VARIABLE DECLARATION*
  IF NO EQ 48 THEN SEG5 ELSE VCI=LC ;
  IF IS INITIALIZED TO -1, SO THAT LOAD POINT IS ZERO
  IC := - 1 ;

```

```

ENTERBODY !
OP ! = FALSE ! THEN COMPSTAT ELSE ERROR(30) !
IF NO EQ 25 THEN COMPILING STATEMENT PART !
EXITBODY !
WRITEOUT !
WRITE(1) !
INSYMBOL !
COMP(1) !
IF NOT(1) !
IF ERR !
IF RL !
BEGIN !
WRITE(EOL) !
LINES(4) !
FOR I = 1 TO HCA DO !
WITH HPASC(I) DO !
BEGIN !
LINES(1) !
WRITE(1) !
WRITE(1) !
WRITE(1) !
WRITE(1) !
END !
NAMRECORD !
EXTRECORD !
OBLRECORD !
ENDRECORD !
IF PUNCHBIN THEN PUNCH A BLANK CARD !
BEGIN !
LSTBIN ! = FALSE !
PUNCHRECORD(=BLANK CARD, 0) !
END !
WHILE ((INPUT + EQ =) OR (INPUT + EQ EOL)) !
AND NOT(EOL) ! DO GET (INPUT) !
IF NOT(EOL) ! THEN START COMPILING NEXT PROGRAM !
BEGIN WRITE(EOL) !
CH = INPUT !
WRITE(CH) !
GO TO 1 !
END !

```

( 2 )

END.

## APPENDIX H

SAMPLE COMPILED PROGRAMS IN PL/2100

```

PROGRAM FACT,L,B,P  CONST  MAX=4;
000000
000000  VAR  N,FACT; INTEGER ;
000000  BEGIN
000000  K:=1;
1:  IF (K EQ MAX) THEN GOTO 2 ;
000016  K:=K+1;
000031  FACT:=FACT*K;
000041  WRITE(7,*,K,FACT);
000067  GOTO 1;
2:  N:=FACT;
000072  WRITE(7,*,N) ;
000119  END
000227  PROGEN

```

```

PROGRAM BIN,B,P,L
000000
000000
000000
000000
000000
000004
000023
000023
000023
000032
000036
000040
000041
000051
000073
000123
000134
000171
000311

CONST FIND=5;
VAR X,I,J,K; INTEGER;
A; ARRAY [1..15] OF INTEGER;
VALUE A=(5*2,3,4,5,1,8,9,7,11,2*4);
BEGIN
  I:=1; J:=15;
  REPEAT
    K:=(I+J) DIV 2;
    IF A[K] LE X THEN I:=K+1;
    IF A[K] GE X THEN J:=K-1;
  UNTIL I GT J;
  WRITE(I,J,X,A[K]);
END;
PROGEND

```

```

PROGRAM GCD,L,P,B      /* TO FIND GCD(X,Y) */
000000
000000
000000
000000
000004
000013
000033
000037
000040
000060
000100
000111
000154
000271
000271

VAR X,Y,A,B; INTEGER ;
BEGIN
  READ(4,9)X,Y;
  A:=X; B:=Y; /* A>0,B>0 */
  REPEAT A GT B DO A:=A-B;
  WHILE B GT A DO B:=B-A;
  UNTIL A EQ B; /* A=B=GCD(X,Y) */
  WRITE(6,7)X,Y,A,B;
END
PROGEND

```



PROGRAM DIV,B,L,P  
/\* PROGRAM TO DIVIDE NATURAL NUMBER X BY Y \*/

```

000000 VAR X,Y,R,I,H ; INTEGER ;
000000 BEGIN
000005   READ (4018,*,X,Y) ;
000010   R:=X ; I:=0 ; H:=2*H ;
000015   WHILE H LE N*Y GT X /*
000020   / * H=2*N*Y DO
000025   WHILE H NE Y DO
000030   BEGIN /* Q*H+R=X ; R GE 0 /*
000035   Q:=2*Q ; H:=H DIV 2 ;
000040   IF H LE R THEN
000045   BEGIN
000050   R:=R-H ; Q:=Q+1 ;
000055   END ;
000060   END /* Y+R=X ; 0 SR<=H ; Q=X DIV H /*
000065   WRITE(6,*,X,Y,Q,R,H) ;
000070   END ;
000075   PROGEND

```





```
PROGRAM TST2,B,P,L
000000
000000
000000
000000
000000
000000
000000
000000
000000
000003
000012
000032
000036
000041
000075
000214
CONST MIN=4,MAX=10 ;
SYN A = B,2 ;
    O = E,E ;
VAR A,D,L ; INTEGER ;
BEGIN
A := MIN ; WRITE(1,*,MIN) ;
B := MAX ; E := MIN ;
L := B+E ;
WRITE(1,*,L,B,E) ;
END ;
PROGEND
```

```

PROGRAM TST6,B,P,L
000000
000000 CONST SH=5,SHT=10;
000000 VAR A,B,Q,D; INTEGER ;
000000 BEGIN
000000 A:=4; B:=2; D:=4;
000000 Q:=A*BLS 2 + B*ALF 7 + A*RAE 11;
000000 D := C*BLF 10 - A*BLS 9 - C*RBL 10 + D*RBR 0;
000000
000000 END;
000000 PROGENO
000000

```

```
PROGRAM TST4,B,P,L  
000000  
000004  
000013  
000021  
000025  
000030  
000041  
000066  
000210  
VAR A,B,C,D : INTEGER ;  
BEGIN  
  A:=2; B:=3; C:=4;  
  REPEAT  
    A := A+1;  
  UNTIL A = B+C;  
  WRITE(1,*,A,0);  
END;  
PROGEND
```

```

PROGRAM TSTS,B,P,L
000000
000004
000013
000021
000032
000045
000052
000077
000220

VAR A,B,C,D ; INTEGER ;
BEGIN
  A:=1; B:=3; C:=5;
  WHILE A NE 10 DO
    BEGIN
      D:=C*B; O:=D*5-B ;
      A := A+1
    END
  WRITE(1,*,A,D) ;
END ;
PROGEND

```

```
PROGRAM TST6,P,L,B VAR A,B,C,D : INTEGER ;
000000
000004 BEGIN
000008 READ(1,*,A,B) ;
000012 C := A+B+A+B ;
000016 READ(4,018,*,G,D) ;
000020 A := B+C+D ;
000024 WRITE(6,*,A,B,C,D) ;
000028 END ;
000032 PROGEN
000036
```



```

PROGRAM TST6.L.P,B
000000
000000
000015
000025
000035
000060
000100
000130
000160
000310

      VARRAY (1,10) OF INTEGER ;
      A,B,C,D : INTEGER ;
      BEGIN
        A(1) := 10; A(5) := 5;
        B(2) := 15; D(3) := 3;
        D := B+A(5) - C(1);
        WRITE(1,0,A(2));
      END ;
PROGEND

```

```

PROGRAM IST9,B,P,L
VAR
A,B      1 INTEGER 1
C        ARRAY [1..10] OF INTEGER 1
D        CHAR 1
VALUE    (2,3,4,5,1,5,6) 1
D = AE 1
BEGIN
A := C[3] + C[4] 1
B := C[5] * 5 1
WRITE(6,+,A,B,0) 1
END 1
PROGEND

```

```

000000
000000
000002
000004
000008
000015
000015
000015
000015
000029
000031
000031
000062
000025
000025

```

```

PROGRAM TEST11,B,P,L
000000 VAR A ; ARRAY (1..5,1..10) OF INTEGER ;
000001 C ; ARRAY (1..5) OF CHAR ;
000002 I ; INTEGER ;
000003 VALUE A = (10*1,10*2,10*3,10*4,10*5) ;
000004 C = (E,A,E,B,E,C,E,D,E,E,E) ;
000005 BEGIN I = 0 ;
000006 REPEAT
000007 I := I + 1 ;
000008 WRITE (1,*,C(I,I)) ;
000009 WRITE (8,*,A(I,I)) ;
000010 UNTIL I EQ 5 ;
000011 END ;
000012 PROGEND
000013
000014
000015
000016
000017
000018
000019
000020
000021
000022
000023
000024
000025
000026

```

