A C.A.I. PROGRAM TO AID COMPUTER SCIENCE STUDENTS

A COMPUTER ASSISTED INSTRUCTIONAL PROGRAM TO AID SECONDARY SCHOOL STUDENTS IN UNDERSTANDING COMPUTERS
by

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## ABSTRACT

The project describes the implementation of a program to simulate a simple computer. The program is implemented on a micro-computer for portability.

A Secondary School student, in the Ontario Educational System, at about the grade 10 or 11 level may write programs for the simulated computer. The student may also simulate, interactively, some of the processes involved in executing his program.

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Dedicated to all of my students who have ever asked: "How does a comupter work?"

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## CHAPTER I

## INTRODUCTION

### 1.1 Purpose and Objectives of Project

As a secondary school teacher, I am often asked by my bright and eager students: "How does a computer work?" They naively assume that the reply will be a simple, one sentence, answer which will explain all. Because of this repeated question over the years, and because it is a very good question - central to the study of computer science I have attempted, as a basic goal of this project, to answer it.

The thrust of the project, therefore, is aimed at a fourteen year old student who is just beginning to study computer science. Because of this, my objective was to keep the presentation simple, perhaps overly simplistic in several instances, yet sufficiently complex to allow the student to explore more complicated aspects of computers and computing.

It is of interest to note how the Ministry of
Education for the Province of Ontario put this objective in a curriculum for computer science:
"With an awareness of how a computer is organized.... the student is ready to begin programming. It is suggested that he be introduced to both high-level and low-level
languages. He does not need to master either type of language but it is important for him to understand the basic concepts of each. Low-level or machine languages have the distinct advantage of being closely associated with the way the machine actually does computations. Hence, the mysteries that so often surround high-level languages can be dispelled by an understanding of a low-level language. The instruction set need not be extensive but should include branching and testing instructions so that the power of looping can be demonstrated." (ONE 1, 1970).

Another objective which I feel is necessary for any computer assisted learming program is that it be interactive. This permits the student to become involved, especially when program segments can be corrected if errors are made.

More specifically, my objective was to illustrate the following computer concepts to the beginning student:

1) The various parts of a computer including:
a) Input and output devices
b) Central processing unit
c) Memory
2) Instruction Sets
3) Instruction Formats
4) Data
5) Addressing
6) Instruction Decoding
7) Sequencing
8) Loops
9) A Loader
10) Cycling
11) Assemblers
12) Compilers
13) Absolute and relative addressing

It is also of interest to note the lack of computer assisted learning material for the subject of computer science. Teachers in other areas of study have taken advantage of the computer and produced many good instructional programs for their disciplines. As a typical example, in the 1980 catalog of Plato Courses (a collection of user written programs available from Control Data (CON, 1980)) material can be found on topics from astronomy, to basic Chinese, to Valence Electrons.

On looking closely at the material listed under computer science, most deal with computer programming in Fortran, APL, Compass, and PL/1. Only a few deal with computer structures themselves, for example: Disk Drive Fundamentals, Input/Output Supervision, and an Introduction to Computers.

Another objective of this project was to produce a program which was portable. The assumption is that with
minimal modification to the program, it will be runnable on several other computers.

Finally, I intend to use this project write up as the basis for a teacher manual which would be useful for others developing computer science courses. Thus, much of what I have written is intended for the secondary school teacher who is just beginning to teach computer science or for the teacher who has had very little exposure to computers. 1.2 Bell Laboratories "Cardiac"

In 1968, Bell Laboratories produced a device that they called Cardiac - a cardboard illustrative aid to computation. It involved pieces of cardboard which were punched out and fitted together to form a display. The operator slid the moving parts up and dow to illustrate the operations of a computer. Bell Laboratories provided these aids free of charge to schools, including instructional manuals and also a Cardiac kit made of ascetate for overhead projector use.

This project is based on the Cardiac idea and attempts to implement the concepts of Cardiac as closely as possible.






### 1.3 Classroom Experience with Cardiac

I have personally used Cardiac in the classroom with a reasonable degree of success. The major difficulty with Cardiac is that it becomes very tedious to execute each instruction when a loop is encountered. My implementation avoids this by allowing automatic cycling at the user's discretion.

## CHAPTER II

## HARDWARE, SOFTWARE SETECTION

### 2.1 Hardware Selection

Because the simulator program was to be used in a classroom environment, I looked at computers which were being used in the classroom. As well, I had to consider what equipment was available and to which I might have reasonable access.

Although there are many kinds of equipment on the market today $I$ considered only the following six systems:

1) The Ohio Scientific Challenger Series
2) Data General Nova Series
3) Radio Shack TRS 80 Series
4) The Commodore Pet
5) The Apple Computer
6) Hewlett-Packard 2647A Intelligent Graphics Terminal

The following discussion by no means is a full and complete comparison of the systems mentioned. I am attempting to explain why under my circumstances I chose the machine I did.
2.1.1 The Challenger Series

A number of the Challenger Series II and III's micro-computers were available to me at McMaster University. As well, disc
storage was available. Using the 65028 K Basic appeared to put a restriction on the use of the input command. I wanted to be able to input values as well as display them anywhere on the screen. On using an input statement the cursor and prompt symbol always moved to the bottom line of the screen. The poke command did however, allow for the opposite process of displaying data on the screen. It was because of this inability to control the positioning of the input prompt on the screen, that I did not use the Challenger. 2.1.2 Data General Nova 2/10

This system is a mini-computer and was available to me at my high school in Port Elgin. I did in fact implement part of the program on this system including the graphics and processor modules. Display speed (10 c.p.s.) and access to the machine was less than satisfactory, so that work on this system was not completed.
2.1.3 Radio Shack TRS 80

The TRS 80 with disc was another possible micro-computer that could have been used. They are quite popular and a number of
these systems are appearing in schools. This system, however, was not available to me, so that it too was not used.
2.1 .4 The Commodore Pet

The Pet, as well, is another micro that could have been used. Using the 40 characters per line would have necessitated considerable change in the graphics display portion of the program. Again, very popular in the classroom but not readily available to me.

### 2.1.5 The Apple Computer

The Apple is another very popular micro and with Applesoft Basic and auxiliary disc storage it would have been my choice of system to use. The Applesoft Basic is very extensive and with the colour graphics capabilities would have made the simulator display very appealing. This system again was not available to me, but as an aside, the Bruce County Board of Education, for whom $I$ work, have just bought five Apples and I intend to implement my package on the Apple in the next year.
2.1.6 Hewlett-Packard 2647A Graphics Terminal

This was the micro-computer I finally decided to implement my package on. Besides
having two such terminals readily available, it offered a great number of features for the programmer which really places it in the "Cadillac" category of micros.

The following features indicate the capabilities of this machine:

- The Basic language available on the HP Terminal is very extensive using high level graphics commands, integer, floating point and string arithmetic.
- A graphics language (AGL) is an extension to Basic which offers many easy to use graphics commands.
- Hardcopy was readily available using the modem attached and routing to the Cyber at McMaster.
- Independent graphics and alphanumeric memories offer great flexibility. The graphics memory, consisting of 32 K bytes of RAM, stores a 360 by 720 dot pattern for the graphics image. The alphanumeric memory offers up to 15 K bytes of RAM workspace.
- Enhancement options allow characters to be displayed in half-bright, underline,
inverse video and blinking.
- Full editing capabilities including roll up or down insert, delete, next page, previous page and user defined soft keys are great aids to programming.
- Dual tape drives using the mini cartridges each with capacity for 110,000 characters of storage provided excellent program storage facilities.
2.2 Language Choice
2.2.1 Criteria

Because one of the objectives of the project was to have a program which could be run on several other commercially available micro-computers, the Basic language seemed to be an obvious choice. Some form of Basic is available on almost all micros. On the negative side, Basic is not a structured language and therefore lends itself to unstructured computer code. Also the restriction to two character variable names leaves much to be desired in terms of writing code which is readable and readily understandable.

Perhaps a much more desirable language would have been Pascal, a structured language
which lends itself nicely to structured programming. It also allows multicharacter variable names which improves program readability. Several micros have Pascal available on them, and more will soon have it in the near future. Unfortunately, no such micro was available to me, thus Basic was selected as the implementation language.
2.2.2. Hewlett-Packard Terminal Basic

The Basic interpreter can be loaded from cartridge tape, and offers a wide range of Basic commands and statements. It also contains several special terminal oriented functions. These functions allow the user to control the terminal cursor and input data from the display screen.

Also graphical operations can be controlled using a special set of graphics language statements within the basic program. This special extension to Basic is called AGI - A Graphics Language, and consists of a powerful set of graphics functions that allow the user to perform graphics operations with a minimum of programming.

At this point I would like to comment
on several of the Basic and AGI commands and statements which I found very useful in implementing my program. Basic Commands and Statements:

Several statements may be assigned to a single line number by separating statements on a line with the "\" (Backslash) character. Commands:

## Auto (Starting line, increment)

This is a fairly common command which
allows the interpreter to generate line numbers as the program is entered.

## Extend

Although I did not use this particular command, it could be very useful to some programmers. The extend command allows a user to add commands to the Basic interpreter. Merge (Filename)

The Merge command loads a copy of the specified file into the Basic workspace. The new program is merged with any existing program. Conflicting line numbers are replaced by the new lines. This command is very useful where part of the program is on one cassette tape and part on another.

## Remove STD/STDX

This command allows the user to remove certain commands from the Basic interpreter. Remove STDX provides an additional 5K Bytes of user workspace.

Set (Condition)
Allows the user to select the default mode of operation for the interpreter. Among other things, one may "set" the default data type for numeric variables. Statements: Call (Sub-Program Name), (Parameters)

The Call Statement allows transfer of control to a sub-program. Parameters are matched in the call and sub-statements according to position. Corresponding variables must agree in type. This statement is part of the STDX package which I removed in favour of more workspace, thus it was not available to me, however Gosub was. Key Code (X)

Each of the keys on the keyboard has a particular code value. The code number is returned in the variable. This is a very useful command as it allows the programmer to select certain keys for user response. This
technique was used in the cycle control portion of the program. The command is not found in most Basics.

## Restore (Iine Number)

The Restore Statement resets the data pointer to the specified data statement. This is a useful statement as it allows the programmer to control data input lists arbitrarily. The line number option is usually not found in Basic languages.

Some Built-in Functions:
Moves ( $R, C$ )
Moves the cursor to row R, column C. $R$ and $C$ are screen-relative co-ordinates ranging from (1, 1) to (24, 80).

## AGL Commands:

AGI offers several types of graphics regions:

1) The logical address space which is the range of data values which may be referenced by the terminal. It may be larger than the physical limits.
2) Physical limits - define the physical display area of the terminal.
3) Graph limits - define the desired display area. This area is within
the terminal's physical limits. Also, AGL offers the user three unit systems: 1) User Defined Units (UDU's 2) Graphic Display Units (GDU's) 3) Metric Units

In my application I used the Default GDU's which had a range of $(0,0)$ to $(200,100)$.

Locate and Frame Commands
In combination, the two commands provide an efficient way to draw a rectangle in any position on the screen. The locate command specifies the display space available for plotting data. E.G.

Limit ( $0,200,0,100$ )
The frame function draws a box around the current region.

Several useful labeling commands are: Lorg (Mode): allows the user to select the labelorigin position: i.e. centered or right or left justified.

LDIR (Angle): sets the letter direction of labels. CSIZE (Height, Ratio, SIant): specifies the size and aspect ratio of characters in a label. Move ( $x, y$ ): actually moves the graphics pen to the desired co-ordinate position on the screen. Print \#0; Text: is the actual labeling command used in graphics mode.

The foregoing, indicates just a few of the graphics commands available on the HP 2647 A intelligent graphics terminal.

## CHAPTER III

DESCRIPTION OF SIMULATOR
The simulator program displays for the user a block diagram of a computer similar to that shown in figures (2) and (3) on pages 6a and 6b. The user may then optionally interact with the simulator program to process a program written in a pseudo-machine language. (See page 7).

The following section attempts to define the various parts of this simulated computer. The chapter following gives the user a detailed description of how the simulator operates.

### 3.1 Instruction Format

The instruction format is three decimal digits.
The left most digit represents the operation code. The right two digits are interpreted as the operand. No intervening blanks should occur between digits. 3.2 Data

Data values must be integer and in the range - 999 to 999 inclusive.
3.3 Instruction Set

The instruction set consists of only 10 instructions. This is consistent with the philosophy of keeping it simple for the beginning student. The instructions are as follows:

Input (INP): OP Code 0
The operand of the input instruction designates the memory cell location to which the input value is to be assigned. E.G. the instruction

052
indicates that the input value is to be assigned to memory location 52 .

Clear and Add (CLA): OP Code 1
This instruction has the effect of clearing the accumulator (set it to zero) and adding to it the contents of the memory location indicated by the operand. E.G. the instruction

152
as an instruction means the accumulator is to be set to zero and the contents of memory location 52 are to be added to it. Add (ADD): OP Code 2

This instruction adds the contents of the memory location indicated by the operand to the accumulator. E.G. the instruction 252

Means: Add the contents of memory location 52 to the contents of the accumulator. Test Accumulator Contents (TAC): OP Code- 3

This instruction represents a conditional
transfer. If the contents of the accumulator are negative then a branch to the instruction held by the memory cell location indicated by the operand of the TAC instruction. Otherwise control passes to the instruction in the next memory location. E.G.

Location
15
16

Contents 352

If the accumulator is negative when the instruction in location 15 is executed, control passes to memory location 52. Otherwise control passes to location 16. Shift (SHF): OP Code 4

The Shift instruction is the only instruction whose operand does not refer to an address in memory. Essentially it shifts the number in the accumulator to the left "x" number of places and then to the right " $y$ " number of places. The values of "x" and " $y$ " are specified by the second and third digits of the Shift instruction.

The following must be kept in mind when using the Shift instruction:

From the point of view of the user, the accumulator retains only 4 digits.

Digits which overflow the accumulator are irretrievable. For example, if the accumulator
contents were 456, and the instruction register contained 433, then the contents of the accumulator would be shifted left 3 places and then right 3 places:
60

| 6 | 0 | 0 |  |
| :---: | :---: | :---: | :---: |
| shift | 3 right |  |  |
| 0 | 0 | 0 | 6 |.

Also, when a digit is moved out it is replaced by zero. For example, a four place shift left would clear the accumulator to zero. Output (OUT): OP Code 5

This instruction transfers the contents of the memory location indicated by the operand of the instruction to the Output device. E.G.

523
As an instruction transfers the contents of memory location 23 to the Output list.

Store (STO): OP Code 6
This instruction places the contents of the accumulator into the memory location indicated by the operand. E.G.

623
As an instruction places the contents of the
accumulator into memory location 23. In fact, if the accumulator contains a number with more than 3 digits, only the 3 least significant digits are stored.

## Subtract (SUB): OP Code 7

This instruction subtracts the contents of the memory location indicated by the operand from the accumulator. E.G.

723
As an instruction subtracts the contents of memory Iocation 23 from the accumulator. Unconditional Jump (JMP): OP Code 8

This instruction causes control to transfer to the instruction found in the memory location indicated by the operand. E.G. 823

As an instruction causes control to pass to the instruction at memory location 23. Halt and Reset (HRS): OP Code 9

This instruction terminates the program and resets the program counter to the memory location indicated by the operand. E.G.

901
As an instruction halts program execution and resets the program counter to 1 .

### 3.4 Input

Because the traditional input device was the punched card reader, input values are typed into a rectangular card in the top left corner of the screen. Each value is then listed below on the input list. Thus a user has a complete visual record of all input.

## Output

Output is handled in a similar way. All values output are listed on the output list. Again, users have a complete visual record of all output.
3.5 Memory

Memory consists of 60 locations which are displayed with their contents and corresponding addresses. When a particular memory location is being used, its contents are displayed in full bright mode. This makes the program easier to find and read, and also makes the display more attractive. Where a memory location is unused, a random collection of letters and symbols is displayed in half-bright mode. The random collection of symbols is used to convey the idea that unassigned memory locations contain essentially garbage.
3.6 Instruction Register

The Instruction Register displays the instruction to be executed. This register may be controlled automatically or manually depending on the choice of the user. If cycling is in automatic mode, the instruction register automatically
displays the correct instruction to be executed. If manual mode is used, the user must type into the instruction register rectangle the correct instruction. Retyping is allowed if an error is made. A message is displayed if an error is made.

### 3.7 Program Counter (PC)

The Program Counter displays the address of the next instruction to be executed. Like the instruction register, the program counter may run in automatic mode or manual mode. Also, on an error retyping is allowed - prompted by an appropriate error message.

### 3.8 Instruction Decoder

The Instruction Decoder displays, in inverse video, a statement in English describing the meaning of the instruction which has just been fetched to the instruction register. This is done automatically with no user input required.

### 3.9 Accumulator

The Accumulator represents the computer's arithmetic unit. Here, numbers are added, subtracted, shifted, and tested. All accumulator operations are automatic, requiring no user input. The accumulator variable $A$, is declared as long (Double Precision Real) in order that shift operations may be performed on its contents. The user is aware of only 4 digits being retained.

### 3.10 Sequencing

Sequencing begins with an instruction being fetched from memory into the instruction register. The flow chart path indicated by the arrows on the display is followed. This is the way in which sequencing is controlled and displayed to the user.
3.11 Control Unit

The three steps required in the fetch-execute cycle are followed in the monitoring program's execution. Those steps are:

1) The instruction indicated by the program counter is fetched from memory and placed into the instruction register.
2) The number in the program counter is incremented to give the address of the next instruction to be fetched.
3) Execution of the previously fetched instruction occurs.

Cycling control, as indicated before, has two modes of operation. The user may allow the program to cycle automatically by holding the space bar down, or permit only one fetch-execute cycle per time by hitting any other key.

CHAPTER IV
PROGRAM OPERATION

### 4.1 Overview

The program is designed for a single user, and attempts to illustrate the operation of a computer to a novice computer science student. The student is first shown a series of program options which are graded with respect to degree of difficulty. The user selects one of these options which runs a program. By observing the program being processed on the screen and optionally interacting with the processing, the student learns a number of things about the processing of a program by a computer.

### 4.2 Menu Selection

4.2.1 Option \#1

This option randomly selects one of three pre-written programs, automatically places it in memory and then allows the user to process the instructions individually or several at a time.

Option \#1 offers very elementary programs to the user to start with. None of the randomly selected programs are identified to the user, thus forcing him to read each
instruction to discover what the program is doing. Two of the programs in option \#1 are similar in that they require user input of two numbers, after which the sum or difference of the two numbers is calculated, stored, and then output.

The third program in option \#1
requires the user to input a number. By using the shift and addition instructions, the product of the input numbers and the number 11 is output. The reason for this last example is pedagogical in nature. It attempts to suggest to the student ways of implementing a multiplication instruction where one does not exist. It also introduces the student to the shift instruction which is an exceptional type of instruction in that its operand does not refer to a memory address.
4.2.2 Option \#2

The second choice on the menu, is also
a pre-written program automatically placed into memory. The program requires no input and produces as output the numbers from 1 to 10. This program is intended to illustrate the use of the unconditional branch and the
conditional branch instructions. It should also suggest to the student other "counting programs" such as:

- counting by 2's, 3's, 5's etc.
- counting within a specific range. For example numbers between 50 and 60. - counting a finite Fibonacci Sequence. 4.2.3 Option \#3

This option is intended to be the one which most students will use once they become familiar with the instruction set. In this option a loader is displayed in the first ten memory locations and is then executed. It allows the user to place his own program into memory, starting at location 11 and then have it executed.

End of program load is signalled by entering any negative number after which execution of the loaded program begins. This end of program condition requires that any loaded program must not contain negative data. I consider this a minor restriction and in fact should offer a good challenge to the brighter student to program around.

After a program has been processed the
user is returned to the same menu selection which also includes a stop option.

### 4.3 Error Diagnostics

A number of diagnostic messages are provided to assist the user in de-bugging his program and in understanding program execution. These messages appear immediately below the memory window.

A number of messages occur when incorrect or invalid input is entered. These messages are just warnings and do not cause program termination. The user is asked to re-enter the data. Data input is required for:

1) Program choice in menu
2) Input values
3) PC Incrementing Update
4) Instruction Register Update

Other messages which are given are error messages which result in program termination. Error messages will be displayed under any of the following conditions:

1) Out of memory condition i.e. assignment to a memory location beyond 60 .
2) Attempt to execute an unassigned memory instruction.
3) Undefined OP-Code
4) Storing into memory locations reserved for loader.
5) Accumulator Overflow.
6) Attempt to jump into memory locations reserved for loader.
7) Instruction stored in memory location reserved for loader.
4.4 Sample Programs

The purpose of this section is to present several programs which students should reasonably be expected to write and to indicate areas which a teacher could explore using the instruction set of the simulator program.

### 4.4.1 Multiplication

One of the first criticisms students will make of the instruction set is that there are no multiplication or division OP-Codes. The teacher could then explore ways of programming these operations and thus introduce such concepts as subroutines and firmware to the student.

The following example is a program which requires a single digit multiplier. The mulitplicand may be one or two digits.

BC 2 digit multiplicand
xA $\quad 1$ digit multiplier Product

Program:

Address

13
14
15
16
17
18
19
20
21
22
23
24

Contents
045
404
646
047
147
710
647
323
146
245
646
815

Comments
Input multiplicand
Clear Acc.
Store 0 (Future Sum)
Input Multiplier

Subtract 1 (From Loader Program)

Test Acc.
Add Previous Sum
Add Multiplicand
Store Revised Sum

Output Product
Halt Reset

### 4.4.2 Reversing Digits

This program takes any three digit number "XYZ", and reverses the digits to output "ZYX". This type of problem is very useful in introducing the shift operation code. Typically, students can develop a two-digit shift program first, and then easily progress to writing this program.

Program:

least significant digits in one memory location and the three most significant digits in another.

Program:

Address

24

| Contents | Comments |
| :--- | :--- |
| 030 | Input Most Significant Digits |
| 031 | Input Least Significant Digits |
| 032 | Input Most Significant Digits |
| 033 | Input Least Significant Digits |
| 131 | Add Least Significant Digits |
| 233 | Add Least Significant Digits |
| 633 | Store Least Significant Digits |
| 403 | Shift Overflow Right |
| 230 | Add Most Significant Digits |
| 232 | Add Most Significant Digits |
| 632 | Store Most Significant Digits |
| 532 | Output Most Significant Digits |
| 533 | Output Least Significant Digits |
| 901 | Halt |

4.4.4 Subroutines

The concept of subroutine is a
profoundly important technique in programming. The calling sequences can be illustrated in an elementary form on the simulator.

The following program example uses the previous double precision program as the
subroutine. The subroutine is called twice in the main program sequence.

## Program:

## Location

11

12
13
14
15
16
17
18
19
20
21
22
23
24
25
26
27
28
29
30

Contents
118
633
820
119
633
820
901
814
817
034
035
036
037
135
237
637
403
234

## 236

636

## Comments

Prepare Return Address
Call Subroutine
\} Prepare Return Address
Call Subroutine
Halt
Return Address after First Call Return Address after Second Call

T

Double Precision
Subroutine for Adding
Two 6 Digit Numbers.

Location
31
32
33

Contents
536
4.5 Placement in the Ontario Ministry of Education Curricula
As indicated by the Ministry of Education Circular
H.S.1., 1979 - 81, there are several guidelines covering computer science. They are:

1) Curriculum RP-33 Data Processing, 1966.
2) Elements of Computer Technology, 1970 Senior Division.
3) Computer Science, 1970 Senior Division.
4) Informatics, Intermediate and Senior Division 1972.
All of the above curricula refer to the need to teach students a low level language, computer organization and operation. It is best summarized in the Computer Science Curriculum, 1970.
".... The Fortran statement $X=A+B / C$ sheds no light whatsoever on precisely how the computer executes this instruction. Hence, it is both interesting and enlightening to study lower-level languages because such languages will provide a general understanding of how a computer performs basic operations. Another reason for studying lower-level languages is that the most efficient programs are generally written by those who know about the machine language counterpart. These factors contribute to the student's general
understanding of the computer and to his ability to use it effectively." (ONE 2,1970).

The curriculum goes on further to comment on the placement of low-level languages in a computer course. The comment therefore is very appropriate for the placement of this program in a computer course. It is as follows: "There are arguments is favour of studying highlevel languages before low-level, and conversely there are arguments in favour of the reverse sequence. For instance, some teachers find that students prefer to start using the machine quickly by introducing very simple high-level statements. Later in the course, they use the low-level language to learn what actually happens. Other teachers find that students prefer to start solving problems by using a low-level language. Later the ease of use and the relative power of the high-level language become apparent when the student starts to use it. The important point to note is that either sequence can be effectively used." (ONE 2,1970).

I personally use the former approach with the student, allowing them some immediate "hands on" experience before getting into the underlying concepts.

This C.A.I. Program could be introduced after several weeks work with a high-level language. The program itself relates to topics which cover about two weeks work.

The topics which this program illustrates are listed quite completely in the previously quoted Government Curriculum.
"A study ...... will help to illustrate the following concepts:

- The relationship between the main sections of a computer; input, output, storage or memory, arithmetic unit, and control unit.
- Various hardware features such as an accumulator and instruction counter.
- Storage, divided into a finite number of parts, each capable of containing information referenced by an address.
- Stored information as either data or an instruction.
- The idea that instructions are usually executed sequentially, unless interrupted by a branch or a halt.
"It is suggested that only very simple programs be done in the low-level language. The language used should employ decimal numbers and should contain at least the following instructions:
- Input/Output: A method of reading and printing a number.
- Arithmetic: The operators +, -, x, $\div$
- Data Movement
- Transfer or Branch: At least an unconditional
transfer, a transfer on a zero condition and a transfer on either a positive or a negative condition.
- Halt" (OME 2, 1970).

Thus this C.A.I. package has a very well defined place in the Ontario Secondary School Curriculum. The various curricula refer to several grade levels ranging from the intermediate grade 10 to the senior grade 11 and 12 levels. The program would be very appropriate near the beginning of an introductory course or as review material in a second course in computer science.

## CHAPTER V

## RESULTS AND CONCLUSIONS

### 5.1 Project Evaluation

"Testing shows the presence, not the absence, of bugs." - E. W. Dijkstra.
"A debugged program is one for which you have not yet found the conditions that make it fail." - Jerry Ogdin.

And to further quote from Yourdon on the topic of anti-bugging or defensive programming:
"Within your module, you should assume that everything that can possibly go wrong will go wrong. To paraphrase one of Murphy's Laws: Even if nothing could possibly go wrong, something inevitably will. You should take this into account when you write your program." (YOU, 1975).

The program was written with the previous comments in mind. Considerable testing was done during the writing as well as after the program was completed.

In terms of evaluating the implementation of the "Cardiac" concept on a computer, the project was successful. In fact, the project has much enhanced the Cardiac idea because it allows for automatic cycling which removes a great deal of the tedium in processing programs on Cardiac.

The one difficulty encountered in implementing the program was with the input command. To quote the HP Basic Manual: "When data is read in, the input is taken from the line containing the cursor. All characters, including non-displaying control characters, to the right of the "?" prompt are input." (HEW, 1979). This means that any characters on the same line in the alphanumeric memory will cause an error. Characters on the same line but displayed from the graphics memory do not cause an error. This difficulty was resolved by simply selecting for input lines on the display which do not have any alphanumeric data on them.

Evaluating the project for portability makes it less successful. Most of the graphics portion of the program would have to be re-written to allow it to run on another micro.

One other area of success which I feel is important, is the production of a document which would be useful for other classroom teachers. I feel that this has been accomplished and that many new teachers will find this material very useful in planning course material for computer science programs.

### 5.2 Areas for Further Development

There are any number of extensions and changes
which could be made to make the program simulate more closely a real computer.

Additional registers would enhance the simulator considerably. This would in turn require that the instruction set be enlarged resulting in a new instruction format. The capability of doing floating point arithmetic might also be considered. Another possible suggestion might be a memory window, which would display different areas of memory depending on the memory location of the program.

### 5.3 Conclusions

Logically, the next step in a computer science student's education would be to study and program in a proper assembly language. It would not surprise me, to find within ten years, in the Ontario Ministry of Education Curriculum, a course in assembly language programming. I say this in all seriousness as I recall my high school mathematics teacher saying in the late 1950's that there was no way the subject of calculus could be introduced into the secondary school curriculum. He argued that it was too complex, there was no room for it in the present math courses and that there were much more important topics such as logarithms, slide rule skills and solving triangles using logs. At that time he was head of the mathematics department, had been teaching for some time and was highly respected. It is interesting to note that an introductory course in integral and differential calculus has been offered at the grade 13 level in Ontario since 1965.

In fairness to my teacher, the pocket calculator had not yet arrived and skill in using trigonometric, logarithmic and interest tables was required to do much of the mathematics in those "good" old days.

The role of comupter science education at the secondary school level is just beginning to be established and depending on the public's attitude and interest in computers, it will either grow or diminish in importance.

APPENDIX A: Instruction Set

| Number | Mnemonic | Explaination |
| :---: | :---: | :---: |
| 0 | INP | Input to Memory Cell |
| 1 | CLA | Clear and Add to Accumulator contents of |
| 2 | ADD | Add to Accumulator contents of .... |
| 3 | TAC | Test Accumulator, if Negative jump to Instruction .... |
| 4 | SHF | Shift Accumulator Contents Lef't then Right |
| 5 | OUT | Output Contents of |
| 6 | STR | Store Contents of Accumulator into Cell .... |
| 7 | SUB | Subtract Contents of ... from Accumulator |
| 8 | JMP | Jump to Cell ... |
| 9 | HRS | Halt Computer and Reset |

APPENDIX B: Program Listing

```
\LIST1-ET
>FXIT
```



```
\IISII-950
    1 PEM
    1 PFM
    3 PFM
    3 REM
    4 REM
    5 REM
7 REM
20 PLOTR \geqT,OON >GCLR
<2, POINT EHRTR2FG:IH&:CHRS(27):AJA
INTEGER CJ,C4,O,L2,O3,R4
24 CSITE (G,1,0i>MOVF, (2,G,90)
35 LONG, A
2A CSIZE (6,1,0) \MOVE (40,90)
```



```
50 MOVE (10,70, PCSITE (1,1,2)
PO MOVE (1O,SCI\CSIZE (2,1.O)
```




```
ITL PRINT EO:&NUMRFRS SUPPPLED BY THE USER OR FULTIPLICATICN OF A NUMRERA
```



```
140 MOVE (10:45)>CSITE (2.
```



```
190 PRINT GO:TTHE NUMRFRS FRCN 1 TO 10 IN ASCENOING CRCER.I
2CO MOVE (IC,3G)>CSIZE (2.1.CI
210 PRINT IU:OOCHOICFTE:I?
220 MOVE (15, <5)>CSI2FE (1, 1,0)
230 PRINT EO ATHIS ORORRAM OISPLAYS A LOAOER WHICHAALLCWS USERS TC LOADE
240 PRINT EO;'THEIP OWN FQOGRANS AND HAVE THEM PROCESSEO.A
250 MOVE (17,15)\geqCSIZE (6,1,0)
OEO PRINT =O:&INPUT YOUR PPOGRAM CHOICE:I
270 PRINT MOVCS(20,50):
280 INPUT tt,x2,
282 ON Y2 GOTOO 290,290,293
284 PRINT,MOVRCI23,O1:IWQONR, NLMHER, TFY AGAIAVA
2&6 GOTOT27O
290 GCLR
290 GCLR
401 PEN
402 DEM
4
402 PEM 
            ELR
                N
                                    ************F##****F###********F#######*
405 REN
405 PEN CNT CHRT(27):IHt:CHR (27):AJt
```



```
420 REN I= TO 50>PEM
                                    :: MEGor, CELSS
440 FOR I=1=-9
450 M(I, 1i)=-9
M
460 NEXT I GOTO 480.595,670>REM FN FOR PRCGRAM CPTION
480 RESTORE 490
490 DATA D,35,0,37.1,3E,2,37,6,38,5,38,9.01\geqREN** ADCIIION PROGRAM
500 FOR I=17 TC 23
510 READ M(I,CI,M(I,1)
530 R=RNO
540 IF R>=.66 THEN 580?PEM
540 IF R>=.66 THEN 5802PEM 
** ADOITION
** SUAITRACTION
```

```
    FEO IFFR< 33 THEN GOSUR 73GO\REM
```



```
    GOTO 800>R IM
    590 GOTO 800>R IM
```



```
    620 FOR I=17 TO 29>REM
    640 NEXT I
    640 NEXT I 
    67C PESTORE 68C\geqREM
    * GOYO PROCESSOR
    ** LOADER
```



```
    7 2 0 ~ R E A D ~ M ( I , O ) , M ( I , 1 )
    730 NEXT I
    731%GOSUR 700O>REM FO FO MEMORY DISPLAY
    REM
    REM
    734 PEN
    735 PEN
    PEN
    POINT MOVCS(M(I+1,2),Y(I+1,3)-5):CHRD(27):\notADDA*#:CHR$(27):\notADS\not=
    GOTO qn2>QFM
    I=16>RFM
```



```
    82 R1=3 (1)
    8C2 R1=3
    * CCOFDS FOR I/O OISPLAY
    804 P2=5>OFM
    810 ODINT MOVCS(M(I,2),M(I,3)-5):# t>REM ** PURCUT PC
```



```
    $80 REM
```



```
    AB3 REM
    B4 REN
    890 IF I< =50 THEN 930
    900 PRINT MOVCS(22,43);*OUT OF MEMORYF
    920 GOSUA 9200\ ,OTO 9100>REN
```



```
    G60 GOSUB 9200\ GOTO 910\\REM GELL CONTENIS UNASSIGNEOR GOTO RESTART
>EXIT
```

```
>1IST961-18C0
```



```
9E5 REN
97C GEIKBO ON 
PRINT MOVCS (2?,44); EFOR AUIOMAIIC: HII SPACE BARF
PRINT MOVCSI2B,44), &FOR USER INPUI: HIIT SPACE BARY OTHER KEYE
IF GETKGN(X)=0, THEN gT3
PRINT MOVCS(2?,44):&
```




```
** AUTOMÁTIC CYCLING
IF X<> 32 THEN GOSUA GOOO\DEEM
PENT**************************************
    GETKO\OFF
    RCN
* OP-COOE PROCESSOR MOOLLE
```



```
87 REM
8B PEN
```





```
1030 RFM
RFM}\times2=3,THFN 104O
IF M(J,1)>10 THEN GOTO 1040
PRINT MOVCSI22,431:\not=ATTEMPTEO LOAR INTO AREA RESERVEC FOR LCADERF
GOSUB 92002 GOIO 9100
GOSUA 7400
M, (1)
*
1050 PPINT MOVCS(5,15):#OEVICE IO CELLI;M(I,1)
1069 R2=R2+1? IF R?=2? THEN F2 =6
107% PRINT MOVCS(3;1):A
1074 INPUT I1
1074
    IAPUT II IN&=999 THEN r,OIO 1121
```



```
    MRINT MOYC
    GCSU9 9200
    roro 107g
```



```
    D=I1
    GCSUR >6CO
    R4=R?>C4
    GOTO A1O
    REM
                                    * CLA MODULE = 1
1140 GCSUS 7400
1142 PPINT MOVCS(5.15):#COPY CCNTENTS OF CELL 
150 PRINT MOYCS (5,15):M(I,1):t INTO ACCO
1160 A=M(MII,1),0i*100+M(M(I, i), 1)
1165 PRINT MOVCS(11.23):%
170 PRINT MOVCS(11:24):A
11880 GOTO 810 % F% ADO MODULE =
1250 GOSUN 7400
1252 PPINT MOVCS (5.15):& ODD CCNIENTS OF CELL }\not
1260 PRINT MOVCSI6,15):M(I,1):ITO ACC. 
1270 A=A+M(M(I,1),0)*100+M(M(I,I), 1)
1280 IFAABS(A)E=32000 THFN 1320
1290 PRINT MOVIS (22.43):AACCUMLLATOR CONTENIS OVERFLOWEDE
1290 PRINT MOVIS122,431:*AC
1310 GOSUB 9200? GOTO 9100
1320 PRIN MOVCS(11,23);:
1322 PRINT MOVCS(11,24):A
134J GOTO B10
13&O REM
*TAC MODULE=3
```



```
13B8 GOSUB 92OG` GOIO 9100
400 IF A> =0 THFN 1420
1410 GOSUB 7401
1411 PRINT MOVCS(5,15);\MOVE PC TO CELL F:M(I,1)
1415 GCTO 1450
1420
1440 GOTO 810
1450 PRINT MOVCS(M(I,2),M(I,3)-5):t 
1470
** SHIFI MODULE =
```




```
1508 F,OSUR M4COPREM,15);ASHIFT ACC CONTENTS & 
1510 PRINT MOVCS(5,15);&SHIFT ACC CONTENTS 
1521 IF L2=0 HHFN 1527
1522 FGR J=1 TC L2
```



```
1527 IF O=0 THEN 1532
152R FCR J=1 TC O
15
1534 PRINT MOVCSI11,241:A
1534 PRINT MOVCST11,24):AA
1540 IF ABS(A)<=3200C THFN 15.9CLATOR HAS OVERFLOWEDA
1570 GOSUB 92002 GOTO 91G0
1580 PPINT MOVCSI11,24::A
1595 GOTO 910
1600 PFMM
```



```
160G PRINT MOVCS(6,15);M(I,1); &INTO OUTPUI OEVICEA
1606 PRINM,MOVCS(G%1F2,MTHENRRI=4NIO
1630 II=M(M(I, 1), D)*100+M(M(I,1),1)
1640 PRINT MOVCS(P1,39);I1
1650 GOTO A10
1650 GOTI`10 THEN *2=02REM * FTOFE MODULE
1662 IF M(I,1)>10 THEN 1570
1662 IF M(I,I)`10 THEN 1570}1654 PRINT MOVCS(22,43):fLOAD INTO MEMCRY AREA RESERVEO FOR LOADERA,
1654 PRINT MOVCS(22,43):FLO
1668 GCSUR 9200> r,OTO 9100
1E71 PRINT MOVCS (5.15):ACOPY CONTENTS CF ACC
1E71 PRINT MOVCS (5,15):ACOPY CONTENTG CF ACCG & 
1674 IF ABS(A)>999 THEN 1742 F F F ASSIGNNENT FOR SUBPOUTINE
ll
1720 M(M(I.1),0)=A DIV 100?PEM FF OPCODE
1730 M(M(I,I),1)=A-(A DIV 100)*100\REM * OPERANO
1740 GOTO A10 %NCS(22,43):+STORIAG RIGHT 3 DIGIIS OF ACC
1742 PPINT MOVRS(22,43);\not=STORING RIGHT 3 DIGIIS OF ACC.t
1743 D=&A DIV 10001*1000>0=A-0
1744
1745 M{M(I,1),1)=0-(0 OIV 100)*100\geqslantREM FOTORING RIGHT 3 DIGI
18OO REM
** subiraction module
```

```
>LIST1800-7800
1800 REM
1808 GOSUB M4OO
1820 PRINT MOVCSIG:15):M(I,11:#FROM ACCUMULATOR&
1820 PRINTMMOVCSIG,15):M(I,II:&FROM, A
1850 IF ABSIAI<=32000 THEN 1890
1860 PRINT MOVCS(22,43);#ACCUMCLATOR OVEPFLOWED&
1860 PRINT MOVCS GOQ,4SI:&ACCUMCL
1890 PRINT MOUCS(11,23):*
1895 PRINT MOVCS(11,24):A
1910 GOTO 810 THFN \times2=0>REM
1920 IF I 1 10 YHEN X2=0 \REM
1922 IF M\I,1I>10 THEN 1930
1924 PRINT MOVCS(22,43):IMEMORY AREA RESERVED FOR LOADERA
1924 PRINT MOVCS(22,43):IMF
1928 rOSUR 3200
1932 PRINT MOVCS(5,15):IMOYE PC TO CELL I:M(I,1)
1950 PRINT MOVCSIMII,2i,M(I,3)-5):t i
l
1975 PRINT MOVCS(M(I,2I,M(I,3)-5);CHRS(27);FACAFF;CHRS(27):XAD<Z 
1990 REM
2000 GOSUQ 7400
2CO2 PPINT MOVCS(5,15);:STCP CCMPUTERA
2030 GOTO 9100
5990 ROTO 91C0 
llown
```



```
5993 REM
G000 REM
                                    INSIR REGG UPDATE
6020 I1=M(I,0)*100+M(I,1)
E030 PPINI MOVCS(23.27):
6030 PPINT MO
6050 IF II=I2 THEN 5090: FINCORFECT CONTENTS.TRY AGAINVF
5070 GOSUP 9200
5070 GOSUP 920
6080 GOTO 6030
G090 REM FOO REM PC MOVE HIIH USER INPUT
6100 REM 
6105 PPINT MOVCS(22.13):#
6110 PRINT MO
E130 IF IL=I+1 THEN 6180
E130 IFII2=I+1STHEN 6180, PRINT MOVCSI22.43I:AINCORRECT CELL NUMBER, IRY AGAINVA
6140 PRINT MOVCS
6150 GCSUB 920
6180 RETURN
6200 REM INSTR. REG. LPOATE
6220 I2=M(I,0)*100+M(I,1)
E225 PRINT MOVCS(23,25i:%
E230 0=I2\geqR4=23\geqC4=29
6232 GOSUGR7710>RFM
6240 REM
E250 PRINT MOVCS\22,14);I+1
5 2 7 0 ~ R E T U R N
6990
6991 REM
F993 REM
```

```
7000 GOSUN 9000>RFM MEMORY DISPLAY MODULE
7004 CSIVF (1.2.0)
7005 I= = C 3 = 49 T0 75 STFP
#
7C30 M(I,2)=R3
7040
70B0 IF R>=.66 THFN PRINT MOVCS(R3,C3);CHR&(27);{ADHE&VF:CHR$(27); ANDS
```



```
7095 PRINT CHE$\27):&ADC才
7100 IF OR.33 1HEN PRINT MOVCSIR3,C3):CHRS(27):AADH%PSt:CHR$(27):&ADSt
7105 f0TO 7122
7106 I1=10ח%M(I*O)+M(I,1
7109 IF I1>99 THEN 7120
7111 PRINT MOVCS(R3,C3+1):I1
712 PRINT MOVCS(R3,C 3):\notOOL
7114 GOTO 7122
}11% PRINT MOVCS(R3,G3):IG*
7118 GOTO 7122
120 PFINT MOVCS(R3,C3-1):I1
7122 IS =I =1 R R3
7140 NEXT C3
7140 NEXI C
7150 RETURNN 7310>REM
7310 OATA 0,39,1,39,4,10,2,39,E,40,5,40,9,0
7320 FOQ I=17 10'23
7330 READ M(I,O),M(I,1)> NEXI I` RETURN
7400 PRINT MOVCS(5,15):CHR$(27): #AOB
7400 PFINT MOVCS(5,15):CHR&(27):\notA^DB
7410 PRINJ MOVCS(F,15):CHR&(27):A^DB
7420 RETURN
7590 REM
7591 REM **********************************************
7592 DEM FORMATIED PRINT SUEROUTINES FOR MEMORY, F
7592 REM F FORMATIED PRINT SUEROUTINES FOR MEMORY,
7593 DEM
7594 PEM
```



```
7610 IF D>99 IHEN 7690
7620 IF D>9 THEN 7560
7690
7630 PRINT MOVCSYM(M(I,1),2),M(M(I,1),3)+1);0
764G PRINT MOVCS(M(M(I,1):2),M(M(I,1):3i): %00t
764\ PRINT
7650 %
7660 O
7E70
7680
769! PPINT MOVCS(M(M(I,11,2),M(M(T,1),3)-1):0
700 RETURN
7710 IF 0.99 THEN 7790
772C IF D>9 THIN 7760
7725, IF O<O IHEN 779C
7740 PRINT MOVCSIR4:C4I: #OOt
7740 PRINT M
770 REIURN
7760 PRINT MOVCS(R4,C4);口
770 PRINT MOVCS(R4:C4):10t
7780 RETURN
7790 PRINT MOVCSIR4,C4-1);0
7800
RETURN
```


8280 MOVF (45.60)
A290 CSIZE I1:1,90!

```

```

8330 FRAMF
3340 MOVE {40,85)

```

```

8380 LOCATE (95.105.10,90)
R390 FRAMF (95,5)
8410 CSITF (1,2,0)
8420 PPINT =0:FOUTPIJT F FEM MEMORY
8440 LCCATF (14(5,197,13,90)
8450 FDAME
8460 MOVF (125,90)
8470 CSITE IL:'\&`O)
8490 MOVE (110,85)

```

```

8540 PPINT MOVCS(6+I,45):I> NEXT I
8563 PRINT MOVCSII-9,53):I> NTXT T
8570 FDP I =31 YO 45
R590 FOR I=46 10 60 , % , I, NEXT I
B750 MOVE (18.55)
\&76O CSIZE IL:1,0)

```

```

A800 PRINT E0;t1-CLAA
881D PRINT 三0:12-ADNz
8820
88830
8840
8840
8870
8880
8890
\&900
8920
8930
8940 MOVE (85 68)
8950 FOR I=1 IG8)
8960
A970
9980 MEXT T (50 13)
8990 OPINT (50:13)
9000
9010 CSITFE (6,Og)
9020 PRINT =0:fA COMPUTER SIMULATOFE
9090 RETIJRN
9090 RFM
9091 PEM
\#\#********************************************
PEM (%MM
9094 PEM
QEM
11) PPINT MOVCS(22,44);CHFt(27):\not=^\cap!PFOGRAM ENDED SUCCESSFULLYA
9112 GOSUB 9200
9120 GCLR > PRINT CHR*(271:1Ht> PKINT CHR\&\&271:t\t
9130 PRINT MOVCS(7,10);{PDOGRAF CHOICES AREZ\&
9132 PRINT MOVCS(1C,15):+1-SAMPLE t,O-gXPPRCGRAMSA
9134
9138
9140
914?
9144
9146
9149
9150
9191 RE
9192
9193
9194 R
9200
CO5 GETKRO ON
9210 IF GETKAC(X)=0 THEN 9210
9220 IF X<>239 THFN 9210
9225 GETKBNOFF
9230 PRINT MOVCS (22,43):t
9240 PRINT MOVCS (23,43):f
9250 RETURN
>EXII

```
(CON, 1980) Control Data Education Company, 1980 Catalog. Plato Courses.
(HEW, 1979) Hewlett-Packard, Terminal Basic Manual No. 02647-90005, 1979.
(ONE 1, 1970) Ontario Ministry of Education, Curriculum: Elements of Computer Technology, 1970 Senior Division.
(OME 2, 1970) Ontario Ministry of Education, Curriculum: Computer Science, 1970 Senior Division.
(YOU, 1975) Yourdon, E. Techniques of Program Structure and Design.
New Jersey: Prentice Hall Inc., 1975.
BIBIIOGRAPHY

Apple Catalog, Vol. 1, No. 3, 1979
Creative Computing, April 1980, Vol. 6, No. 4.
Creative Computing, July 1980, Vol. 6, No. 7.
Kanaroglou, P.S. "A Simulator and a Linker Loader for the Machine Mix", M.Sc. Project, McMaster University, 1979.

Mano, M. M. Computer System Architecture
New Jersey: Prentice-Hall Inc., 1976.
Struble, G.W. Assembler Language Programming: The I.B.M. System 360 and 370 , Addison-Wesley, Second Edition, 1975.

TRS-80 Microcomputer Catalog, F-191 11/5/9```

